

CharlieVM

Charles Larson

10/19/16

# **1 General Information**

2 byte words

64 KB RAM

16 KB ROM (Harvard Architecture)

Single Core

12 registers

## 2 Registers

| Register | Usage              |
|----------|--------------------|
| R0       | Return Value       |
| R1       | General Purpose    |
| R2       | General Purpose    |
| R3       | General Purpose    |
| R4       | General Purpose    |
| R5       | General Purpose    |
| R6       | General Purpose    |
| R7       | General Purpose    |
| R8       | General Purpose    |
| PC       | Program Counter    |
| SR       | Status Register    |
| BP       | Base Stack Pointer |
| SP       | Top Stack Pointer  |