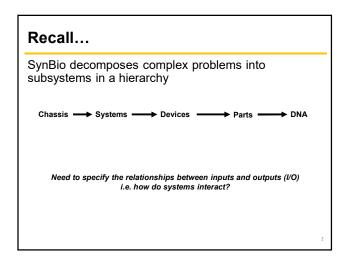
L4 Input/Output Relationships

Instructor: Prof. K. Solomon Ph.D. Assistant Professor Agricultural & Biological Engineering Laboratory for Renewable Resources Engineering

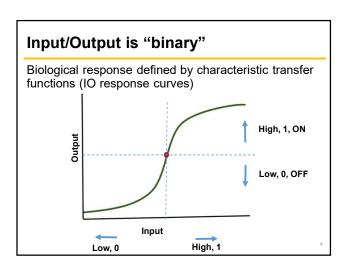
Fall 2018

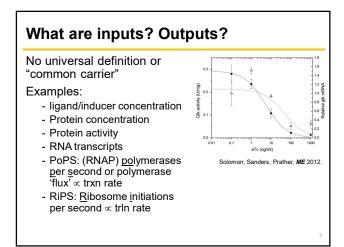


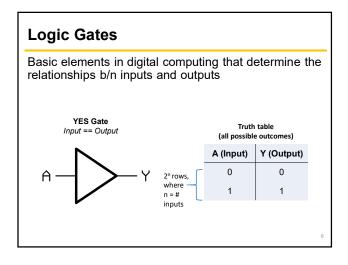


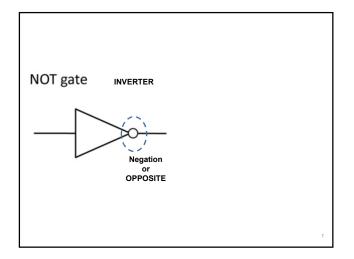
This lecture....

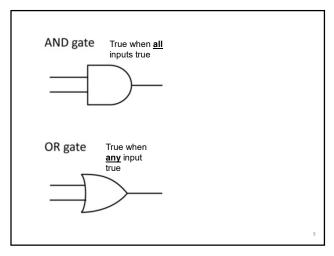
- Define inputs and outputs
- Logic gates and truth tables
- · Gene circuits for logic gates
- More complex functions

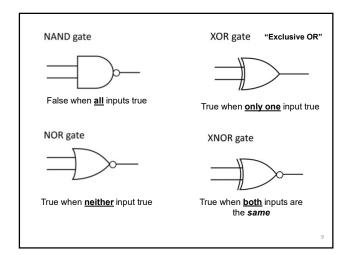


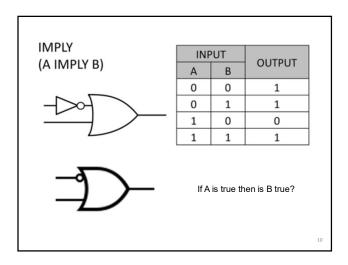


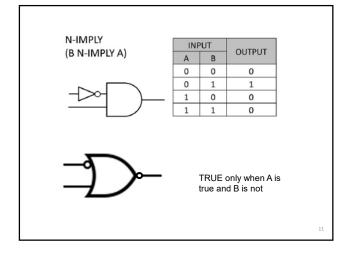


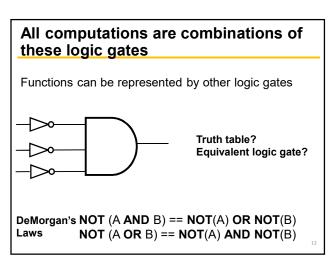


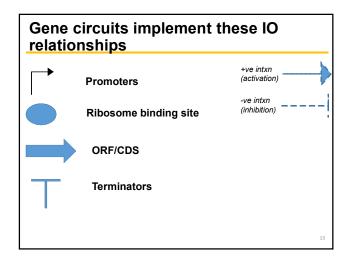


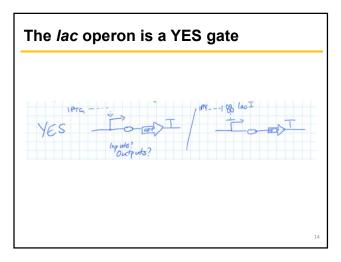


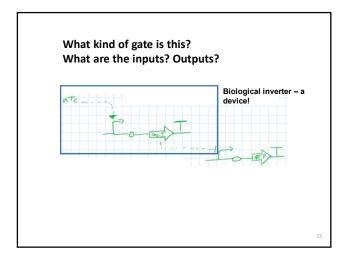


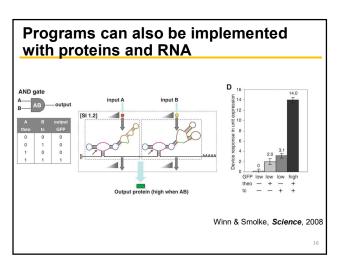


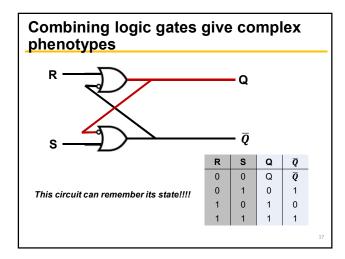


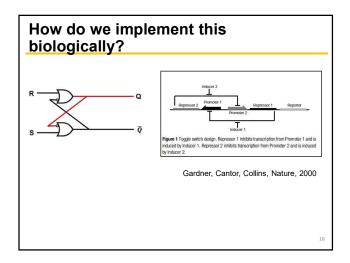


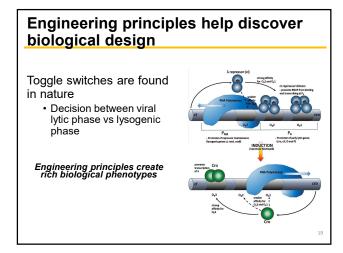












• What properties must we have to design these systems?