

## Table of contents

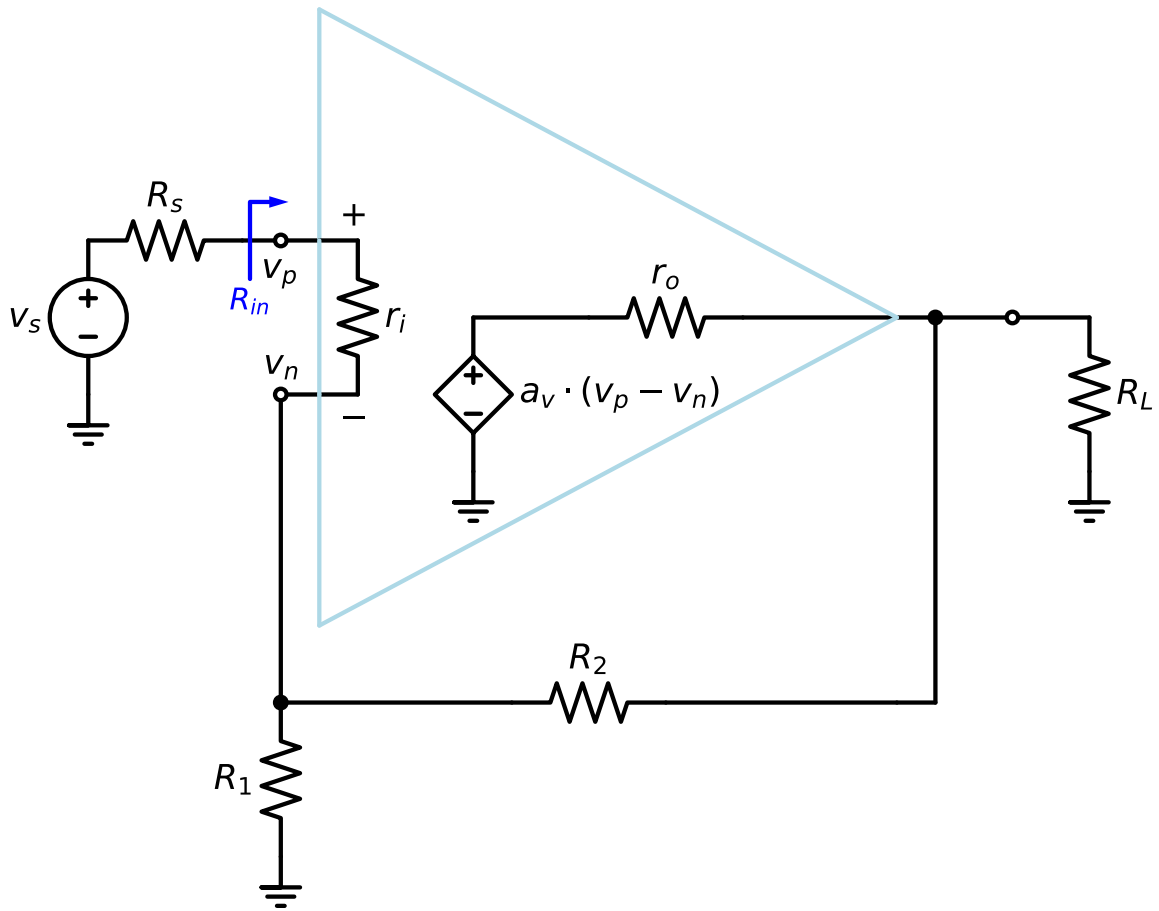


Figure 1: Input resistance of non-inverting op amp circuit