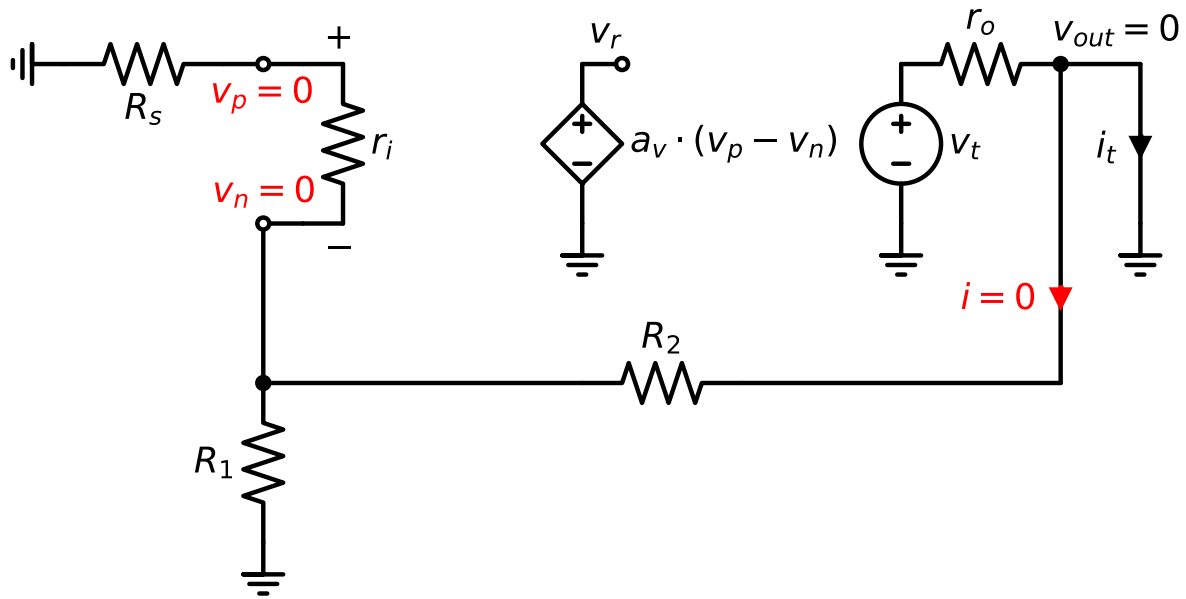# Table of contents

Figure 1: T(with output port shorted) for non-inverting configuration