

Table of contents

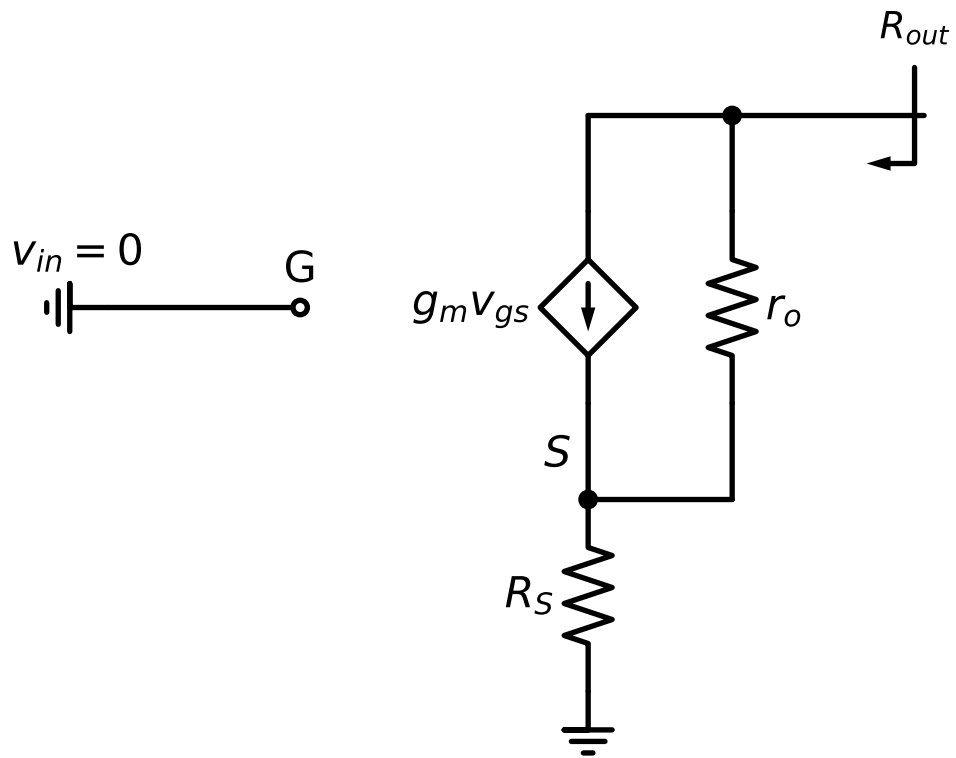


Figure 1: Small signal model to find the output resistance