Table of contents

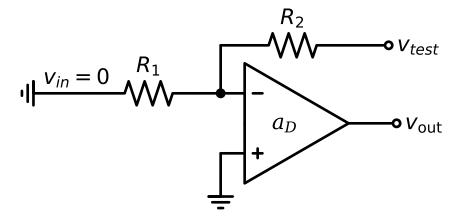


Figure 1: op amp based inverting amplifier with break in the loop $\left(v_{in}=0\right)$