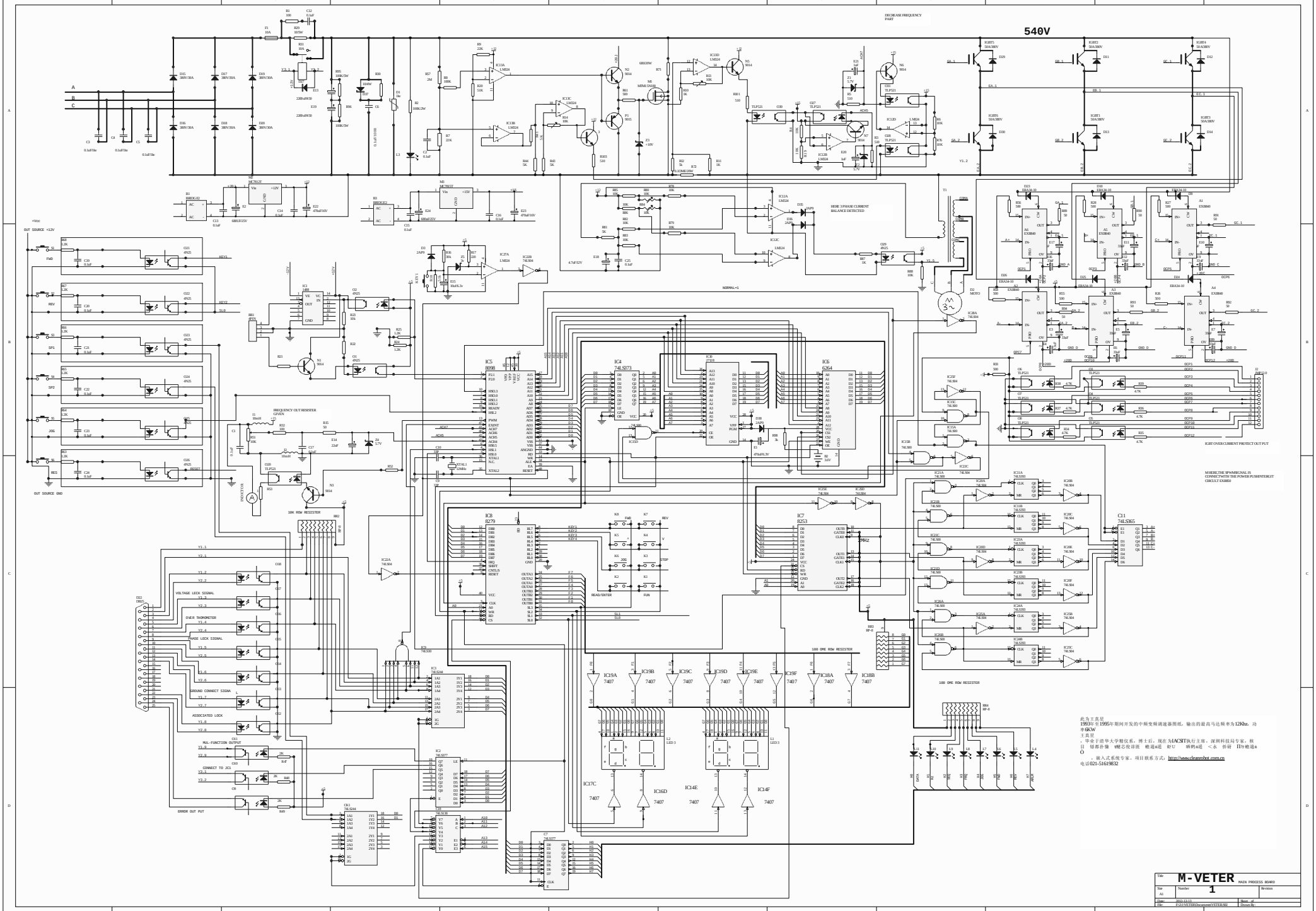
540V
DECREASE FREQUENCY PART
HERE 3PHASE CURRENT BALANCE DETECTED
NORMAL=1
FREQUENCY OUT REMISTER GIVEN
10K ROW RESISTER
100 OME ROW RESISTER
IGBT OVER CURRENT PROTECT OUT PUT
M1SERE,THE SPWMSIGNAL IS CONNECTWITH THE POWER PUSHBNTEJGIT CIRCUIT EXB840
VOLTAGE LECK SIGNAL
OVER THORMETER
PHASE LECK SIGNAL
GROUND CONNECT SIGNA
ASSOCIATED LOCK
MUL-FUNCTION OUTPUT
CONNECT TO JC1
ERROR OUT PUT
OUT SOURCE +12V
OUT SOURCE GND
IC5 89S8
IC4 74LS373
IC6 6264
IC7 8253
IC8 8279
IC11 74LS365
IC3 74LS244
IC2 74LS377
IC1 74LS244
IC9 27S8
IC17C 7407
IC16D 7407
IC14E 7407
IC14F 7407
IC19A 7407
IC19B 7407
IC19C 7407
IC19D 7407
IC19E 7407
IC19F 7407
IC18A 7407
IC18B 7407
此为主轴星
1993年至1995年期间开发的中频变频调速器图纸。输出的最高马达频率为12Khz，功率6KW
王良星
，毕业于清华大学精仪系，博士后。现在为IACSIT执行主席，深圳科技局专家，核
目 怪都乔豫 t嘿芯俊详崽 蟮遥a蟮 舒U 晒鸥a蟮 <永 侨俗 IIr蟮遥a
O
，嵌入式系统专家。项目联系方式：http://www.cleanrobot.com.cn
电话021-51619832
Title M-VETER MAIN PROCESS BOARD
Size A1
Number 1
Revision
Date: 2015-11-11
File: F:\2015\TIEDiscussion\VETER.SCH
Sheet of
Drawn By: