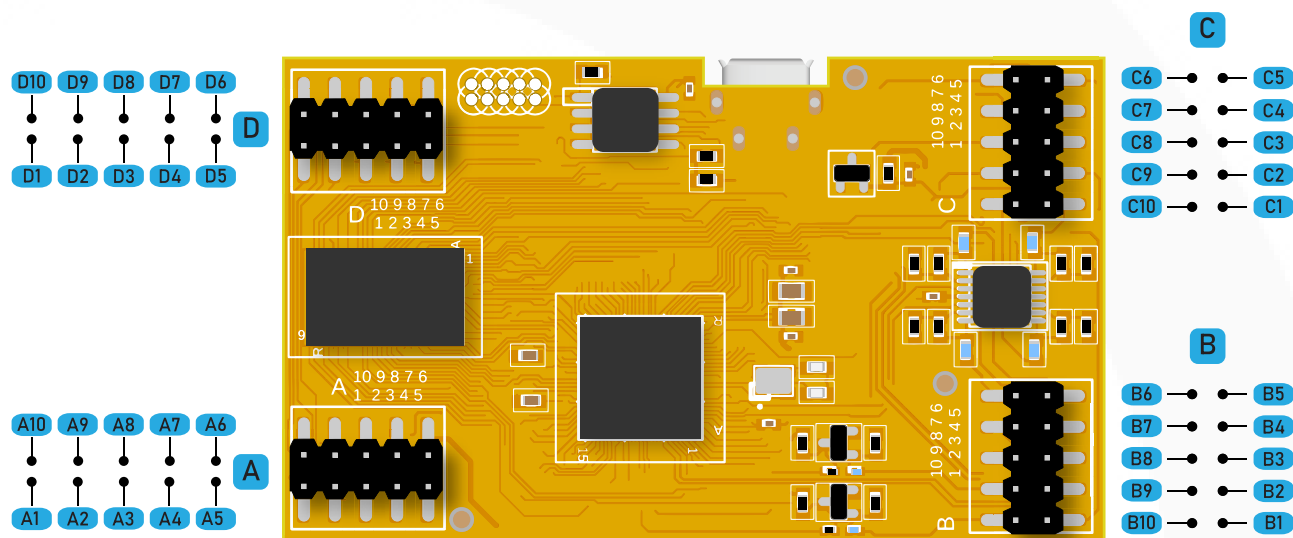




Patch SM Pinout



A Table	B Table	C Table	D Table
A1 ← -12V IN	B1 AUDIO_OUT_RIGHT	C1 CV_OUT_2	D1 SPI_CS
A2 ADC_9 UART_RX	B2 AUDIO_OUT_LEFT	C2 CV_4	D2 SDMMC_D3 UART_RX
A3 ADC_10 UART_TX	B3 AUDIO_IN_RIGHT	C3 CV_3	D3 SDMMC_D2 UART_TX
A4 GND	B4 AUDIO_IN_LEFT	C4 CV_2	D4 SDMMC_D1
A5 ← +12V IN	B5 GATE_OUT_1	C5 CV_1	D5 SDMMC_D0
A6 → +5V OUT	B6 GATE_OUT_2	C6 CV_5	D6 SDMMC_CLK UART_RX
A7 GND	B7 I2C_SCL UART_RX PWM	C7 CV_6	D7 SDMMC_CMD UART_TX
A8 USB_DM UART_TX	B8 I2C_SDA UART_TX PWM	C8 CV_7	D8 ADC_12 SPI_MISO
A9 USB_DP UART_RX	B9 GATE_IN_2	C9 CV_8	D9 ADC_11 SPI_MOSI
A10 → +3V3 OUT	B10 GATE_IN_1	C10 CV_OUT_1	D10 SPI_SCK

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GPIO

GROUND

POWER

DAISY
PIN NAMES