

# A verified parallel scheduler for OCaml 5

ANONYMOUS AUTHOR(S)

We present the implementation and mechanized verification of a realistic parallel scheduler for OCaml 5 using the Iris-based Zoo framework. Similarly to Domainslib, it relies on a work-stealing strategy to perform load balancing but also supports other scheduling strategies thanks to its flexible interface. We provide basic benchmarks demonstrating that its performance is on par with other schedulers from the OCaml ecosystem.

As part of this effort, we verify the Chase-Lev work-stealing deque, as implemented in the Saturn library. We show that it features a subtle external and future-dependent linearization point. To deal with it, we introduce new abstractions for reasoning about prophecy variables in Iris.

## 1 Introduction

The OCaml programming language migrated from a sequential to a multicore runtime in OCaml 5, imported from the Multicore OCaml research project [Sivaramakrishnan, Dolan, White, Jaffer, Kelly, Sahoo, Parimala, Dhiman and Madhavapeddy 2020] and first released as OCaml 5.0 in 2022. The sequential runtime had a “big runtime lock” guaranteeing that at most one OCaml thread would run at any point in time. The multicore runtime supports “domains” (heavyweight threads) that can run OCaml code in parallel, operating on a shared heap and cooperating for garbage collection. It is designed to offer a M:N threading model with M lightweight tasks (or threads, fibers) are mapped to N domains, with N no larger than the number of CPU cores<sup>1</sup>. The implementation of lightweight tasks and their scheduler is left to be done in userland, as an OCaml library running on top of runtime-provided domains.

The authors of the Multicore OCaml runtime implemented the Domainslib library for CPU-bound tasks, initially to write benchmarks to test the scalability of the OCaml runtime. It uses a work-stealing scheduler and is state-of-the-art in the OCaml library ecosystem. Other lightweight task libraries include Moonpool, which was implemented independently, and Eio which focuses on efficient asynchronous I/O. All of those schedulers have been used in performance-sensitive scenarios, benchmarked and optimized, notably using lock-free data structures implemented in OCaml 5.

In this work we present Parabs, a verified implementation of a state-of-the-art scheduler for lightweight tasks, following the overall design of Domainslib, with some implementation choices inspired by the Taskflow C++ library [Huang, Lin, Lin and Lin 2022]. This verification builds on top of the Zoo framework [Allain and Scherer 2026], which supports the formal verification of a subset of OCaml 5 in the Iris program logic [Jung, Krebbers, Jourdan, Bizjak, Birkedal and Dreyer 2018], mechanized within the Rocq proof assistant. Our verification effort includes a new mechanized verification of the Chase-Lev work-stealing queue [Chase and Lev 2005], with stronger invariants than had previously appeared in the literature. Our scheduler supports two different scheduling strategies, one using standard randomized work-stealing [Blumofe and Leiserson 1999], the other using work-stealing with *private* deques [Acar, Charguéraud and Rainey 2013]. On top of the scheduler, we expose an API closely resembling Domainslib, but also a *task graph* abstraction which implements the DAG-calculus of Acar, Charguéraud, Rainey and Sieczkowski [2016].

---

<sup>1</sup>The multicore runtime of OCaml performs frequent stop-the-world pause in its garbage collector, to facilitate the migration of existing programs using the OCaml foreign function interface — this design does not require adding a read barrier. A consequence of these stop-the-world events is that runtime performance declines sharply if a domain is paused by the operating system, so it is critical to limit the number of domains to available cores: domains are a heavier, less composable abstraction than “kernel threads” in typical M:N models. In consequence, they must be controlled by end-used applications, possibly via a concurrency framework that hides them entirely, and software libraries should not implicitly spawn new domains, they need to use a more lightweight task abstraction.

This work is focused on formal verification but we did write and run relatively simple benchmarks to validate experimentally that the performance of our verified implementation, Parabs, is comparable to Domainslib; in our tests it is equal or faster.

*Contributions.* Our contributions include:

- (1) A new mechanized verification of the Chase-Lev work-stealing queue [Chase and Lev 2005], with finer-grained invariants than had previously appeared in the literature. In particular, our invariant lets us reason about the failure case of the pop operation, which was missing from earlier formalizations and is essential to prove termination of the work-stealing scheduler when all task queues are exhausted.
- (2) A fully verified implementation of a parallel scheduler in OCaml 5, Parabs, which provides a verified alternative to the state-of-the-art Domainslib library. To the best of our knowledge, this is the first verified implementation of a parallel work-stealing task scheduler (for any language) using realistic implementation techniques. Our experimental evaluation on a set of simple benchmarks shows that Parabs has comparable or better performance than Domainslib, and better performance than Moonpool.
- (3) A verified implementation of the DAG-calculus interface for parallel task graphs proposed by Acar, Charguéraud and Rainey [2013].
- (4) At the level of Iris proof techniques, we developed extensions of prophecy variables. To reason about the linearization points of our concurrent data structures we needed to introduce “wise” and “multiplexed” prophecy variables, which are reusable building blocks and could be useful for the verification of other concurrent data structures, in any Iris formalization of any programming language.

*Artifact.* The Zoo verification framework supports a fragment of OCaml called ZooLang, with formal semantics defined as an Iris program logic, and a partial translator from OCaml to ZooLang programs deeply embedded within Rocq. Our developments are thus available as OCaml libraries that are readily available for usage, and their Rocq specifications, invariants and proofs. For reasons of space we cannot possibly hope to describe them in full in the paper, so we focus on the most readily reusable parts: specifications, and key invariants and proof techniques. For more details, we view our code and mechanized proofs as an integral part of this submission; they are available at <https://anonymous.4open.science/r/zoo-A236>, publicly available and open-source.<sup>2</sup> To ease in-depth exploration, the paper contains direct references to the implementation and the proof as picture/icon links, for example 🦊 and 🐼.

## 2 Iris arsenal

*Separation logic.* Iris is a concurrent separation logic [O’Hearn 2007; O’Hearn, Reynolds and Yang 2001; Reynolds 2002] fully mechanized in the Rocq proof assistant [Krebbers, Jourdan, Jung, Tassarotti, Kaiser, Timany, Charguéraud and Dreyer 2018]. As such, it features basic connectives like separation conjunction  $*$  and separating implication  $\multimap$ .

*Persistent assertions.* In Iris, assertions are affine: using a resource consumes it, removes it from the proof context. Some assertions, however, are *persistent*. Once a persistent assertion holds, it holds forever; using it does not consume it. This enables *duplication* ( $P \vdash P * P$ ) and *sharing*. In particular, pure (meta-level) assertions embedded into the logic are persistent.

<sup>2</sup>For ease of development we followed the Zoo approach of working in a mono-repository, so we have an experimental version of Zoo with our developments added, as well as some cross-cutting improvements to the pre-existing support libraries and tactics.

Formally, persistence is defined in terms of the *persistence modality*:

$$\text{persistent } P \triangleq P \vdash \Box P$$

Informally,  $\Box P$  means  $P$  holds without asserting any exclusive ownership; in other words, it only expresses knowledge. Naturally,  $\Box P$  is persistent.

*Ghost state.* One of the most important features of Iris is its *user-defined higher-order ghost state*, a very flexible form of ghost state. Ghost updates, of the form  $\Rightarrow P$ , allow updating the ghost state during the proof; they are purely logical, hence not visible in the program.

*Sequential specification.* Sequential specifications take the form of Hoare triples:

$$\frac{\frac{P}{e}}{\Phi} \quad \frac{\frac{\text{stack-model } t \text{ vs}}{\text{stack\_push } t \text{ } v}}{(\cdot). \text{stack-model } t \text{ } (v :: \text{vs})}$$

where  $P$  is an Iris assertion,  $e$  an expression and  $\Phi$  an Iris predicate over values.

Informally, this triple says: if the precondition  $P$  holds, we can safely execute  $e$  and, if the execution terminates, the returned value satisfies the postcondition  $\Phi$ . It is a persistent resource, allowing executing  $e$  many times.

*Weakest precondition.* Hoare triples are defined using the more primitive notion of *weakest precondition*  $\text{wp } e \{ \Phi \}$ . Informally, it says that: once only, we can execute  $e$  and, if the execution terminates, the returned value satisfies the postcondition  $\Phi$ . Contrary to Hoare triples, it can depend on exclusive ownership and therefore is not persistent.

*Atomic specification.* To specify concurrent operations, we use the notion of *logical atomicity* [da Rocha Pinto, Dinsdale-Young and Gardner 2014]. An operation is said to be logically atomic if it appears to take effect atomically at some point during its execution; this point is called the *linearization point* of the operation. Birkedal, Dinsdale-Young, Guéneau, Jaber, Svendsen and Tzevelekos showed that this notion implies *linearizability* [Herlihy and Wing 1990] in a sequentially consistent memory model.

In Iris, logical atomicity takes the form of *atomic specifications*:

$$\frac{\frac{\frac{P_{\text{priv}}}{\bar{x}. P_{\text{pub}}}}{e}}{\bar{y}. Q}{\Phi} \quad \frac{\frac{\frac{\text{stack-inv } t}{\text{vs. stack-model } t \text{ vs}}}{\text{stack\_push } t \text{ } v}}{\text{stack-model } t \text{ } (v :: \text{vs})}{(\cdot). \text{True}}$$

$P_{\text{priv}}$  and  $\Phi$  are standard *private* pre- and postcondition for the user of the specification, similarly to Hoare triples.  $P_{\text{pub}}$  and  $Q$  are *public* pre- and postcondition; they specify the linearization point of the operation. Quantifiers  $\bar{x}$  represent the *demonic nature* of  $P_{\text{pub}}$ : the exact state at the linearization point, given by  $P_{\text{pub}}$ , is unknown until it happens. Quantifiers  $\bar{y}$  represent the *angelic nature* of  $Q$ : at the linearization point, the operation can choose how to instantiate the new state  $Q$ .

In sum, the atomic specification says: if the private precondition  $P_{\text{priv}}$  holds, we can safely execute  $e$  and, if the execution terminates, (1) the returned value satisfies the private postcondition  $\Phi$  and (2) at some point during the execution, the state was atomically updated from  $P_{\text{pub}}$  to  $Q$ .

### 3 Prophecy variables

In 2020, Jung, Lepigre, Parthasarathy, Rapoport, Timany, Dreyer and Jacobs introduced *prophecy variables* in Iris. Essentially, prophecy variables — or *prophets*, as we will call them in this section —

PROPHET-MODEL-EXCLUSIVE		WP-PROPH	
$\text{model } pid \text{ prophs}_1$	$\text{model } pid \text{ prophs}_2$	True	
<hr/>		Proph	
False		$pid. \exists \text{ prophs. } \text{model } pid \text{ prophs}$	
<hr/>		<hr/>	
WP-RESOLVE		atomic $e$	
to-val $e = \text{None}$	$\text{model } pid \text{ prophs}$	$\text{wp } e \left\{ \begin{array}{l} \text{res. } \forall \text{ prophs}'. \\ \text{prophs} = (\text{res}, v) :: \text{prophs}' \text{ } \ast \\ \text{model } pid \text{ prophs}' \text{ } \ast \\ \Phi \text{ res} \end{array} \right\}$	
<hr/>		$\text{wp Resolve } e \text{ } pid \text{ } v \{ \Phi \}$	

Fig. 1. Reasoning rules for primitive prophets

can be used to predict the future of the program execution and reason about it. They are key to handle *future-dependent linearization points* [Dongol and Derrick 2014]: linearization points that may or may not occur at a given location in the code depending on a future observation.

In the program, prophecies take the form of two primitives: Proph and Resolve. To reason about them in the logic, Jung, Lepigre, Parthasarathy, Rapoport, Timany, Dreyer and Jacobs [2020] proposed two abstraction layers, which we recall in Sections 3.1 and 3.2. To verify the Chase-Lev work-stealing deque (see Section 4), we needed to introduce two additional layers, presented in Sections 3.3 and 3.4.

### 3.1 Primitive prophet

The first layer consists of *primitive prophets* 🏹. These prophets are primitive in the sense that they simply reflect the semantics of Proph and Resolve in the program logic. The corresponding reasoning rules are given in Figure 1.

The assertion  $\text{model } pid \text{ prophs}$  represents the exclusive ownership of the prophet with identifier  $pid$ ;  $\text{prophs}$  is the list of prophecies that must still be resolved.

WP-PROPH says that Proph allocates a new prophet with some unknown prophecies to be resolved.

WP-RESOLVE says that Resolve  $e \text{ } pid \text{ } v$  *atomically* resolves the next prophecy of prophet  $pid$ : we learn that the prophecies before resolution  $\text{prophs}$  is non-empty and its head is the pair  $(\text{res}, v)$  where  $\text{res}$  is the evaluation of  $e$ .

### 3.2 Typed prophet

The second layer consists of *typed prophets* 🏹. They are very similar to primitive prophets except prophecies are now typed. The corresponding reasoning rules, given in Figure 2, are essentially the same as before. The prophet must provide a type  $\tau$  along with two functions of-val and to-val. to-val converts an inhabitant of  $\tau$  to a value; TYPED-PROPHET-RESOLVE-SPEC relies on it to enforce that the prophecies are well-typed. of-val attempts to convert a value to  $\tau$ ; it is used internally. of-val and to-val must be compatible:  $\text{of-val } (\text{to-val } \text{proph}) = \text{Some } \text{proph}$ .

### 3.3 Wise prophet

The third layer consists of *wise prophets* 🏹. These prophets *remember* past prophecies. The corresponding reasoning rules are given in Figure 3.

TYPED-PROPHET-MODEL-EXCLUSIVE		TYPED-PROPHET-PROPH-SPEC	
<i>model</i> <i>pid</i> <i>prophs</i> <sub>1</sub>	<i>model</i> <i>pid</i> <i>prophs</i> <sub>2</sub>	True	
<hr/>		<hr/>	
False		Proph	
		<i>pid</i> . $\exists$ <i>prophs</i> . <i>model</i> <i>pid</i> <i>prophs</i>	
<hr/>			
TYPED-PROPHET-RESOLVE-SPEC			
	atomic <i>e</i>	to-val <i>e</i> = None	
<i>v</i> = <i>prophet.to-val</i> <i>proph</i>	<i>model</i> <i>pid</i> <i>prophs</i>	$\text{wp } e \left\{ \begin{array}{l} w. \forall \textit{prophs}'. \\ \textit{prophs} = \textit{proph} :: \textit{prophs}' \multimap \\ \textit{model } \textit{pid } \textit{prophs}' \multimap \\ \Phi \ w \end{array} \right\}$	
<hr/>			
wp Resolve <i>e</i> <i>pid</i> <i>v</i> { $\Phi$ }			

Fig. 2. Reasoning rules for typed prophets

persistent ( $\text{full } \gamma \text{ } prophs$ )		persistent ( $\text{snapshot } \gamma \text{ } past \text{ } prophs$ )		persistent ( $\text{lb } \gamma \text{ } lb$ )	
WISE-PROPHET-MODEL-EXCLUSIVE			WISE-PROPHET-FULL-GET		
$\text{model } pid \text{ } \gamma_1 \text{ } past_1 \text{ } prophs_1$		$\text{model } pid \text{ } \gamma_2 \text{ } past_2 \text{ } prophs_2$		$\text{model } pid \text{ } \gamma \text{ } past \text{ } prophs$	
<hr/>		<hr/>		<hr/>	
False				$\text{full } \gamma \text{ } (past \text{ } \# \text{ } prophs)$	
WISE-PROPHET-FULL-VALID			WISE-PROPHET-FULL-AGREE		
$\text{model } pid \text{ } \gamma \text{ } past \text{ } prophs_1$		$\text{full } \gamma \text{ } prophs_2$		$\text{full } \gamma \text{ } prophs_1 \quad \text{full } \gamma \text{ } prophs_2$	
<hr/>		<hr/>		<hr/>	
$prophs_2 = past \text{ } \# \text{ } prophs_1$				$prophs_1 = prophs_2$	
WISE-PROPHET-SNAPSHOT-GET			WISE-PROPHET-SNAPSHOT-VALID		
$\text{model } pid \text{ } \gamma \text{ } past \text{ } prophs$		$\text{model } pid \text{ } \gamma \text{ } past_1 \text{ } prophs_1$		$\text{snapshot } \gamma \text{ } past_2 \text{ } prophs_2$	
<hr/>		<hr/>		<hr/>	
$\text{snapshot } \gamma \text{ } past \text{ } prophs$		$\exists past_3. past_1 = past_2 \text{ } \# \text{ } past_3 \text{ } * \text{ } prophs_2 = past_3 \text{ } \# \text{ } prophs_1$			
WISE-PROPHET-LB-GET			WISE-PROPHET-LB-VALID		
$\text{model } pid \text{ } \gamma \text{ } past \text{ } prophs$		$\text{model } pid \text{ } \gamma \text{ } past \text{ } prophs$		$\text{lb } \gamma \text{ } lb$	
<hr/>		<hr/>		<hr/>	
$\text{lb } \gamma \text{ } prophs$		$\exists past_1 \text{ } past_2. past = past_1 \text{ } \# \text{ } past_2 \text{ } * \text{ } lb = past_2 \text{ } \# \text{ } prophs$			
WISE-PROPHET-PROPH-SPEC					
<hr/>					
True					
<hr/>					
Proph					
<hr/>					
$pid. \exists \gamma \text{ } prophs. \text{model } pid \text{ } \gamma \text{ } [] \text{ } prophs$					
WISE-PROPHET-RESOLVE-SPEC					
atomic $e$		to-val $e = \text{None}$		$v = \text{prophet.to-val } proph$	
$\text{model } pid \text{ } \gamma \text{ } past \text{ } prophs$		$\text{wp } e$		$\left\{ \begin{array}{l} w. \forall prophs'. \\ prophs = proph :: prophs' \text{ } \# \\ \text{model } pid \text{ } \gamma \text{ } (past \text{ } \# \text{ } [proph]) \text{ } prophs' \text{ } \# \\ \Phi \text{ } w \end{array} \right\}$	
<hr/>		<hr/>			
$\text{wp Resolve } e \text{ } pid \text{ } v \{ \Phi \}$					

Fig. 3. Reasoning rules for wise prophets

The exclusive assertion `model pid γ past prophs` represents the ownership of the prophet with identifier `pid`;  $\gamma$  is the logical name of the prophet; `past` is the list of prophecies resolved so far; `prophs` is the list of prophecies that must still be resolved.

The persistent assertion `full γ prophs` represents the list of all (resolved or not) prophecies associated to the prophet with name  $\gamma$ , as stated by `WISE-PROPHET-FULL-VALID`.

The persistent assertion `snapshot γ past prophs` represents a snapshot of the state of the prophet with name  $\gamma$  at some point in the past. `WISE-PROPHET-SNAPSHOT-VALID` allows to relate the current state of `model` to the past state of `snapshot`.

The persistent assertion `lb γ lb` represents a lower bound on the non-resolved prophecies of the prophet with name  $\gamma$ . In particular, as stated by `WISE-PROPHET-LB-VALID`, the list of currently non-resolved prophecies carried by `model` is always a suffix of `lb`.

`WISE-PROPHET-RESOLVE-SPEC` is the same as before, except we also update the list of resolved prophecies after resolution.

### 3.4 Multiplexed prophet

The fourth layer consists of *multiplexed prophets* 🐼. Essentially, they allow to combine different prophets, each operating at a fixed index. They were made to handle the case when a single prophet is used to make independent predictions, as in Section 4. The corresponding reasoning rules are given in Figure 4.

The predicates and rules are basically the same as before, except that (1) `model` now carries sequences of lists of prophecies — one past and one future per index — and (2) `full`, `snapshot` and `lb` are parameterized with an index.

Importantly, the third argument provided to `Resolve` in `WISE-PROPHETS-RESOLVE-SPEC` must be a pair of an index and a prophecy value. Resolution happens only at the given index, meaning the prophecies at other indices are unchanged.

Note that we could generalize this abstraction to non-integer keys. In other words, we could replace sequences with functions of type  $X \rightarrow \tau$ , where  $\tau$  is the prophecy type, and indices with inhabitants of  $X$ . In practice, however, we never needed such generalization.

## 4 Chase-Lev work-stealing deque

*Work-stealing*. Randomized *work stealing* [Blumofe and Leiserson 1999] is the standard strategy for parallel task scheduling. It has been implemented in many libraries, including Cilk [Blumofe, Joerg, Kuszmaul, Leiserson, Randall and Zhou 1996; Frigo, Leiserson and Randall 1998], TBB, OpenMP, Taskflow [Huang, Lin, Lin and Lin 2022], Tokio and Domainslib [Multicore OCaml development team 2025].

The idea of work-stealing, illustrated in Figure 5, is the following. Each domain owns a deque-like data structure, called *work-stealing deque*, to store its tasks. Locally, each domain treats its deque as a stack, operating at the back end. When a domain runs out of tasks, it becomes a thief: it tries to steal a task from the deque of another randomly selected “victim” domain, operating at the front end. Multiple thieves may concurrently attempt to steal tasks from a single deque.

*Work-stealing deque*. The most popular work-stealing deque algorithm is the Chase-Lev deque [Chase and Lev 2005; Lê, Pop, Cohen and Nardelli 2013]; it is lock-free and unbounded. We verified the implementation from the Saturn library [Karvonen and Morel 2025] 🐼 🐼 along with two other variants: a bounded variant 🐼 🐼, used in the Moonpool [Cruanes 2025] and Taskflow [Huang, Lin, Lin and Lin 2022] libraries, and an idealized infinite-array-based variant 🐼 🐼.

$$\begin{array}{c}
\text{persistent } (\text{full } \gamma \ i \ \text{prophs}) \quad \text{persistent } (\text{snapshot } \gamma \ i \ \text{past } \text{prophs}) \quad \text{persistent } (\text{lb } \gamma \ i \ \text{lb}) \\
\\
\text{WISE-PROPHETS-MODEL-EXCLUSIVE} \quad \text{WISE-PROPHETS-FULL-GET} \\
\frac{\text{model } pid \ \gamma_1 \ \text{pasts}_1 \ \text{prophss}_1 \quad \text{model } pid \ \gamma_2 \ \text{pasts}_2 \ \text{prophss}_2}{\text{False}} \quad \frac{\text{model } pid \ \gamma \ \text{pasts } \text{prophss}}{\text{full } \gamma \ i \ (\text{pasts } i \ \# \ \text{prophss } i)} \\
\\
\text{WISE-PROPHETS-FULL-VALID} \quad \text{WISE-PROPHETS-FULL-AGREE} \\
\frac{\text{model } pid \ \gamma \ \text{pasts } \text{prophss} \quad \text{full } \gamma \ i \ \text{prophs}}{\text{prophs} = \text{pasts } i \ \# \ \text{prophss } i} \quad \frac{\text{full } \gamma \ i \ \text{prophs}_1 \quad \text{full } \gamma \ i \ \text{prophs}_2}{\text{prophs}_1 = \text{prophs}_2} \\
\\
\text{WISE-PROPHETS-SNAPSHOT-GET} \\
\frac{\text{model } pid \ \gamma \ \text{pasts } \text{prophss}}{\text{snapshot } \gamma \ (\text{pasts } i) \ (\text{prophss } i)} \\
\\
\text{WISE-PROPHETS-SNAPSHOT-VALID} \quad \text{WISE-PROPHETS-LB-GET} \\
\frac{\text{model } pid \ \gamma \ \text{pasts } \text{prophss} \quad \text{snapshot } \gamma \ i \ (\text{pasts } i) \ (\text{prophss } i)}{\exists \text{past}' . \text{pasts } i = \text{past} \ \# \ \text{past}' * \text{prophs} = \text{past}' \ \# \ \text{prophss } i} \quad \frac{\text{model } pid \ \gamma \ \text{pasts } \text{prophss}}{\text{lb } \gamma \ i \ (\text{prophss } i)} \\
\\
\text{WISE-PROPHETS-LB-VALID} \\
\frac{\text{model } pid \ \gamma \ \text{pasts } \text{prophss} \quad \text{lb } \gamma \ i \ \text{lb}}{\exists \text{past}_1 \ \text{past}_2 . \text{pasts } i = \text{past}_1 \ \# \ \text{past}_2 * \text{lb} = \text{past}_2 \ \# \ \text{prophss } i} \\
\\
\text{WISE-PROPHETS-PROPH-SPEC} \\
\frac{\text{True}}{\text{Proph}} \\
\frac{}{pid . \exists \gamma \ \text{prophss} . \text{model } pid \ \gamma \ (\lambda \_ . []) \ \text{prophss}} \\
\\
\text{WISE-PROPHETS-RESOLVE-SPEC} \\
\text{atomic } e \quad \text{to-val } e = \text{None} \quad v = \text{prophet.to-val } \text{proph} \quad \text{model } pid \ \gamma \ \text{pasts } \text{prophss} \\
\text{wp } e \left\{ \begin{array}{l} w . \forall \text{prophs} . \\ \text{prophss } i = \text{proph} :: \text{prophs} \text{ -*} \\ \text{model } pid \ \gamma \ (\text{alter } (\cdot \ \# \ [\text{proph}]) \ i \ \text{pasts}) \ (\text{prophss } [i \mapsto \text{prophs}]) \text{ -*} \\ \Phi \ w \end{array} \right\} \\
\hline
\text{wp Resolve } e \ pid \ (i, v) \ \{ \Phi \}
\end{array}$$

Fig. 4. Reasoning rules for multiplexed prophets

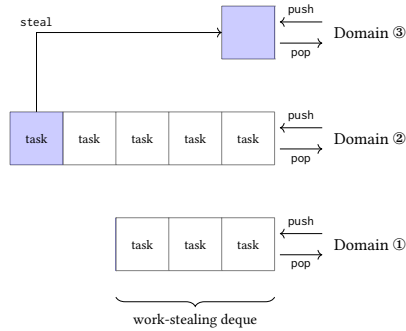


Fig. 5. Work stealing



persistent ( <i>inv t</i> )		
INF-WS-DEQUE-MODEL-EXCLUSIVE $\frac{\text{model } t \text{ vs}_1 \quad \text{model } t \text{ vs}_2}{\text{False}}$	INF-WS-DEQUE-OWNER-EXCLUSIVE $\frac{\text{owner } t \text{ ws}_1 \quad \text{owner } t \text{ ws}_2}{\text{False}}$	INF-WS-DEQUE-OWNER-MODEL $\frac{\text{owner } t \text{ ws} \quad \text{model } t \text{ vs}}{\text{suffix vs ws}}$
INF-WS-DEQUE-CREATE-SPEC $\frac{\text{True}}{\text{create } ()}$ $t. \text{inv } t *$ $\text{model } t [] *$ $\text{owner } t []$	INF-WS-DEQUE-SIZE-SPEC $\frac{\text{inv } t * \quad \text{owner } t \text{ ws}}{\text{vs. model } t \text{ vs}}$ $\frac{\text{size } t}{\text{suffix vs ws } *}$ $\frac{\text{model } t \text{ vs}}{\text{res. res = length vs } *}$ $\text{owner } t \text{ vs}$	INF-WS-DEQUE-IS-EMPTY-SPEC $\frac{\text{inv } t * \quad \text{owner } t \text{ ws}}{\text{vs. model } t \text{ vs}}$ $\frac{\text{is\_empty } t}{\text{suffix vs ws } *}$ $\frac{\text{model } t \text{ vs}}{\text{res. res = decide (vs = []) } *}$ $\text{owner } t \text{ vs}$
INF-WS-DEQUE-PUSH-SPEC $\frac{\text{inv } t * \quad \text{owner } t \text{ ws}}{\text{vs. model } t \text{ vs}}$ $\frac{\text{push } t \text{ v}}{\text{suffix vs ws } *}$ $\frac{\text{model } t (\text{vs} \# [v])}{(). \text{owner } t (\text{vs} \# [v])}$	INF-WS-DEQUE-STEAL-SPEC $\frac{\text{inv } t \quad \text{vs. model } t \text{ vs}}{\text{steal } t}$ $\frac{\text{model } t (\text{tail vs})}{\text{res. res = head vs}}$	INF-WS-DEQUE-POP-SPEC $\frac{\text{inv } t * \quad \text{owner } t \text{ ws}}{\text{vs. model } t \text{ vs}}$ $\frac{\text{pop } t}{o \text{ ws}' . \text{suffix vs ws } *}$ $\text{match } o \text{ with}$ $  \text{None} \Rightarrow$ $\text{vs} = [] * \text{ws}' = [] *$ $\text{model } t []$ $  \text{Some } v \Rightarrow$ $\exists \text{vs}' .$ $\text{vs} = \text{vs}' \# [v] * \text{ws}' = \text{vs}' *$ $\text{model } t \text{ vs}'$ $\text{end}$ $\text{res. res} = o *$ $\text{owner } t \text{ ws}'$

Fig. 6. **Inf\_ws\_deque**: Specification

Remarkably, the three variants essentially share the same logical states. In particular, although they do not behave exactly the same way, the original and the idealized versions follow a similar concurrent protocol, involving external and future-dependent linearization.

#### 4.1 Infinite work-stealing deque

**4.1.1 Specification.** The specification of the infinite-array-based version is given in Figure 6. It features three predicates: *inv*, *model* and *owner*.

The persistent assertion *inv t* represents the knowledge that *t* is a valid deque. It is returned by *create* (**INF-WS-DEQUE-CREATE-SPEC**) and required by all operations.


The exclusive assertion *model t vs* represents the ownership of the content of the deque *vs*. It is returned by *create* and accessed atomically by all operations.



WS-DEQUE-STEAL-SPEC-WEAK	WS-DEQUE-POP-SPEC-WEAK
$\text{inv } t$	$\text{inv } t *$
$\text{vs. model } t \text{ vs}$	$\text{vs. owner } t \text{ vs}$
$\text{steal } t$	$\text{pop } t$
$o. \text{match } o \text{ with}$	$o. \text{match } o \text{ with}$
None $\Rightarrow$	None $\Rightarrow$
$\text{model } t \text{ vs}$	$\text{model } t \text{ vs}$
Some $v \Rightarrow$	Some $v \Rightarrow$
$\exists \text{ vs}'. \text{ vs} = v :: \text{vs}' *$	$\exists \text{ vs}'. \text{ vs} = \text{vs}' \# [v] *$
$\text{model } t \text{ vs}'$	$\text{model } t \text{ vs}'$
<b>end</b>	<b>end</b>
$\text{res. res} = o$	$\text{res. res} = o *$
	$\text{owner } t$

Fig. 7. `Ws_deque`: Weak specification (excerpt)

The exclusive assertion `owner t ws` represents the owner of the deque; `ws` is an upper bound on the current content of the deque (`INF-WS-DEQUE-OWNER-MODEL`). It is returned by `create` and used by all private operation: `size` (`INF-WS-DEQUE-SIZE-SPEC`), `is_empty` (`INF-WS-DEQUE-IS-EMPTY-SPEC`), `push` (`INF-WS-DEQUE-PUSH-SPEC`) and `pop` (`INF-WS-DEQUE-POP-SPEC`). The only public operation is `steal` (`INF-WS-DEQUE-STEAL-SPEC`), which does not require `owner`.

Note that the public postconditions of the private operations are quite verbose. This is due to the fact that `owner` is passed to the operation and therefore cannot be combined with `model` through `INF-WS-DEQUE-OWNER-MODEL` to get information about the content of the deque; instead, we provide such information in the public postcondition. We need this expressivity in practice to verify a wrapper  with better liveness properties.

**4.1.2 Weak specification.** Jung, Lee, Choi, Kim, Park and Kang [2023]<sup>3</sup> also worked on the verification of the Chase-Lev work-stealing deque. However, we argue that the specification they prove, given in Figure 7, is unsatisfactory. Indeed, contrary to our specification, `WS-DEQUE-STEAL-SPEC-WEAK` and `WS-DEQUE-POP-SPEC-WEAK` say nothing about the observed content of the deque when the operation fails.

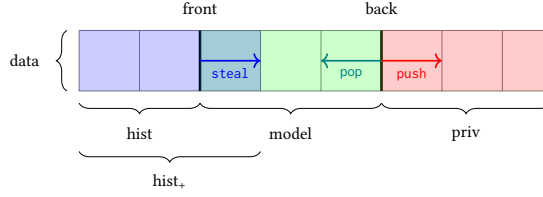
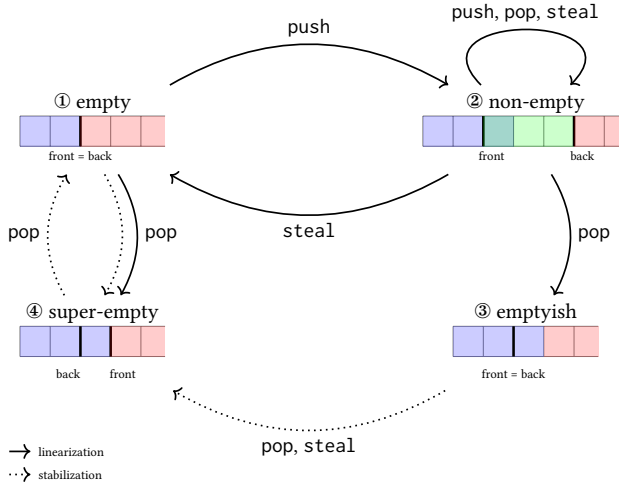
In practice, these weaker specifications, especially that of `pop`, are not sufficient to reason about the *termination* of a work-stealing scheduler. In Section 5, we show how our strong specifications are lifted all the way up to the scheduler.

Another point we would like to make is that weakening the specification does make the verification simpler, but one may argue that the most subtle and interesting part of it is lost.

**4.1.3 Implementation.** The implementation relies on (1) an infinite array, (2) a *monotonic* front index for the thieves, and (3) a back index reserved to the owner of the deque.

In general, we can divide the infinite array as in Figure 8. The first part, between 0 and the front index, corresponds to the *persistent* history of stolen values. The second part, between the two indices, corresponds to the logical content of the deque, as represented by `model`. The last part, beyond the back index, corresponds to the private section of the array, reserved to the owner.

<sup>3</sup>See also the master thesis of Choi [2023].

Fig. 8. `Inf_ws_deque`: Physical stateFig. 9. `Inf_ws_deque`: Logical state

Given this representation, the algorithm proceeds as follows. `push t v` writes  $v$  into the first private cell and atomically increments the back index, thereby publishing the value. Symmetrically, `pop t` atomically decrements the back index and returns the value of the cell it just privatized. `steal t` is much more careful: (1) it reads the front and the back indices; (2) if the deque looks empty, it fails; (3) otherwise, it attempts to advance the front index; (4) if the update succeeds, the value at the front index is returned; (5) otherwise, it starts over.

The above description overlooked one crucial aspect: what happens at the limit, when pop and steal compete for the last value in the deque? In that case, the deque must be *stabilized*: pop also attempts to advance the front index before incrementing the back index — whether it wins the update or not — thereby equalizing the two indices.

**4.1.4 Logical states.** Figure 9 tells the same story as above in terms of four *logical states*: (1) in the stable “empty” state, the deque is indeed empty, as indicated by the two equal indices; (2) in the stable “non-empty” state, the `model` is non-empty, meaning thieves may compete for the first value; (3) in the unstable “emptyish” state, the thieves and the owner compete for the same value; (4) in the unstable “super-empty” state, some operation won the value and the deque is waiting to be stabilized by the owner.

Let us now focus on the “emptyish” state. In this physical configuration, it makes sense to say that the `model` of the deque should be empty. In fact, it has to be empty: if a steal operation observed this state, it would conclude that the deque is empty — except under a weak specification.

But then, if the `model` should be empty, which operation was linearized during the transition to the “emptyish” state? We have no choice: it should be the winner of the front update, *i.e.* the operation which triggers the transition to the “super-empty” state. In conclusion, we have to predict the winner at each index using a multiplexed prophecy variable (see [Section 3](#)).

## 4.2 Bounded work-stealing deque

In the bounded variant, the infinite array is replaced with a finite circular array. As a consequence, the convenient infinite representation goes away and tedious reasoning about circular array slices is required. However, the logical states and transitions as well as the prophecy mechanism are essentially the same.

It is an open question whether we could factorize part of the verification through a well-chosen abstraction that could be instantiated both with infinite and circular arrays. One certainty is that this is not possible without slightly altering the implementation of the infinite variant: in `steal`, the front cell is read after performing the update in the infinite variant, which would be incorrect in the finite variant since the owner is allowed to overwrite the value.

## 4.3 Dynamic work-stealing deque

In the original algorithm, the owner may dynamically resize the circular array. More precisely, it can change the array at will provided that the public part (between the two indices) is preserved. Thus, while only one array is stored in the deque, there can be many different circular arrays alive at the same time, *i.e.* accessible by thieves.

While the invariant of [Choi \[2023\]](#) requires additional ghost state to keep track of the arrays and maintain their compatibility, the precision of our notion of logical state allows to only maintain compatibility between the current array and the array read by the next winner (if any).

## 5 Parabs: A library of parallel abstractions

We present the verified Parabs library 🦊🔧, offering parallel abstractions atop a task scheduler. While it was originally based on `Domainslib` [[Multicore OCaml development team 2025](#)], it evolved as a more ambitious project aimed at unifying various existing paradigms and scheduling strategies. It was designed with a focus on *flexibility*, letting users choose the scheduling strategy and build their own scheduler. One of the motivations of this design is to provide a framework to easily develop and experiment parallel infrastructures in OCaml 5.

## 6 Overview

[Figure 10](#) gives an overview of Parabs; solid edges represent module dependencies while dashed edges represent interface implementations. Essentially, the library is made of four abstraction levels built on top of each other: `Ws_deques`, `Ws_hub`, `Pool` and `Future` / `Vertex`.

The `Pool` module provides a task scheduler; internally, it maintains a pool of domains. Its design is inspired by `Domainslib`, `Taskflow` [[Huang, Lin, Lin and Lin 2022](#)] and `Moonpool` [[Cruanes 2025](#)]. As of today, it supports three scheduling strategies: (1) standard randomized work-stealing [[Blumofe and Leiserson 1999](#)] with public deques (as presented in [Section 4](#)), (2) randomized work-stealing with private deques [[Acar, Charguéraud and Rainey 2013](#)], (3) a simple “first-in first-out” strategy with one shared queue. In addition, it should be possible to implement other scheduling strategies (see [Section 16](#)), *e.g.* work sharing.

On top of `Pool`, the `Vertex` module provides a *task graph* abstraction. More precisely, it is an implementation of *DAG-calculus* [[Acar, Charguéraud, Rainey and Sieczkowski 2016](#)] — we present it in [Section 13](#).

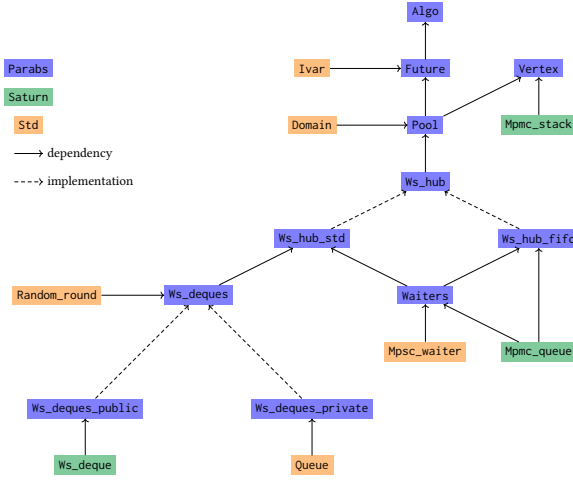


Fig. 10. Overview of the Parabs library

Remarkably, the three upper levels implemented on top of **Ws\_deques** should be OCaml functors. Unfortunately, ZooLang does not currently support functors; therefore, only one branch of the tree of Figure 10 is active at a time.

## 7 Work-stealing dequeues

At the first level, **Ws\_deques** 🐘 provides a generic interface for a set of work-stealing dequeues, abstracting over the underlying scheduling strategy. It currently has two realizations: **Ws\_deques\_public** (Section 7.1) and **Ws\_deques\_private** (Section 7.2).

### 7.1 Public dequeues

The first realization, **Ws\_deques\_public** 🐘 🚗, implements the standard work-stealing strategy with *public dequeues*. More precisely, it simply relies on a shared array of Chase-Lev work-stealing dequeues (see Section 4). These dequeues are public in the sense that both their owner and the thieves can access it directly — which requires synchronization.

### 7.2 Private dequeues

The second realization, **Ws\_deques\_private** 🐘 🚗, implements the *receiver-initiated* work-stealing algorithm proposed by Acar, Charguéraud and Rainey [2013]<sup>4</sup>. Their idea is to reduce synchronization costs in the fast path of local (owner-only) operations by essentially introducing an indirection. They show that this work-stealing strategy performs well for *fine-grained* parallel programs, *i.e.* when task sizes are small, especially irregular graph computations.

Instead of stealing directly from public dequeues, thieves follow a protocol: (1) having selected a victim, a thief attempts to send a request by atomically updating the *request cell* of the victim; (2) if the update fails, the thief starts over with another victim, otherwise it awaits a response by repeatedly checking its *response cell*; (3) if the response is negative, the thief starts over, otherwise it returns the task transferred by the victim.


Symmetrically, busy domains regularly poll their request cell and respond accordingly through response cells. Crucially, tasks are stored in private, non-concurrent dequeues that are only accessed

<sup>4</sup>They also propose a *sender-initiated* algorithm that we have not implemented.


by their owner. In addition, each domain has a *status cell* indicating whether it is (1) blocked, meaning it has no task to share, or (2) non-blocked, meaning it may have tasks to share; before sending a request, thieves check that their victim is non-blocked.

## 8 Waiters


In the realizations of the second level, described in the next section, we use a *sleep-based mechanism* to adapt the number of active thieves. The idea is to put to sleep desperate thieves who do not find work after a number of failed steal attempts. In practice, doing so can improve the overall system performance, especially when tasks are scarce.

To manage sleeping thieves, we use the **Waiters** module . Following the design of Taskflow [Huang, Lin, Lin and Lin 2022], it implements a *two-phase commit protocol*<sup>5</sup> — Domainslib<sup>6</sup> relies on a similar mechanism, although it is not as clear-cut.


## 9 Work-stealing hub

At the second level, **Ws\_hub** , provides a generic interface for a set of tasks supporting work-stealing operations — a so-called “work-stealing hub”. It currently has two realizations: **Ws\_hub\_std** (Section 9.1) and **Ws\_hub\_fifo** (Section 9.2).

### 9.1 Work-stealing strategy

The first realization, **Ws\_hub\_std** , implements the standard randomized work-stealing strategy. Under the hood, any work-stealing algorithm may be used, provided that it fits into the **Ws\_hub** interface; in particular, it can be instantiated with both realization of **Ws\_deques**.


### 9.2 FIFO strategy

The second realization, **Ws\_hub\_fifo** , implements a simple “first-in first-out” scheduling strategy. All workers push and pop tasks from a shared concurrent queue taken from Saturn; thieves also attempt to pop from the queue. Moonpool adopted a similar strategy<sup>7</sup>.

As explained by Cruanes<sup>8</sup>, the point of this strategy is to provide better *latency* than work-stealing — as demanded by certain applications like network servers — at the cost of a lower throughput. Indeed, contrary to work-stealing, older tasks have priority over younger tasks.

However, this strategy may also have undesirable consequences. For example, in divide-and-conquer algorithms, this strategy corresponds to *breadth-first* search, whereas work-stealing corresponds to *depth-first* search. On large problems, the former may be unsustainable; on some benchmarks (see Section 14), especially for small cutoffs, Moonpool saturates the memory.

## 10 Pool

At the third level, **Pool** , implements a task scheduler on top of a given realization of **Ws\_hub**. It offers essentially the same functionalities as Domainslib with a few notable differences. (1) Exceptions raised by tasks are not caught and therefore not re-raised properly by the scheduler since ZooLang does not currently support them. (2) Since ZooLang does not support algebraic effects [Sivaramakrishnan, Dolan, White, Kelly, Jaffer and Madhavapeddy 2021] neither, the interface is slightly more involved (see *execution contexts* in Section 10.1).

Moreover, this limitation imposes a *child-stealing* strategy, as opposed to a *continuation-stealing* strategy that would require capturing the continuation of a computation.

<sup>5</sup><https://www.1024cores.net/home/lock-free-algorithms/eventcounts>

<sup>6</sup>[https://github.com/ocaml-multicore/domainslib/blob/main/lib/multi\\_channel.ml](https://github.com/ocaml-multicore/domainslib/blob/main/lib/multi_channel.ml)

<sup>7</sup>[https://github.com/c-cube/moonpool/blob/main/src/core/fifo\\_pool.ml](https://github.com/c-cube/moonpool/blob/main/src/core/fifo_pool.ml)

<sup>8</sup>[https://github.com/c-cube/moonpool/blob/main/src/core/fifo\\_pool.mli](https://github.com/c-cube/moonpool/blob/main/src/core/fifo_pool.mli)

persistent ( <i>inv</i> <i>t</i> <i>sz</i> )		persistent ( <i>obligation</i> <i>t</i> <i>P</i> )		persistent ( <i>finished</i> <i>t</i> )	
$\frac{\text{POOL-INV-AGREE} \quad \text{inv } t \text{ } sz_1 \quad \text{inv } t \text{ } sz_2}{sz_1 = sz_2}$		$\frac{\text{POOL-OBLIGATION-FINISHED} \quad \text{obligation } t \text{ } P \quad \text{finished } t}{\triangleright \Box P}$			
$\frac{\text{POOL-CREATE-SPEC} \quad 0 \leq sz}{\text{create } sz}$		$\frac{\text{POOL-RUN-SPEC} \quad \text{model } t * \quad \forall \text{ ctx scope. } \text{context } t \text{ ctx scope } -* \quad \text{wp task ctx } \{ v. \text{context } t \text{ ctx scope } * \Psi v \}}{\text{run } t \text{ task}}$		$\frac{\text{POOL-KILL-SPEC} \quad \text{model } t}{\text{kill } t}$	
$\frac{\text{POOL-SIZE-SPEC} \quad \text{inv } t \text{ } sz * \quad \text{context } t \text{ ctx scope}}{\text{size ctx}}$		$\frac{\text{POOL-ASYNC-SPEC} \quad \text{context } t \text{ ctx scope } * \quad \forall \text{ ctx scope. } \text{context } t \text{ ctx scope } -* \quad \text{wp task ctx } \{ \_ . \text{context } t \text{ ctx scope } * \triangleright \Box P \}}{\text{async ctx task}}$		$\frac{\text{POOL-KILL-SPEC} \quad \text{kill } t}{(). \text{finished } t}$	
$\frac{\text{POOL-SIZE-SPEC} \quad \text{res. res} = sz * \quad \text{context } t \text{ ctx scope}}{\text{context } t \text{ ctx scope}}$		$\frac{\text{POOL-ASYNC-SPEC} \quad \text{context } t \text{ ctx scope } * \quad \forall \text{ ctx scope. } \text{context } t \text{ ctx scope } -* \quad \text{wp task ctx } \{ \_ . \text{context } t \text{ ctx scope } * \triangleright \Box P \}}{(). \text{context } t \text{ ctx scope } * \text{obligation } t \text{ } P}$			
		$\frac{\text{POOL-WAIT-UNTIL-SPEC} \quad \text{context } t \text{ ctx scope } * \quad \{ \text{True} \} \text{ pred } () \{ b. \text{if } b \text{ then } P \text{ else True} \}}{\text{wait\_until ctx pred}}$			
		$\frac{\text{POOL-WAIT-UNTIL-SPEC} \quad \text{context } t \text{ ctx scope } * \quad \{ \text{True} \} \text{ pred } () \{ b. \text{if } b \text{ then } P \text{ else True} \}}{(). \text{context } t \text{ ctx scope } * P}$			

Fig. 11. **Pool**: Specification

Also, this makes it difficult to implement a yield operation<sup>9</sup>, *i.e.* an operation that yields control to the scheduler, letting it reschedule the current task later.

## 10.1 Specification

The specification is given in Figure 11. It features five predicates: *inv*, *model*, *context*, *finished* and *obligation*.

The persistent assertion *inv* *t* *usz* represents the knowledge that *t* is a valid scheduler; *sz* is the number of worker domains. It is returned by *create* (**POOL-CREATE-SPEC**) and required only by *size* (**POOL-SIZE-SPEC**). Its only purpose is to record the immutable characteristics of the scheduler.

The assertion *model* *t* represents the ownership of scheduler *t*. It is returned by *create* (**POOL-CREATE-SPEC**) and required by external operations (**POOL-RUN-SPEC**, **POOL-KILL-SPEC**). For example, *run* *t* *task* submits *task* to scheduler *t*; it returns both *model* and the output predicate of *task*.

The assertion *context* *t* *ctx scope* represents the ownership of *execution context* *ctx* attached to scheduler *t*; *scope* is a purely logical parameter connecting input and output *context*, which is necessary in the proof. Any task execution happens under such a context (**POOL-RUN-SPEC**, **POOL-ASYNC-SPEC**, **POOL-WAIT-UNTIL-SPEC**). In particular, all internal operations require and return

<sup>9</sup>Domainslib does not currently provide a yield operation but it can be easily implemented.

**context.** For example, `async ctx task` submits `task` asynchronously while executing under context `ctx`; `task` must be shown to execute safely under any context attached to the same scheduler (**POOL-ASYNC-SPEC**).

The persistent assertion **finished** `t` represents the knowledge that scheduler `t` has finished, meaning all submitted tasks were executed. It can be obtained by calling `kill` (**POOL-KILL-SPEC**).

The persistent assertion **obligation** `t P` represents a proof obligation attached to scheduler `t`. It allows retrieving `P` once `t` has finished executing (**POOL-OBLIGATION-FINISHED**). Obligations are obtained by submitting tasks through `async` (**POOL-ASYNC-SPEC**).



## 10.2 Implementation

**Worker domains.** The implementation relies on a pool of worker domains and a work-stealing hub. Each worker runs the following loop: (1) get a task using `Ws_hub.pop_steal`; (2) if it fails, the scheduler has been killed and so the worker stops, otherwise execute the task in the context of the current worker; (3) start over.

**Blocking.** Care must be taken to block and unblock work-stealing dequeues properly. When the scheduler is killed, it is crucial that workers block their deque before stopping; otherwise, the scheduler may never terminate because of a running worker waiting forever for a response from a stopped but unblocked worker. Also, the main domain, from which tasks can be submitted externally through `run`, must unblock when it is executing tasks and block when it is not.

**Shutdown.** In `Domainslib`, scheduler shutdown consists in submitting special tasks through the main domain; when a worker finds such a task, it quickly stops. However, this simple mechanism has at least two drawbacks: (1) it introduces an indirection for every regular task, which may be expensive; (2) it works well under standard work-stealing but is more difficult to implement under other scheduling strategies, especially work-stealing with private dequeues (see [Section 7.2](#)). Consequently, we use an alternative mechanism implemented at the level of `Ws_hub`: a shared flag, regularly checked in `Ws_hub.steal` and `Ws_hub.pop_steal`, is set when the scheduler is killed.

## 11 Futures

At the fourth level, **Future**   implements futures<sup>10</sup>, a standard abstraction for representing the future result of an asynchronous task.

### 11.1 Specification

The specification is given in [Figure 11](#). It features four predicates: **inv**, **result**, **consumer** and **obligation**.

`async` also allows submitting a task asynchronously while executing under a context (**FUTURE-ASYNC-SPEC**), returning a *future* representing the result of the task. To actually get the result, one must call `wait` (**FUTURE-WAIT-SPEC**). `iter ctx fut task` attaches callback `task` to `fut` (**FUTURE-ITER-SPEC**) and `map ctx fut1 task` creates a new future to be resolved after `fut1` (**FUTURE-MAP-SPEC**).

The persistent assertion **inv** `pool t depth`  $\Psi \Xi$  represents the knowledge that `t` is a valid future attached to pool `pool` such that: (1)  $\Psi$  is the *non-persistent output predicate* satisfied by the produced value; (2)  $\Xi$  is the *persistent output predicate* satisfied by the produced value. `depth` is the depth of `t` in the forest formed by all futures.

The persistent assertion **result** `t v` represents the knowledge that future `t` has been resolved to value `v`. Using **FUTURE-INV-RESULT**, it can also be combined with **inv** to obtain the persistent

<sup>10</sup>Futures are called *promises* in `Domainslib`. In fact, the two notions are often used in conjunction to represent the two sides of the same object.



persistent (**inv** pool  $t$  depth  $\Psi \Xi$ )    persistent (**obligation** pool depth  $P$ )    persistent (**result**  $t$   $v$ )

<p>FUTURE-RESULT-AGREE</p> $\frac{\text{result } t \ v_1 \quad \text{result } t \ v_2}{v_1 = v_2}$	<p>FUTURE-INV-RESULT</p> $\frac{\text{inv pool } t \ \text{depth } \Psi \ \Xi \quad \text{result } t \ v}{\Rightarrow \triangleright \square \Xi \ v}$	<p>FUTURE-INV-FINISHED</p> $\frac{\text{inv pool } t \ \text{depth } \Psi \ \Xi \quad \text{pool.finished pool}}{\triangleright^{2 \cdot \text{depth} + 1} \Xi \ v. \text{result } t \ v}$
<p>FUTURE-CONSUMER-DIVIDE</p> $\frac{\text{inv pool } t \ \text{depth } \Psi \ \Xi \quad \text{consumer } t \ X \quad \forall v. X \ v \multimap \bigstar_{X \in X_s} X \ v}{\Rightarrow \bigstar_{X \in X_s} \text{consumer } t \ X}$	<p>FUTURE-INV-RESULT-CONSUMER</p> $\frac{\text{inv pool } t \ \text{depth } \Psi \ \Xi \quad \text{result } t \ v \quad \text{consumer } t \ X}{\Rightarrow \triangleright^2 X \ v}$	<p>FUTURE-OBLIGATION-FINISHED</p> $\frac{\text{obligation pool depth } P \quad \text{pool.finished pool}}{\triangleright^{2 \cdot \text{depth} + 2} \square P}$
<p>FUTURE-ASYNC-SPEC</p> $\frac{\text{pool.context pool ctx scope } * \quad \forall \text{ ctx scope. pool.context pool ctx scope } \multimap \left\{ \begin{array}{l} v. \text{pool.context pool ctx scope } * \\ \triangleright \Psi \ v \ * \\ \triangleright \square \Xi \ v \end{array} \right\}}{t. \text{pool.context pool ctx scope } * \quad \text{inv pool } t \ 0 \ \Psi \ \Xi \ * \quad \text{consumer } t \ \Psi}$	<p>FUTURE-WAIT-SPEC</p> $\frac{\text{pool.context pool ctx scope } * \quad \text{inv pool } t \ \text{depth } \Psi \ \Xi \quad \text{wait ctx } t}{v. \text{f } 2 \ * \quad \text{pool.context pool ctx scope } * \quad \text{result } t \ v}$	
<p>FUTURE-ITER-SPEC</p> $\frac{\text{pool.context pool ctx scope } * \quad \text{inv pool } t \ \text{depth } \Psi \ \Xi \ * \quad \forall \text{ ctx scope } v. \text{pool.context pool ctx scope } \multimap \text{result } t \ v \multimap \text{wp task ctx } v \left\{ \begin{array}{l} (). \text{pool.context pool ctx scope } * \\ \triangleright \square P \end{array} \right\}}{(). \text{pool.context pool ctx scope } * \quad \text{obligation pool depth } P}$	<p>FUTURE-MAP-SPEC</p> $\frac{\text{pool.context pool ctx scope } * \quad \text{inv pool } t_1 \ \text{depth } \Psi_1 \ \Xi_1 \ * \quad \forall \text{ ctx scope } v_1. \text{pool.context pool ctx scope } \multimap \text{result } t_1 \ v_1 \multimap \text{wp task ctx } v_1 \left\{ \begin{array}{l} v_2. \text{pool.context pool ctx scope } * \\ \triangleright \Psi_2 \ v_2 \ * \\ \triangleright \square \Xi_2 \ v_2 \end{array} \right\}}{t_2. \text{pool.context pool ctx scope } * \quad \text{inv pool } t_2 \ (\text{depth} + 1) \ \Psi_2 \ \Xi_2 \ * \quad \text{consumer } t_2 \ \Psi_2}$	

Fig. 12. **Future**: Specification

output predicate. After the pool has finished, it is guaranteed that all futures have been resolved (**FUTURE-INV-FINISHED**).

The assertion **consumer**  $t$   $X$  represents the right to consume  $X$  once future  $t$  has been resolved. Indeed, using **FUTURE-INV-RESULT-CONSUMER**, it can be combined with **inv** and **result** to obtain  $X$ .

When  $t$  is created, this assertion is produced with the full non-persistent predicate (**FUTURE-ASYNC-SPEC**, **FUTURE-MAP-SPEC**); then, it can be divided into several parts (**FUTURE-CONSUMER-DIVIDE**).

The persistent assertion **obligation**  $pool\ depth\ P$  represents a proof obligation emitted by `iter` (**FUTURE-ITER-SPEC**). It allows retrieving  $P$  once  $pool$  has finished (**FUTURE-OBLIGATION-FINISHED**).

One notable aspect of this specification is that resolution of the future — as indicated by **result** — is separated from the division of the output predicates — as achieved by **consumer**.

## 11.2 Implementation

Futures are implemented using *ivars* (concurrent write-once variables), as implemented and verified in the Zoo standard library. `async` creates an ivar and calls `async_silent` to resolve it asynchronously. `wait` calls `wait_until` to wait *actively* until the ivar is resolved and returns the resulting value. `wait_until` runs a loop similar to that of the worker domains described above; the wait is *active* in the sense that the domain participate in the execution of tasks. Consequently, `wait` and `wait_until` can be nested. This can be a problem in practice because it increases the call stack size in an arbitrary way, potentially causing stack overflow.

Instead, Domainslib leverages algebraic effects: awaiting a future captures the continuation and stores it into the future; when the future is resolved, it resubmits all the waiting tasks. This avoids any stack issue and is probably more efficient, since no polling is necessary.

## 12 Parallel iterators

On top of **Future**, we implemented and verified standard parallel iterators 🐘 🚗 that are particularly useful for benchmarks (see [Section 14](#)): `for_`, `for_each`, `fold` and `find`.

## 13 Vertex

At the fourth level, **Vertex** 🐘 🚗 implements *DAG-calculus* [Acar, Charguéraud, Rainey and Sieczkowski 2016], i.e. a task graph abstraction. Taskflow offers similar, although much more developed, abstractions. The longer term goal is to support the more practical Taskflow interface, including static, dynamic, module and condition tasks.

The raison d'être of these works is to represent more interesting dependency relations than is possible using standard parallel primitives (`fork/join`, futures, *etc.*) in order to express irregular parallel computations, e.g. those for graph problems.

This takes the form of a simple and elegant programming model: a parallel computation is seen as a graph where vertices represent basic sequential computations and edges represent dependencies between vertices. A vertex can be executed only when its predecessors, i.e. dependencies, are finished. Crucially, the structure of the graph is not static: while executing, a vertex may create new vertices and edges. Naturally, with great expressivity comes great responsibility: care must be taken not to introduce cycles in the graph, although the model does allow looping on a vertex.

### 13.1 Specification

The specification is given in [Figure 13](#). It features no less than six predicates: **inv**, **model**, **ready**, **output**, **finished** and **predecessor**.

The persistent assertion **inv**  $t\ P\ R$  represents the knowledge that  $t$  is a valid vertex;  $P$  is the *non-persistent* output while  $R$  is the *persistent* output. It is returned by `create` (**VERTEX-CREATE-SPEC**) and required by most operations.

The exclusive assertion **model**  $t\ task\ iter$  represents the ownership of vertex  $t$ . It is returned by `create` (**VERTEX-CREATE-SPEC**).  $task$  is the current computation attached to  $t$ ; it can be accessed using `task` (**VERTEX-TASK-SPEC**) and `set_task` (**VERTEX-SET-TASK-SPEC**).  $iter$  is the current *logical*

persistent ( <i>inv</i> $t$ $P$ $R$ )			persistent ( <i>ready</i> $iter$ )			persistent ( <i>finished</i> $t$ )		
			persistent ( <i>predecessor</i> $t$ $iter$ )					
						VERTEX-OUTPUT-DIVIDE		
						<i>inv</i> $t$ $P$ $R$		
						<i>output</i> $t$ $Q$		
						$Q \multimap \bigstar Q$		
						$Q \in Q_s$		
						$\Rightarrow \bigstar \text{output } t \ Q$		
						$Q \in Q_s$		
						VERTEX-INV-FINISHED-OUTPUT		
						<i>inv</i> $t$ $P$ $R$		
						<i>finished</i> $t$		
						<i>output</i> $t$ $Q$		
						$\Rightarrow \triangleright^2 Q$		
						VERTEX-CREATE-SPEC		
						True		
						create $task$		
						$t. \exists \text{ iter.}$		
						<i>inv</i> $t$ $P$ $R$ *		
						<i>model</i> $t$ (option.get (fun: $\langle \rangle \Rightarrow ()$ ) $task$ ) $iter$ *		
						<i>output</i> $t$ $P$		
						VERTEX-TASK-SPEC		
						<i>model</i> $t$ $task$ $iter$		
						task $t$		
						res. res = $task$ *		
						<i>model</i> $t$ $task$ $iter$		
						VERTEX-SET-TASK-SPEC		
						<i>model</i> $t$ $task_1$ $iter$		
						set_task $t$ $task_2$		
						$()$ . <i>model</i> $t$ $task_2$ $iter$		
						VERTEX-RELEASE-SPEC		
						pool.context $pool$ $ctx$ $scope$ *		
						<i>inv</i> $t$ $P$ $R$ *		
						<i>model</i> $t$ $task$ $iter$ *		
						$\forall \text{ pool } ctx \text{ scope } iter'.$		
						pool.context $pool$ $ctx$ $scope$ *		
						<i>ready</i> $iter$ *		
						<i>model</i> $t$ $task$ $iter'$ *		
						$\left( () . \exists \text{ task.} \right.$		
						pool.context $pool$ $ctx$ $scope$ *		
						<i>model</i> $t$ $task$ $iter'$ *		
						$\triangleright P$ *		
						$\triangleright \Box R$		
						release $ctx$ $t$		
						$()$ . pool.context $pool$ $ctx$ $scope$		

Fig. 13. **Vertex**: Specification

iteration of  $t$ . Indeed, a vertex may be executed several times; more precisely, a vertex task returns a boolean indicating whether the vertex should be re-executed.

The persistent assertion `ready iter` represents the knowledge that the iteration identified by  $iter$  has started — it may be finished and obsoleted by subsequent iterations.

The assertion `output t Q` represents the right to consume  $Q$  from the non-persistent output of  $t$  once the latter has finished executing. It is returned by `create (VERTEX-CREATE-SPEC)` with the full non-persistent output and can then be divided using `VERTEX-OUTPUT-DIVIDE`.

The persistent assertion `finished t` represents the knowledge that vertex  $t$  has finished executing. It allows retrieving both the persistent (`VERTEX-INV-FINISHED`) and non-persistent (`VERTEX-INV-FINISHED-OUTPUT`) output of  $t$ .

The persistent assertion `predecessor t iter` represents the knowledge that iteration  $iter$  has predecessor  $t$ , i.e.  $iter$  can only run once vertex  $t$  has finished (`VERTEX-PREDECESSOR-FINISHED`). It can be obtained through `precede (VERTEX-PRECEDE-SPEC)`, including while the target vertex is executing; in other words, a vertex may add dependencies to itself so that its next iteration only starts when the new dependencies have finished.

The most important operation is `release (VERTEX-RELEASE-SPEC)`, which declares a vertex ready for execution, provided that its dependencies (more precisely, those of the corresponding iteration) have finished. The current task must be shown to execute safely in any execution context given back the possession of the vertex and produce the two outputs.

## 13.2 Implementation

Our implementation is very close to that of [Acar, Charguéraud, Rainey and Sieczkowski \[2016\]](#). The representation of a vertex consists of: (1) the current task, (2) an atomic counter corresponding to the number of unfinished predecessors, (3) a closable concurrent stack from Saturn corresponding to the successors. When creating a new edge through `precede`, the target is added to the successors of the source and the counter of the target is incremented. After executing, a vertex atomically closes its successors and decrements their counter, releasing those with zero remaining predecessors.

Actually, a vertex counter does not exactly correspond to the number of predecessors. Before the vertex is released for the first time and during its execution, there is one phantom predecessor preventing premature release; it is removed by `release`.

## 14 Benchmarks

We ran several concurrent benchmarks exercising three scheduler implementations: our own Parabs scheduler, the reference Domainslib library, and its alternative Moonpool. The benchmark results validate our qualitative claim that the performance of Parabs is on par with that of Domainslib; we found that it is as efficient, and even slightly faster on some benchmarks.

Due to space constraints, we do not present our benchmark results here. They can be found in [Appendix A](#).

## 15 Related work

*Chase-Lev work-stealing deque*. [Jung, Lee, Choi, Kim, Park and Kang \[2023\]<sup>11</sup>](#) were the first to achieve foundational verification of the Chase-Lev work-stealing deque, including safe memory reclamation schemes. Before, [Lê, Pop, Cohen and Nardelli \[2013\]](#) presented a pen-and-paper proof of the correctness of an ARM implementation and [Mutluergil and Tasiran \[2019\]](#) verified an idealized implementation based on an infinite array using CIVL [\[Kragl and Qadeer 2021\]](#).

<sup>11</sup>See also the master thesis of [Choi \[2023\]](#).

As explained in Section 4, however, Jung, Lee, Choi, Kim, Park and Kang [2023] only verify a weak specification, too weak to prove the termination of our scheduler. We verify a strong specification but, contrary to Lê, Pop, Cohen and Nardelli [2013], we rely on a sequentially consistent memory model; extending our work to relaxed memory is left for future work (see Section 16).

*Parallel scheduler.* To the best of our knowledge, ParabS is the first realistic scheduler to be verified in Iris. Previous works cover toy implementations, not suitable for real-world usage; in contrast, our implementation is close to state-of-the-art schedulers and offers comparable performance according to our preliminary experiments.

De Vilhena and Pottier [2021] verify a simple cooperative scheduler based on algebraic effects, as a case study for their Iris-based program logic. This scheduler does not support parallelism; it runs fibers inside a single domain. Their notion of future/promise is rudimentary; it only supports persistent output predicates. However, their work, especially the way they formalize the scheduler's effects, will be of particular interest when introducing algebraic effects into ZooLang and ParabS.

Ebner, Martínez, Rastogi, Dardinier, Frisella, Ramananandro and Swamy [2025] verify a parallel scheduler with the same interface as Domainslib, which also serves as a case-study for their program logic. However, their implementation is extremely simplified: a task list protected by a mutex. Their notion of future/joinable is also somewhat rudimentary.

## 16 Future work

*Relaxed memory model.* The main limitation of our work is inherited from Zoo: it relies on a sequentially consistent memory model whereas OCaml 5 has a relaxed memory model [Dolan, Sivaramakrishnan and Madhavapeddy 2018]. This simplification endangers the soundness of our specifications. Transitioning to relaxed memory by merging Zoo with Cosmo [Mével and Jourdan 2021; Mével, Jourdan and Pottier 2020] involves introducing memory views, which complicates specifications and invariants.

*Language features.* ParabS suffers from the lack of a number of language features unsupported by Zoo. With functors, we could make the ParabS library completely modular. With exceptions, we could catch and re-raise exceptions in Pool and Vertex. With algebraic, we could get rid of evaluation contexts in Pool and use continuation-stealing.

*Extensions.* In the future, we would like to extend the library in several directions: (1) develop the interface of futures, similarly to Moonpool<sup>12</sup>; (2) support the different task types of Taskflow, aiming at a more practical Vertex interface.

*Other designs.* We could experiment other designs. For instance, one of the two designs of Moonpool relies on a bounded work-stealing deque combined with a master queue. In the literature, many other scheduling strategies were proposed: continuation-stealing [Schmaus, Pfeiffer, Schröder-Preikschat, Hönig and Nolte 2021; Williams and Elliott 2025], steal-half work-stealing [Hendler and Shavit 2002], split work-stealing [Cartier, Dinan and Larkins 2021; Custódio, Paulino and Rito 2023; Dinan, Larkins, Sadayappan, Krishnamoorthy and Nieplocha 2009; Rito and Paulino 2022; van Dijk and van de Pol 2014], idempotent work-stealing [Michael, Vechev and Saraswat 2009].

## References

- Umut A. Acar, Arthur Charguéraud, and Mike Rainey. 2013. Scheduling parallel programs by work stealing with private deques. In *ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming, PPoPP '13, Shenzhen, China, February 23-27, 2013*, Alex Nicolau, Xiaowei Shen, Saman P. Amarasinghe, and Richard W. Vuduc (Eds.). ACM, 219–228. doi:10.1145/2442516.2442538

<sup>12</sup><https://github.com/c-cube/moonpool/blob/main/src/core/fut.mli>

- Umüt A. Acar, Arthur Charguéraud, Mike Rainey, and Filip Sieczkowski. 2016. Dag-calculus: a calculus for parallel computation. In *Proceedings of the 21st ACM SIGPLAN International Conference on Functional Programming, ICFP 2016, Nara, Japan, September 18-22, 2016*, Jacques Garrigue, Gabriele Keller, and Eijiro Sumii (Eds.). ACM, 18–32. doi:10.1145/2951913.2951946
- Clément Allain and Gabriel Scherer. 2026. Zoo: A framework for the verification of concurrent OCaml 5 programs using separation logic. *Proc. ACM Program. Lang.* 10, POPL (2026). <https://clef-men.github.io/publications/allain-scherer-26.pdf>
- Lars Birkedal, Thomas Dinsdale-Young, Armaël Guéneau, Guilhem Jaber, Kasper Svendsen, and Nikos Tzevelekos. 2021. Theorems for free from separation logic specifications. *Proc. ACM Program. Lang.* 5, ICFP (2021), 1–29. doi:10.1145/3473586
- Robert D. Blumofe, Christopher F. Joerg, Bradley C. Kuszmaul, Charles E. Leiserson, Keith H. Randall, and Yuli Zhou. 1996. Cilk: An Efficient Multithreaded Runtime System. *J. Parallel Distributed Comput.* 37, 1 (1996), 55–69. doi:10.1006/JPDC.1996.0107
- Robert D. Blumofe and Charles E. Leiserson. 1999. Scheduling Multithreaded Computations by Work Stealing. *J. ACM* 46, 5 (1999), 720–748. doi:10.1145/324133.324234
- Hannah Cartier, James Dinan, and D. Brian Larkins. 2021. Optimizing Work Stealing Communication with Structured Atomic Operations. In *ICPP 2021: 50th International Conference on Parallel Processing, Lemont, IL, USA, August 9 - 12, 2021*, Xian-He Sun, Sameer Shende, Laxmikant V. Kalé, and Yong Chen (Eds.). ACM, 36:1–36:10. doi:10.1145/3472456.3472522
- David Chase and Yossi Lev. 2005. Dynamic circular work-stealing deque. In *SPAA 2005: Proceedings of the 17th Annual ACM Symposium on Parallelism in Algorithms and Architectures, July 18-20, 2005, Las Vegas, Nevada, USA*, Phillip B. Gibbons and Paul G. Spirakis (Eds.). ACM, 21–28. doi:10.1145/1073970.1073974
- Jaemin Choi. 2023. Formal Verification of Chase-Lev Deque in Concurrent Separation Logic. *CoRR* abs/2309.03642 (2023). arXiv:2309.03642 doi:10.48550/ARXIV.2309.03642
- Simon Cruanes. 2025. Moonpool. <https://github.com/c-cube/moonpool>
- Rafael Custódio, Hervé Paulino, and Guilherme Rito. 2023. Efficient Synchronization-Light Work Stealing. In *Proceedings of the 35th ACM Symposium on Parallelism in Algorithms and Architectures, SPAA 2023, Orlando, FL, USA, June 17-19, 2023*, Kunal Agrawal and Julian Shun (Eds.). ACM, 39–49. doi:10.1145/3558481.3591099
- Pedro da Rocha Pinto, Thomas Dinsdale-Young, and Philippa Gardner. 2014. TaDA: A Logic for Time and Data Abstraction. In *ECOOP 2014 - Object-Oriented Programming - 28th European Conference, Uppsala, Sweden, July 28 - August 1, 2014. Proceedings (Lecture Notes in Computer Science, Vol. 8586)*, Richard E. Jones (Ed.). Springer, 207–231. doi:10.1007/978-3-662-44202-9\_9
- Paulo Emilio de Vilhena and Francois Pottier. 2021. A separation logic for effect handlers. *Proc. ACM Program. Lang.* 5, POPL (2021), 1–28. doi:10.1145/3434314
- James Dinan, D. Brian Larkins, P. Sadayappan, Sriram Krishnamoorthy, and Jarek Nieplocha. 2009. Scalable work stealing. In *Proceedings of the ACM/IEEE Conference on High Performance Computing, SC 2009, November 14-20, 2009, Portland, Oregon, USA*. ACM. doi:10.1145/1654059.1654113
- Stephen Dolan, K. C. Sivaramakrishnan, and Anil Madhavapeddy. 2018. Bounding data races in space and time. In *Proceedings of the 39th ACM SIGPLAN Conference on Programming Language Design and Implementation, PLDI 2018, Philadelphia, PA, USA, June 18-22, 2018*, Jeffrey S. Foster and Dan Grossman (Eds.). ACM, 242–255. doi:10.1145/3192366.3192421
- Brijesh Dongol and John Derrick. 2014. Verifying linearizability: A comparative survey. *CoRR* abs/1410.6268 (2014). arXiv:1410.6268 <http://arxiv.org/abs/1410.6268>
- Gabriel Ebner, Guido Martínez, Aseem Rastogi, Thibault Dardinier, Megan Frisella, Tahina Ramananandro, and Nikhil Swamy. 2025. PulseCore: An Impredicative Concurrent Separation Logic for Dependently Typed Programs. *Proc. ACM Program. Lang.* 9, PLDI (2025), 1516–1539. doi:10.1145/3729311
- Matteo Frigo, Charles E. Leiserson, and Keith H. Randall. 1998. The Implementation of the Cilk-5 Multithreaded Language. In *Proceedings of the ACM SIGPLAN '98 Conference on Programming Language Design and Implementation (PLDI), Montreal, Canada, June 17-19, 1998*, Jack W. Davidson, Keith D. Cooper, and A. Michael Berman (Eds.). ACM, 212–223. doi:10.1145/277650.277725
- Danny Hendler and Nir Shavit. 2002. Non-blocking steal-half work queues. In *Proceedings of the Twenty-First Annual ACM Symposium on Principles of Distributed Computing, PODC 2002, Monterey, California, USA, July 21-24, 2002*, Aletta Ricciardi (Ed.). ACM, 280–289. doi:10.1145/571825.571876
- Maurice Herlihy and Jeannette M. Wing. 1990. Linearizability: A Correctness Condition for Concurrent Objects. *ACM Trans. Program. Lang. Syst.* 12, 3 (1990), 463–492. doi:10.1145/78969.78972
- Tsung-Wei Huang, Dian-Lun Lin, Chun-Xun Lin, and Yibo Lin. 2022. Taskflow: A Lightweight Parallel and Heterogeneous Task Graph Computing System. *IEEE Trans. Parallel Distributed Syst.* 33, 6 (2022), 1303–1320. doi:10.1109/TPDS.2021.3104255
- Jaehwang Jung, Janggun Lee, Jaemin Choi, Jaewoo Kim, Sunho Park, and Jeehoon Kang. 2023. Modular Verification of Safe Memory Reclamation in Concurrent Separation Logic. *Proc. ACM Program. Lang.* 7, OOPSLA2 (2023), 828–856. doi:10.1145/3622827



- Ralf Jung, Robbert Krebbers, Jacques-Henri Jourdan, Ales Bizjak, Lars Birkedal, and Derek Dreyer. 2018. Iris from the ground up: A modular foundation for higher-order concurrent separation logic. *J. Funct. Program.* 28 (2018), e20. doi:10.1017/S0956796818000151
- Ralf Jung, Rodolphe Lepigre, Gaurav Parthasarathy, Marianna Rapoport, Amin Timany, Derek Dreyer, and Bart Jacobs. 2020. The future is ours: prophecy variables in separation logic. *Proc. ACM Program. Lang.* 4, POPL (2020), 45:1–45:32. doi:10.1145/3371113
- Vesa Karvonen and Carine Morel. 2025. Saturn. <https://github.com/ocaml-multicore/saturn>
- Bernhard Kragl and Shaz Qadeer. 2021. The Civi Verifier. In *Formal Methods in Computer Aided Design, FMCAD 2021, New Haven, CT, USA, October 19-22, 2021*. IEEE, 143–152. doi:10.34727/2021/ISBN.978-3-85448-046-4\_23
- Robbert Krebbers, Jacques-Henri Jourdan, Ralf Jung, Joseph Tassarotti, Jan-Oliver Kaiser, Amin Timany, Arthur Charguéraud, and Derek Dreyer. 2018. MoSeL: a general, extensible modal framework for interactive proofs in separation logic. *Proc. ACM Program. Lang.* 2, ICFP (2018), 77:1–77:30. doi:10.1145/3236772
- Nhat Minh Lê, Antoniu Pop, Albert Cohen, and Francesco Zappa Nardelli. 2013. Correct and efficient work-stealing for weak memory models. In *ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming, PPoPP '13, Shenzhen, China, February 23-27, 2013*, Alex Nicolau, Xiaowei Shen, Saman P. Amarasinghe, and Richard W. Vuduc (Eds.). ACM, 69–80. doi:10.1145/2442516.2442524
- Glen Mével and Jacques-Henri Jourdan. 2021. Formal verification of a concurrent bounded queue in a weak memory model. *Proc. ACM Program. Lang.* 5, ICFP (2021), 1–29. doi:10.1145/3473571
- Glen Mével, Jacques-Henri Jourdan, and Francois Pottier. 2020. Cosmo: a concurrent separation logic for multicore OCaml. *Proc. ACM Program. Lang.* 4, ICFP (2020), 96:1–96:29. doi:10.1145/3408978
- Maged M. Michael, Martin T. Vechev, and Vijay A. Saraswat. 2009. Idempotent work stealing. In *Proceedings of the 14th ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming, PPoPP 2009, Raleigh, NC, USA, February 14-18, 2009*, Daniel A. Reed and Vivek Sarkar (Eds.). ACM, 45–54. doi:10.1145/1504176.1504186
- Multicore OCaml development team. 2025. Domainlib. <https://github.com/ocaml-multicore/domainlib>
- Suha Orhun Mutluergil and Serdar Tasiran. 2019. A mechanized refinement proof of the Chase-Lev deque using a proof system. *Computing* 101, 1 (2019), 59–74. doi:10.1007/S00607-018-0635-4
- Peter W. O'Hearn. 2007. Resources, concurrency, and local reasoning. *Theor. Comput. Sci.* 375, 1-3 (2007), 271–307. doi:10.1016/J.TCS.2006.12.035
- Peter W. O'Hearn, John C. Reynolds, and Hongseok Yang. 2001. Local Reasoning about Programs that Alter Data Structures. In *Computer Science Logic, 15th International Workshop, CSL 2001. 10th Annual Conference of the EACSL, Paris, France, September 10-13, 2001, Proceedings (Lecture Notes in Computer Science, Vol. 2142)*, Laurent Fribourg (Ed.). Springer, 1–19. doi:10.1007/3-540-44802-0\_1
- John C. Reynolds. 2002. Separation Logic: A Logic for Shared Mutable Data Structures. In *17th IEEE Symposium on Logic in Computer Science (LICS 2002), 22-25 July 2002, Copenhagen, Denmark, Proceedings*. IEEE Computer Society, 55–74. doi:10.1109/LICS.2002.1029817
- Guilherme Rito and Hervé Paulino. 2022. Scheduling computations with provably low synchronization overheads. *J. Sched.* 25, 1 (2022), 107–124. doi:10.1007/S10951-021-00706-6
- Florian Schmaus, Nicolas Pfeiffer, Wolfgang Schröder-Preikschat, Timo Hönig, and Jörg Nolte. 2021. Nowa: A Wait-Free Continuation-Stealing Concurrency Platform. In *35th IEEE International Parallel and Distributed Processing Symposium, IPDPS 2021, Portland, OR, USA, May 17-21, 2021*. IEEE, 360–371. doi:10.1109/IPDPS49936.2021.00044
- K. C. Sivaramakrishnan, Stephen Dolan, Leo White, Sadiq Jaffer, Tom Kelly, Anmol Sahoo, Sudha Parimala, Atul Dhiman, and Anil Madhavapeddy. 2020. Retrofitting parallelism onto OCaml. *Proc. ACM Program. Lang.* 4, ICFP (2020), 113:1–113:30. doi:10.1145/3408995
- K. C. Sivaramakrishnan, Stephen Dolan, Leo White, Tom Kelly, Sadiq Jaffer, and Anil Madhavapeddy. 2021. Retrofitting effect handlers onto OCaml. In *PLDI '21: 42nd ACM SIGPLAN International Conference on Programming Language Design and Implementation, Virtual Event, Canada, June 20-25, 2021*, Stephen N. Freund and Eran Yahav (Eds.). ACM, 206–221. doi:10.1145/3453483.3454039
- Tom van Dijk and Jaco C. van de Pol. 2014. Lace: Non-blocking Split Deque for Work-Stealing. In *Euro-Par 2014: Parallel Processing Workshops - Euro-Par 2014 International Workshops, Porto, Portugal, August 25-26, 2014, Revised Selected Papers, Part II (Lecture Notes in Computer Science, Vol. 8806)*, Luís M. B. Lopes, Julius Zilinskas, Alexandru Costan, Roberto G. Cascella, Gabor Kecsckemeti, Emmanuel Jeannot, Mario Cannataro, Laura Ricci, Siegfried Benkner, Salvador Petit, Vittorio Scarano, José Gracia, Sascha Hunold, Stephen L. Scott, Stefan Lankes, Christian Lengauer, Jesús Carretero, Jens Breitbart, and Michael Alexander (Eds.). Springer, 206–217. doi:10.1007/978-3-319-14313-2\_18
- Conor John Williams and James Elliott. 2025. Libfork: Portable Continuation-Stealing With Stackless Coroutines. *IEEE Trans. Parallel Distributed Syst.* 36, 5 (2025), 877–888. doi:10.1109/TPDS.2025.3543442



## A Benchmarks

In this section, we present simple benchmarks to assess the performance of Parabs relatively to Domainslib [Multicore OCaml development team 2025] and Moonpool [Cruanes 2025] on simple workloads. Benchmarking parallel schedulers is subtle and difficult; we have not tried here to validate and study experimentally all our implementation choices, or to cover the wide range of parallel workloads, but to validate a simple qualitative claim:

For CPU-bound tasks, Parabs has comparable throughput to Domainslib, a state-of-the-art scheduler used in production in the OCaml 5 library ecosystem.

In fact our results validate a stronger qualitative claim: the performance of Parabs are equal or better than Domainslib, with a 10% speedup in some cases.

### A.1 Setting

*A.1.1 Machine.* The benchmark results were produced on a 12-core AMD Ryzen 5 7640U machine, set at a fixed frequency of 2GHz.

*A.1.2 Parameters.* For each benchmark, we pick an input parameter that gives long-enough computation times on our test machine, typically between 200ms and 2s. We use the hyperfine tool and run each benchmark ten time. All benchmark were run with two parameters varying:

- DOMAINS, the number of domains used for computation;
- CUTOFF, representing an input size or chunk size below which a sequential baseline is used.

For each benchmark, we show:

- per-cutoff results with a fixed value DOMAINS = 6, which should be enough to experience scaling issues while not suffering from CPU contention;
- per-domain results with a CUTOFF value that is chosen to work well for all implementations for this benchmark.

Remark: Large cutoff values tend to work well for benchmarks with homogeneous-enough tasks, as they effectively amortize the scheduling costs. The advantage of having schedulers that also perform well on small cutoffs are two-fold. First, this typically indicate that they will adapt to irregular tasks (but: our benchmarks do not perform an in-depth exploration of irregular workloads). Second, this can alleviate the burden of asking users to choose cutoff sizes (by widening the range of values that perform well), an activity which requires cumbersome hand-tuning and can limit performance portability.

*A.1.3 Scheduler implementations.* Each benchmark is written on top of a simple scheduler interface, for which the following implementations are provided:

- domainslib uses the Domainslib library;
- parabs uses our Parabs library;
- moonpool-fifo uses the Moonpool scheduler with a global FIFO queue of task;
- moonpool-ws uses the Moonpool scheduler with a work-stealing pool of tasks, which is described as better for throughput
- sequential is a baseline implementation with no parallelism, all tasks run sequentially on a single domain.

We used the latest software versions currently available: Domainslib 0.5.2, and Moonpool 0.9.

#### A.1.4 Benchmarks.

*fibonacci* 🐘. A parallel implementation of Fibonacci extended with a sequential cutoff: below the cutoff value, a sequential implementation is used.

`iota` 🐘. This benchmark uses a parallel-for to write a default value in each cell of an array. We expect significant variations due to the CUTOFF parameter.

`for_irregular` 🐘. This benchmark uses a parallel-for loop with irregular per-element workload: as a first approximation, the  $i$ -th iteration computes fibonacci  $i$ ; this cost grows exponentially in  $i$ , so the majority of computation work is concentrated on the largest loop indices.

`lu` 🐘. This benchmark performs the LU factorization of a random matrix of floating-point values. It consists in  $O(N)$  repetitions of a parallel-for loop of  $O(N)$  iterations, where each iteration performs  $O(N)$  sequential work.

`matmul` 🐘. This benchmark computes matrix multiplication with a very simple parallelization strategy — only the outer loop is parallelized. In other word, there is a parallel-for loop with  $O(N)$  iterations, where each iteration performs  $O(N^2)$  sequential work work.

## A.2 Results

*A.2.1 Pre-benchmarking expectations.* Our expectation before running the benchmarks is that Parabs has the same performance as Domainslib, and that they are both more efficient than Moonpool (which uses a central pool of jobs instead of per-domain deque).

Because Moonpool has a less optimized scheduler, we expect scheduling overhead to be an issue for small CUTOFF values.

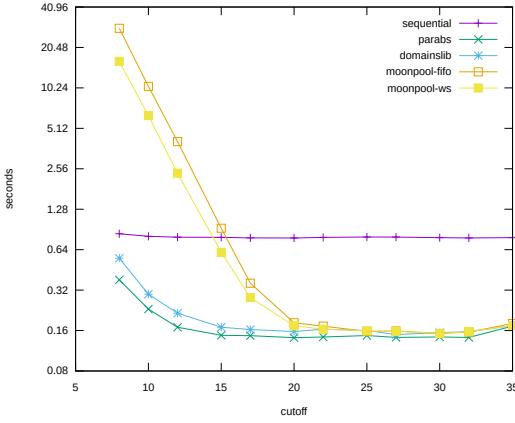
On all schedulers, the performance for larger CUTOFF values should be good if the benchmark has homogeneous/regular tasks, and it should be worse if the benchmark has heterogeneous/irregular tasks.

*A.2.2 Per-benchmark results.* Figure 14 and Figure 14 contain the full results, with per-cutoff and per-domain plots for each benchmarks. Notice that while the per-domain plot always use linear axes, the per-cutoff plots often use logarithmic plot axes, to preserve readability when performance difference become very large for small cutoff values, and to express large ranges of possible cutoff choices.

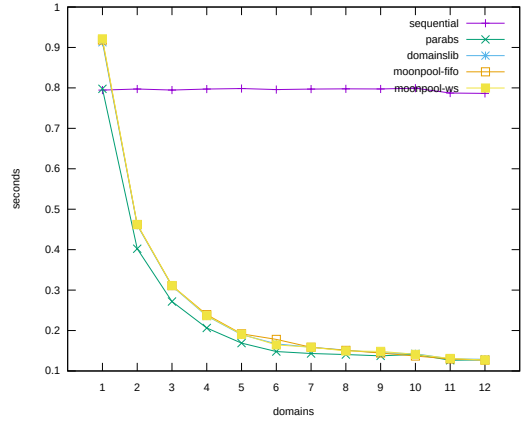
`fibonacci`. In the per-cutoff results (logarithmic scale), we see that all schedulers start to behave badly when the CUTOFF becomes small enough, with exponentially-decreasing performance after a certain drop point. For Moonpool, performance drops around CUTOFF = 20. The FIFO and work-stealing variants have similar profiles, with work-stealing performing noticeably better. For Parabs and Domainslib, performance drops around CUTOFF = 12. Parabs performs noticeably better for small-enough cutoff values. In fact, even for the sequential scheduler we observe a small performance drop: the task-using version creates closures and performs indirect calls, so it is noticeably slower (by a constant factor) than the version used below the sequential cutoff.

Note: we observe very large memory usage with Moonpool at smaller cutoff values — when computing fibonacci 40, attempting to run the benchmark with CUTOFF = 5 fails with out-of-memory errors on a machine with 32Gio of RAM. This seems to come from the FIFO architecture which runs the oldest and thus biggest task first, and thus stores an exponential number of smaller tasks in the queue.

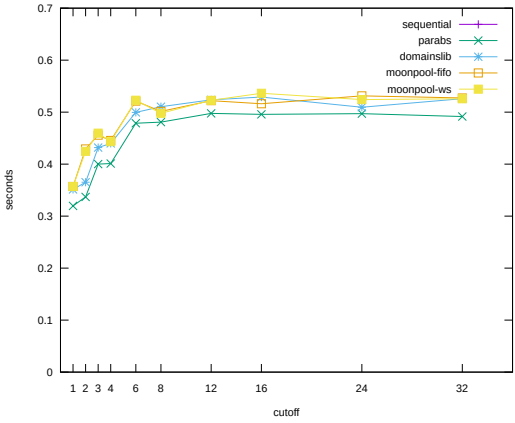
Per-domain results (linear scale): we studied per-domain performance on a CUTOFF = 25 point where all implementations behave well. For this value we see that parabs and domainslib perform similarly, and both moonpool implementations are measurably slower. Performance becomes very close for larger number of domains (DOMAINS  $\geq 7$ ).



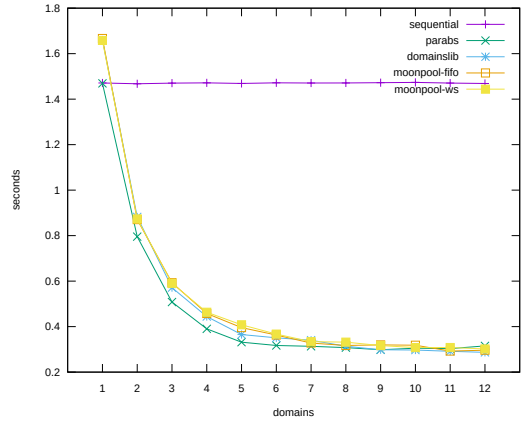
(a) fibonacci: varying CUTOFF



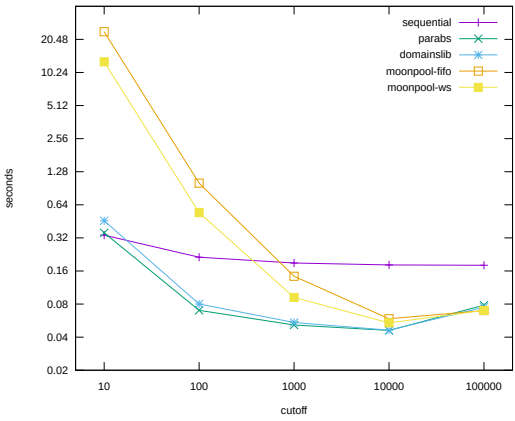
(b) fibonacci: varying DOMAINS



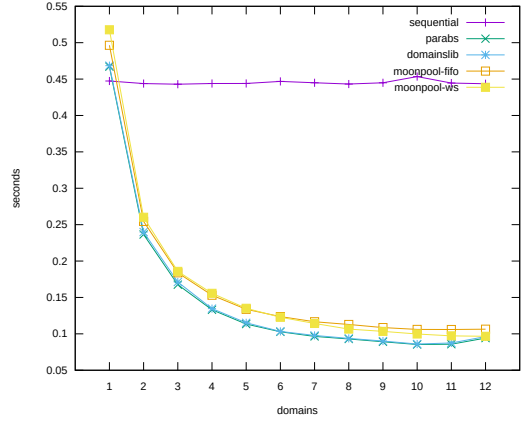
(c) for\_irregular: varying CUTOFF



(d) for\_irregular: varying DOMAINS

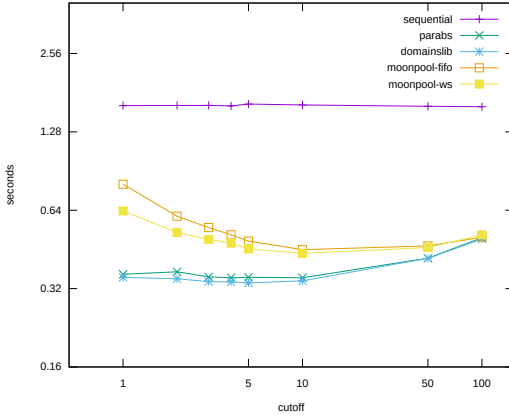


(e) iota: varying CUTOFF

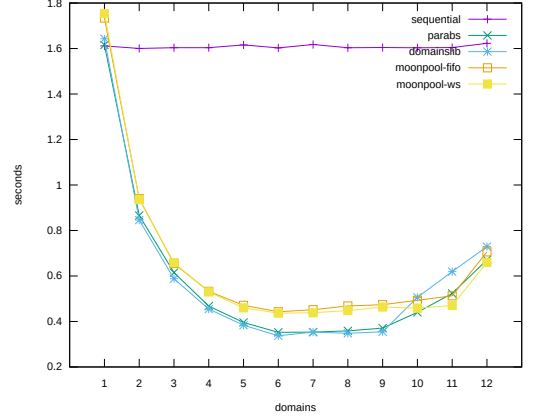


(f) iota: varying DOMAINS

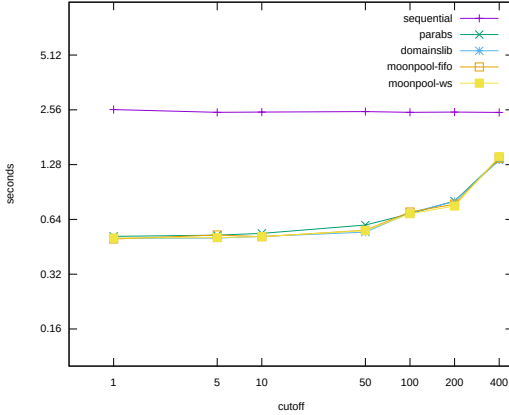
Fig. 14. Benchmarks (1/2)



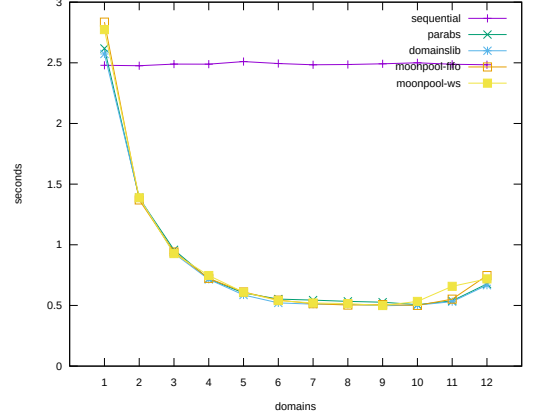
(g) lu: varying CUTOFF



(h) lu: varying DOMAINS



(i) matmul: varying CUTOFF



(j) matmul: varying DOMAINS

Fig. 14. Benchmarks (2/2)

for\_irregular. This benchmark is designed to behave poorly with large CUTOFF values. We indeed observe better noticeably performance with CUTOFF = 1 than with larger values, across all schedulers — for example domainslib is 50% slower with CUTOFF = 8.

In the per-cutoff results we observe that parabs performs best on this benchmark, then domainslib, then moonpool.

In the per-domain results (with CUTOFF = 1) we see that parabs performs noticeably better than the other implementations for relatively low domain counts, and they become comparable around DOMAINS  $\geq 7$ .

iota. Each iteration of parallel-for in iota is immediate, so as expected we observe a large sensitivity to the choice of CUTOFF, with parabs and domainslib performing much better than moonpool on smaller CUTOFF values.

In the per-domains result we see that domainslib and parabs have similar performance, noticeably better than the moonpool implementations.

1u. The performance is relatively stable over most choices of CUTOFF. The per-domain results are similar across all benchmarks after controlling for the one-domain shift of Moonpool.

Remark: we observe a marked decline in performance, across all schedulers, when the number of domains becomes close to the number of available cores, around  $\text{DOMAINS} \geq 10$ . We believe that this comes from the high-allocation rate of this benchmark (10.2GiB/s) causing frequent minor collections, and thus stop-the-world pauses, with some domains temporarily suspended by the operating system. In other words, the slowdown comes from the OCaml runtime, not from the scheduler implementations. The allocations can be avoided in this benchmark by optimizing more aggressively to eliminate float boxing, but this phenomenon is likely to occur for other high-allocation OCaml programs so we chose to preserve it.

matmul. The performance is stable across a wide range of CUTOFF values. The parallel-loop performs 500 iterations, so CUTOFF values closer to 500 prevent parallelization and bring performance closer to the sequential scheduler.

The per-domain performance is remarkably similar under all schedulers: our implementation of matrix multiplication has a coarse-grained parallelization strategy where the choice of scheduler makes no difference.

*A.2.3 Result summary.* Overall, Parabs has the same qualitative performance as Domainslib. In fact it performs measurably better (around 10% better for some domain values) on the benchmarks fibonacci and for\_irregular, which have irregular tasks; and it has qualitatively the same performance otherwise.