

Implementing the masterplan: OCaml

Clément Allain

February 18, 2025

The Iris religion



Iris masterplan

Zoology

Physical equality

Specimen: Kcas (ongoing work)

Future work

The prophecy (Iris masterplan)

We shall verify everything with Iris.
(December 2023)

So I went verifying everything...
in OCaml.



J. J. the prophet

Verifying everything in OCaml

- ▶ **Verifying (program transformations from) the compiler**
 - ▶ *Tail Modulo Cons, OCaml, and Relational Separation Logic*
- ▶ **Verifying (tricky) programs & libraries**
 - ▶ semantics, program logic
 - ▶ sequential algorithms: Store
 - ▶ concurrent (lock-free) algorithms: Saturn, Kcas
- ▶ **Verifying (parts of) the runtime system**
 - ▶ Boxroot

Iris masterplan

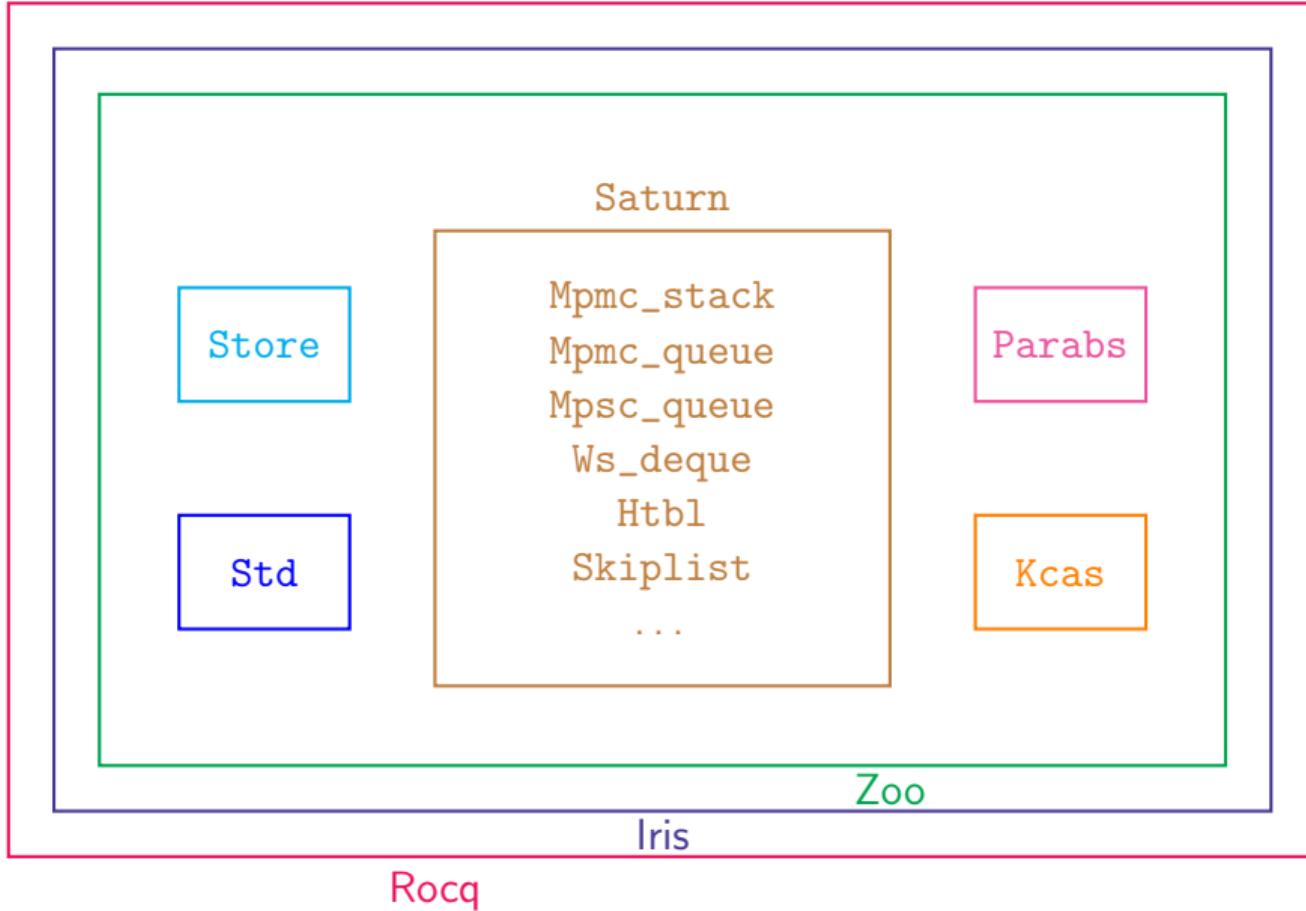
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The big picture



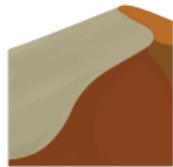
In search of a verification language

language	concurrency	Iris	\simeq OCaml	traduction	automation
Cameleer	:(:(:)	:)	:(
coq_of_ocaml	:(:(:)	:)	:(
CFML	:(:(:)	:)	:(
Osiris	:(:)	:)	:)	:(
HeapLang	:)	:)	:(:(:(
Zoo	:)	:)	:)	:)	:(

Zoo in practice



OCaml



DUNE

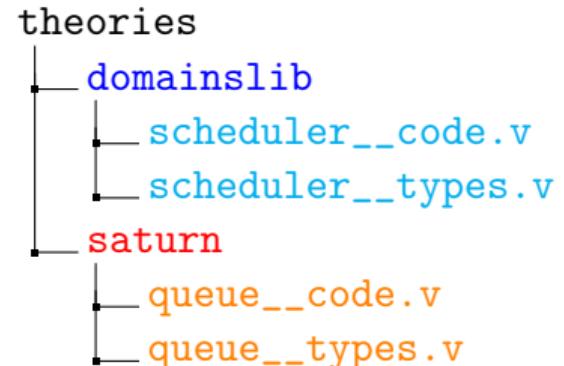
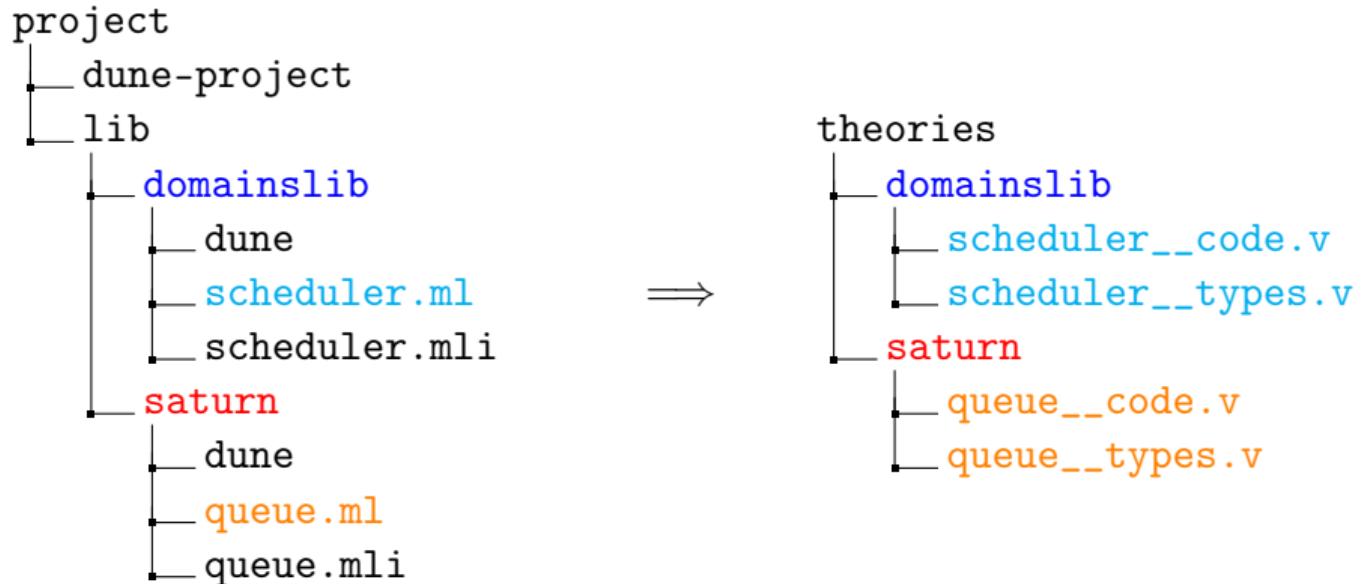
ocaml2zoo
→



Zoo



Zoo in practice



```
$ ocaml2zoo project theories
```

Zoo in practice

```
Lemma stack_push_spec_seq t  $\iota$  v :  
{{{  
  stack_model t vs  
}}}  
  stack_push t v  
{{{  
  RET ();  
  stack_model t (v :: vs)  
}}}.  
Proof.  
...  
Qed.
```

```
Lemma stack_push_spec_atomic t  $\iota$  v :  
<<<  
  stack_inv t  $\iota$   
|  $\forall$  vs,  
  stack_model t vs  
>>>  
  stack_push t v @  $\uparrow$  $\iota$   
<<<  
  stack_model t (v :: vs)  
| RET (); True  
>>>.  
Proof.  
...  
Qed.
```

Algebraic data types

```
type 'a t =
| Nil
| Cons of 'a * 'a t

let rec map fn t =
  match t with
  | Nil -> Nil
  | Cons (x, t) ->
    let y = fn x in
    Cons (y, map fn t)
```

```
Notation "'Nil'" := (
  in_type "t" 0
)(in custom zoo_tag).

Notation "'Cons'" := (
  in_type "t" 1
)(in custom zoo_tag).
```

```
Definition map : val :=
rec: "map" "fn" "t" =>
  match: "t" with
  | Nil => §Nil
  | Cons "x" "t" =>
    let: "y" := "fn" "x" in
    'Cons( "y", "map" "fn" "t" )
end.
```

Records

```
type 'a t =
{ mutable f1: 'a;
  mutable f2: 'a;
}
```

```
let swap t =
  let f1 = t.f1 in
  t.f1 <- t.f2 ;
  t.f2 <- f1
```

```
Notation "'f1'" := (
  in_type "t" 0
)(in custom zoo_field).
Notation "'f2'" := (
  in_type "t" 1
)(in custom zoo_field).
```

```
Definition swap : val :=
  fun: "t" =>
    let: "f1" := "t".{f1} in
    "t" <-{f1} "t".{f2} ;;
    "t" <-{f2} "f1".
```

Inline records

```
type 'a node =
| Null
| Node of
  { mutable next: 'a node;
    mutable data: 'a;
  }
```

```
Notation "'Null'" := (
  in_type "node" 0
)(in custom zoo_tag).
Notation "'Node'" := (
  in_type "node" 1
)(in custom zoo_tag).

Notation "'next'" := (
  in_type "node__Node" 0
)(in custom zoo_field).
Notation "'data'" := (
  in_type "node__Node" 1
)(in custom zoo_field).
```

Mutually recursive functions

```
Definition f_g := (
  recs: "f" "x" => "g" "x"
  and:  "g" "x" => "f" "x"
)%zoo_recs.

(* boilerplate *)

let rec f x = g x
and g x = f x

Definition f := ValRecs 0 f_g.
Definition g := ValRecs 1 f_g.

Instance : AsValRecs' f 0 f_g [f;g].
Proof. done. Qed.
Instance : AsValRecs' g 1 f_g [f;g].
Proof. done. Qed.
```

Concurrency

`Atomic.set e1 e2`

$e_1 \leftarrow e_2$

`Atomic.exchange e1 e2`

Xchg $e_1.[\text{contents}] e_2$

`Atomic.compare_and_set e1 e2 e3`

CAS $e_1.[\text{contents}] e_2 e_3$

`Atomic.fetch_and_add e1 e2`

FAA $e_1.[\text{contents}] e_2$

`type t = { ...; mutable f: τ[@atomic]; ... }`

`Atomic.Loc.exchange [%atomic.loc e1.f] e2`

Xchg $e_1.[f] e_2$

`Atomic.Loc.compare_and_set [%atomic.loc e1.f] e2 e3`

CAS $e_1.[f] e_2 e_3$

`Atomic.Loc.fetch_and_add [%atomic.loc e1.f] e2`

FAA $e_1.[f] e_2$

<https://github.com/ocaml/ocaml/pull/13404>

<https://github.com/ocaml/ocaml/pull/13707>

Standard library

- ▶ Array
- ▶ Dynarray
- ▶ List
- ▶ Stack
- ▶ Queue
- ▶ Deque
- ▶ Domain
- ▶ Atomic_array
- ▶ Mutex
- ▶ Condition

Iris masterplan

Zoology

Physical equality

Specimen: Kcas (ongoing work)

Future work

Physical equality: Treiber stack

```
type 'a t =
  'a list Atomic.t

let create () =
  Atomic.make []

let rec push t v =
  let old = Atomic.get t in
  let new_ = v :: old in
  if not @@ Atomic.compare_and_set t old new_ then (
    Domain.cpu_relax ();
    push t v
  )
```

Sharing

```
let test1 = Some 0 == Some 0 (* true *)
let test2 = [0;1] == [0;1] (* true *)
```

Value representation conflicts

```
let test1 = Obj.repr false == Obj.repr 0 (* true *)
let test2 = Obj.repr None == Obj.repr 0 (* true *)
let test3 = Obj.repr [] == Obj.repr 0 (* true *)
```

Sharing + conflicts

```
type any =
  Any : 'a -> any

let test1 = Any false == Any 0 (* true *)
let test2 = Any None   == Any 0 (* true *)
let test3 = Any []     == Any 0 (* true *)
```

Back to Treiber stack

```
let rec push t v =
  let old = Atomic.get t in
  let new_ = v :: old in
  if not @@ Atomic.compare_and_set t old new_ then (
    Domain.cpu_relax () ;
    push t v
  )
```

Physical equality: Eio.Rcfd

```
type state = Open of Unix.file_descr | Closing of (unit -> unit)
type t = { mutable ops: int [@atomic]; mutable state: state [@atomic] }

let make fd = { ops= 0; state= Open fd }

let closed = Closing (fun () -> ())
let close t =
  match t.state with
  | Closing _ -> false
  | Open fd as prev ->
    let close () = Unix.close fd in
    let next = Closing close in
    if Atomic.Loc.compare_and_set [%atomic.loc t.state] prev next then
      ...
    else
      false
```

Unsharing

```
let x = Some 0  
let test = x == x (* false *)
```



Clément Allain
Impossible! Unique identity.



Armaël Guéneau
This would be unsharing.



Vincent Lviron
It's possible!

Back to Eio.Rcfd

```
let closed = Closing (fun () -> ())
let close t =
  match t.state with
  | Closing _ -> false
  | Open fd as prev ->
    let close () = Unix.close fd in
    let next = Closing close in
    if Atomic.Loc.compare_and_set [%atomic.loc t.state] prev next then
      ...
    else
      false
```

Generative constructors

```
type 'a list =
| Nil
| Cons of 'a * 'a list [@generative]

type state =
| Open of Unix.file_descr [@generative] [@zoo.reveal]
| Closing of (unit -> unit)
```

Iris masterplan

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Future work

Kcas: software transactional memory for OCaml

```
let a = Loc.make 10 in
let b = Loc.make 52 in
let x = Loc.make 0 in

let tx ~xt =
    let a = Xt.get ~xt a in
    let b = Xt.get ~xt b in
    Xt.set ~xt x (b - a)
in

Xt.commit { tx }
```

Kcas: software transactional memory for OCaml

```
type ('k, 'v) cache =
{ space: int Loc.t;
  table: ('k, 'k Dllist.Xt.node * 'v) Hashtbl.Xt.t;
  order: 'k Dllist.Xt.t;
}
```

MCAS

```
let a = Loc.make 10 in  
let b = Loc.make 52 in  
let x = Loc.make 0 in
```

let a = Xt.get ~xt a in	CAS (a, 10, 10)
let b = Xt.get ~xt b in	CAS (b, 52, 52)
Xt.set ~xt x (b - a)	CAS (x, 0, 42)

MCAS specification

$$\frac{\frac{\frac{\left\{ \begin{array}{c} * \\ \ell \in ls \end{array} \text{ loc-inv } \ell \ i \right\}}{\left\langle \forall vs. \begin{array}{c} * \\ \ell, v \in ls, vs \end{array} \ell \rightarrowtail v \right\rangle}}{\text{mcas } ls \text{ } \textcolor{red}{b} \text{efores } \textcolor{green}{afters}, \uparrow i}}{\left\langle \begin{array}{l} \text{if } b \text{ then } vs = \textcolor{red}{b} \text{efores } * \\ \text{else } \exists i. vs_i \neq \textcolor{red}{b} \text{efores}_i \end{array} \begin{array}{c} * \\ \ell, v \in ls, \textcolor{green}{afters} \end{array} \ell \rightarrowtail v \right\rangle} \frac{}{\{ b. \text{True} \}}$$

MCAS specification: taking physical equality seriously

$$\frac{\frac{\frac{\left\{ \begin{array}{c} * \text{ loc-inv } \ell \ i \\ \ell \in ls \end{array} \right\}}{\left\langle \forall vs. \begin{array}{c} * \text{ } \ell \rightarrowtail v \\ \ell, v \in ls, vs \end{array} \right\rangle} \text{ mcas } ls \text{ before } afters, \uparrow i}{\left\langle \begin{array}{c} \text{if } b \text{ then } vs \approx \text{before} * \begin{array}{c} * \text{ } \ell \rightarrowtail v \\ \ell, v \in ls, \text{afters} \end{array} \\ \text{else } \exists i. vs_i \not\approx \text{before}_i * \begin{array}{c} * \text{ } \ell \rightarrowtail v \\ \ell, v \in ls, vs \end{array} \end{array} \right\rangle} \{ b. \text{ True} \}}$$

MCAS specification: read-only locations

$$\frac{\left\{ \begin{array}{c} \text{loc-inv } \ell \hookrightarrow v * \text{ loc-inv } \ell \hookrightarrow v \\ \ell \in ls \end{array} \right\} \quad \left\langle \forall ws, vs. \begin{array}{c} \text{loc-inv } \ell \hookrightarrow v * \text{ loc-inv } \ell \hookrightarrow v \\ \ell, v \in ls, ws \quad \ell, v \in ls, vs \end{array} \right\rangle}{\text{mcas } ls \text{ } ls \text{ } \textcolor{red}{b}efores \text{ } \textcolor{green}{a}fters, \uparrow \iota}$$

$$\frac{\left\langle \begin{array}{c} \text{if } b \text{ then } vs \approx \textcolor{red}{b}efores * \text{ loc-inv } \ell \hookrightarrow v \\ \exists b. \text{ loc-inv } \ell \hookrightarrow v * \text{ loc-inv } \ell \hookrightarrow v \\ \ell, v \in ls, ws \end{array} \right. \quad \left. \begin{array}{c} \text{else } \text{ loc-inv } \ell \hookrightarrow v \\ \ell, v \in ls, vs \end{array} \right\rangle}{\{ b. \text{ True } \}}$$

MCAS specification: relaxed memory

$$\frac{
 \left\{ \exists W * \underset{\ell \in ls}{*} \text{loc-inv } \ell \iota \right\}
 }{
 \left\langle \forall vs, Vs. \underset{\ell, v, V \in ls, vs, Vs}{*} \ell \mapsto (v, V) \right\rangle
 }$$

$$\frac{
 \text{mcas } ls \text{ } before \text{ } afters, \uparrow \iota
 }{
 \left\langle \begin{array}{l}
 \text{if } b \text{ then } vs \approx before * \underset{\ell, v, V \in ls, afters, vs}{*} \ell \mapsto (v, V \sqcup W) \\
 \text{else } \exists i. vs_i \not\approx before_i * \underset{\ell, v, V \in ls, vs, Vs}{*} \ell \mapsto (v, V)
 \end{array} \right\rangle
 }$$

$$\left\{ b. \text{if } b \text{ then } \underset{v \in vs}{*} \exists V \text{ else True} \right\}$$

MCAS algorithm: Harris, Fraser & Pratt (2002)

A Practical Multi-Word Compare-and-Swap Operation

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Abstract. Work on non-blocking data structures has proposed extending processor designs with a compare-and-swap primitive, CAS2, which acts on two arbitrary memory locations. Experience suggested that current operations, typically single-word compare-and-swap (CAS1), are not expressive enough to be used alone in an efficient manner. In this paper we build CAS2 from CAS1 and, in fact, build an arbitrary multi-word compare-and-swap (CASn). Our design requires only the primitives available on contemporary systems, reserves a small and constant amount of space in each word updated (either 0 or 2 bits) and permits non-overlapping updates to occur concurrently. This provides compelling evidence that current primitives are not only universal in the theoretical sense introduced by Herlihy, but are also universal in their use as foundations for practical algorithms. This provides a straightforward mechanism for deploying many of the interesting non-blocking data structures presented in the literature that have previously required CAS2.

1 Introduction

Verified RDCSS by Jung et al.



The Future is Ours: Prophecy Variables in Separation Logic

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Early in the development of Hoare logic, Owicki and Gries introduced *auxiliary variables* as a way of encoding information about the *history* of a program's execution that is useful for verifying its correctness. Over a decade later, Abadi and Lamport observed that it is sometimes also necessary to know in advance what a program will do in the *future*. To address this need, they proposed *prophecy variables*, originally as a proof technique for refinement mappings between state machines. However, despite the fact that prophecy variables are a clearly useful reasoning mechanism, there is (surprisingly) almost no work that attempts to integrate them into Hoare logic. In this paper, we present the first account of prophecy variables in a Hoare-style program logic that is flexible enough to verify *logical atomicity* (a relative of linearizability) for classic examples from the concurrency literature like RDCSS and the Herlihy-Wing queue. Our account is formalized in the Iris framework for separation logic in Coq. It makes essential use of *ownership* to encode the exclusive right to resolve a prophecy, which in turn lets us enforce soundness of prophecies with a very simple set of proof rules.

CCS Concepts: • Theory of computation → Separation logic; Programming logic; Operational semantics.

Additional Key Words and Phrases: Prophecy variables, separation logic, logical atomicity, linearizability, Iris

ACM Reference Format:

Ralf Jung, Rodolphe Lepigre, Gaurav Parthasarathy, Marianna Rapoport, Amin Timany, Derek Dreyer, and Bart Jacobs. 2020. The Future is Ours: Prophecy Variables in Separation Logic. *Proc. ACM Program. Lang.* 4, POPL, Article 45 (January 2020), 32 pages. <https://doi.org/10.1145/3371113>

1 INTRODUCTION

When proving correctness of a program P , it is often easier and more natural to reason *forward*—that is, to start at the beginning of P 's execution and reason about how it behaves as it executes. But

MCAS algorithm: Guerraoui, Kogan, Marathe & Zablotchi (2020)

Efficient Multi-Word Compare and Swap

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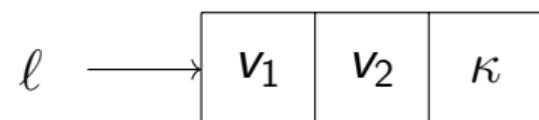
Abstract

Atomic lock-free multi-word compare-and-swap (MCAS) is a powerful tool for designing concurrent algorithms. Yet, its widespread usage has been limited because lock-free implementations of MCAS make heavy use of expensive compare-and-swap (CAS) instructions. Existing MCAS implementations indeed use at least $2k + 1$ CASes per k -CAS. This leads to the natural desire to minimize the number of CASes required to implement MCAS.

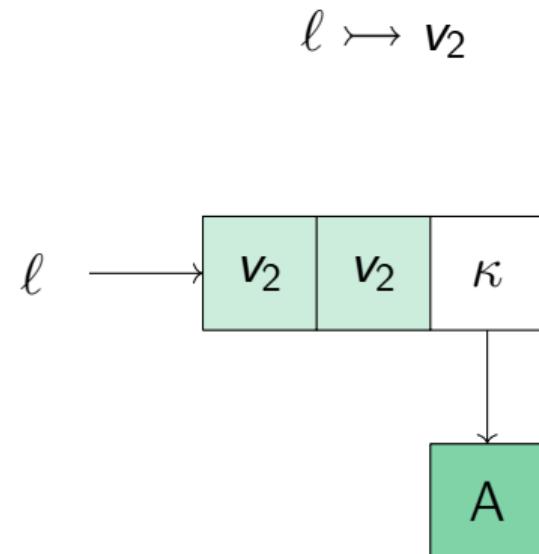
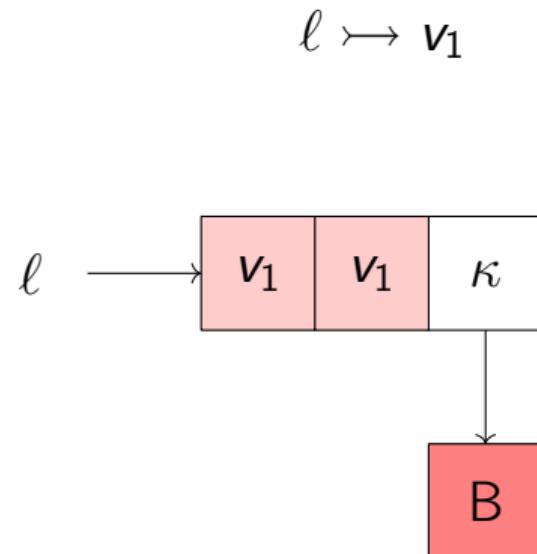
We first prove in this paper that it is impossible to “pack” the information required to perform a k -word CAS (k -CAS) in less than k locations to be CASed. Then we present the first algorithm that requires $k + 1$ CASes per call to k -CAS in the common uncontended case. We implement our algorithm and show that it outperforms a state-of-the-art baseline in a variety of benchmarks in most considered workloads. We also present a durably linearizable (persistent memory friendly) version of our MCAS algorithm using only 2 persistence fences per call, while still only requiring $k + 1$ CASes per k -CAS.

MCAS location

$\ell \rightarrowtail v_1 \text{ or } v_2$

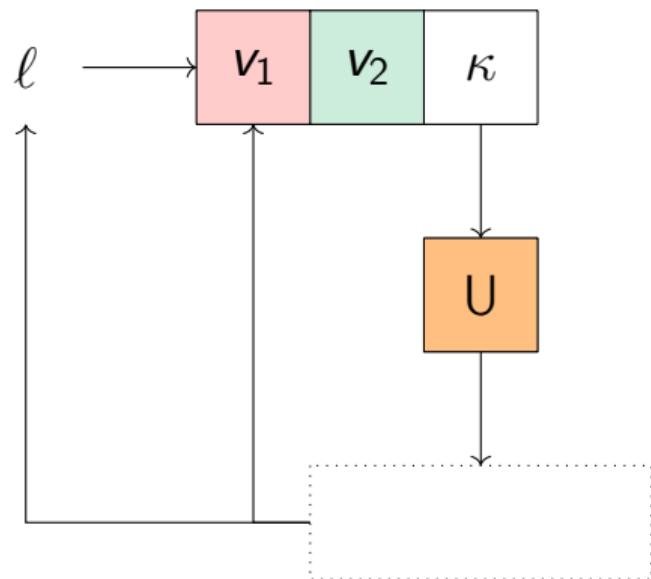


Finished MCAS

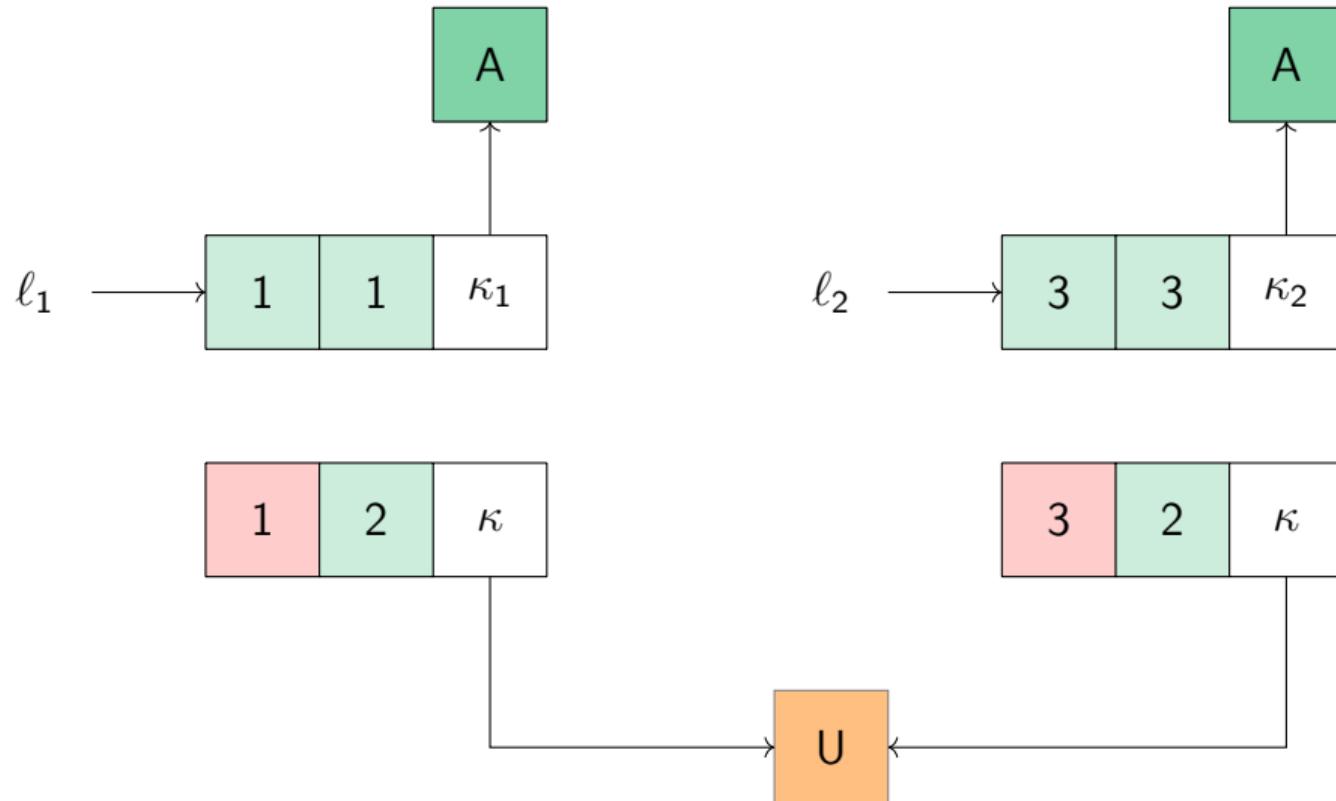


Undetermined MCAS

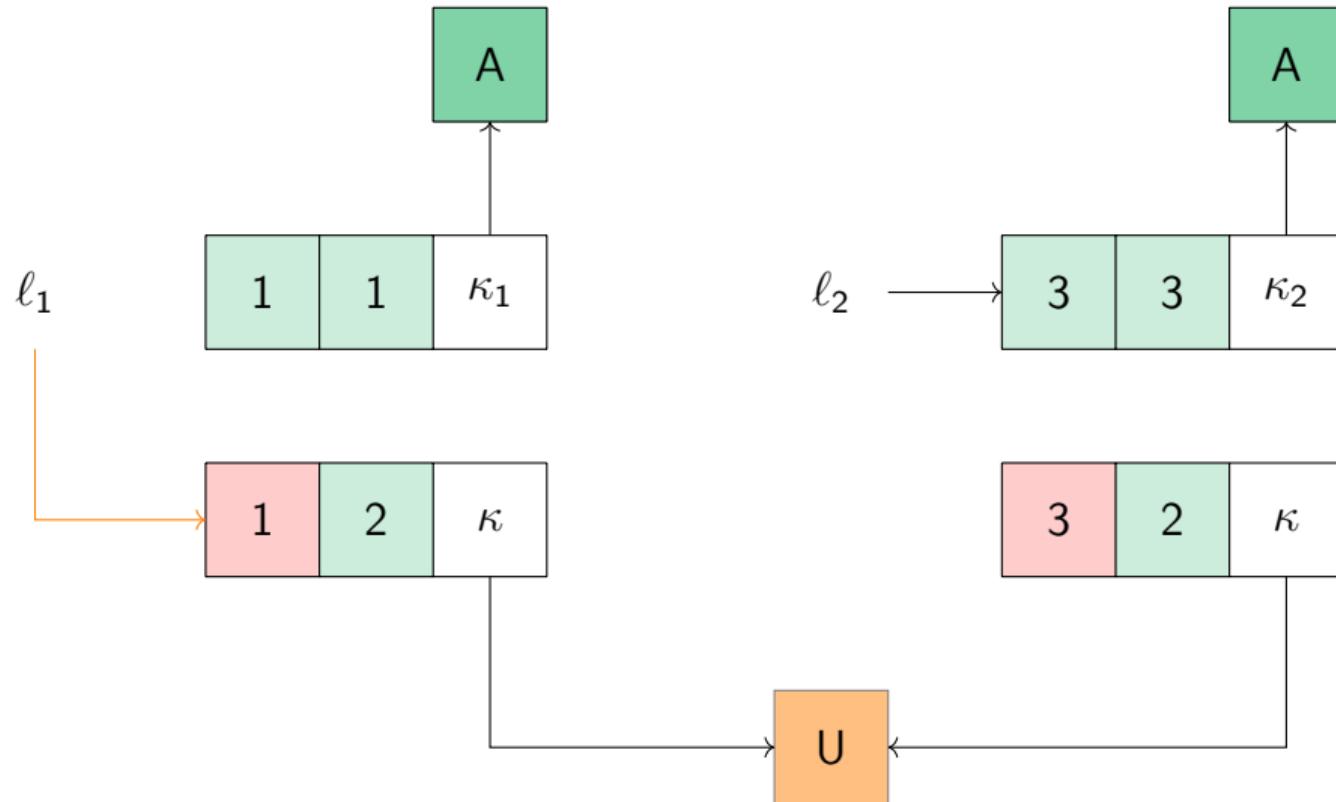
$$\ell \rightarrowtail v_1$$



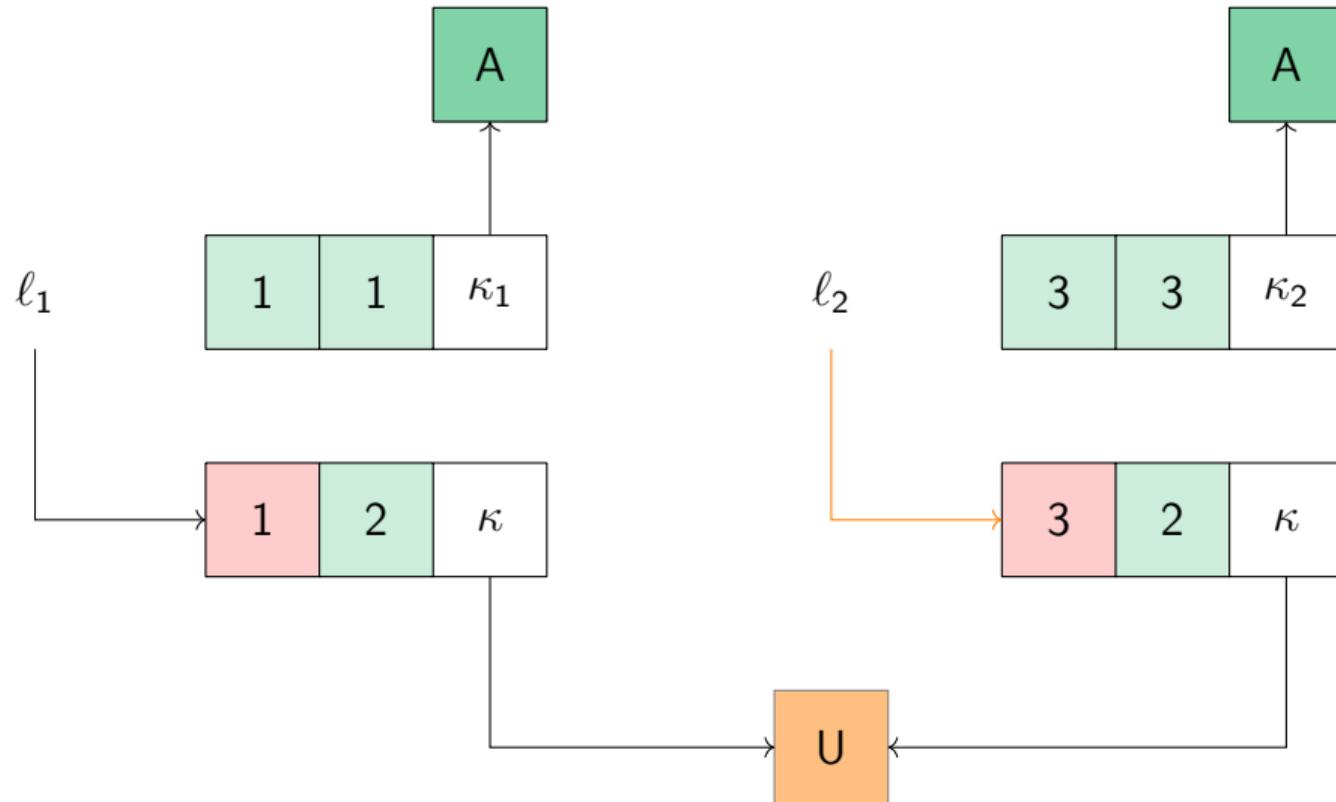
MCAS algorithm



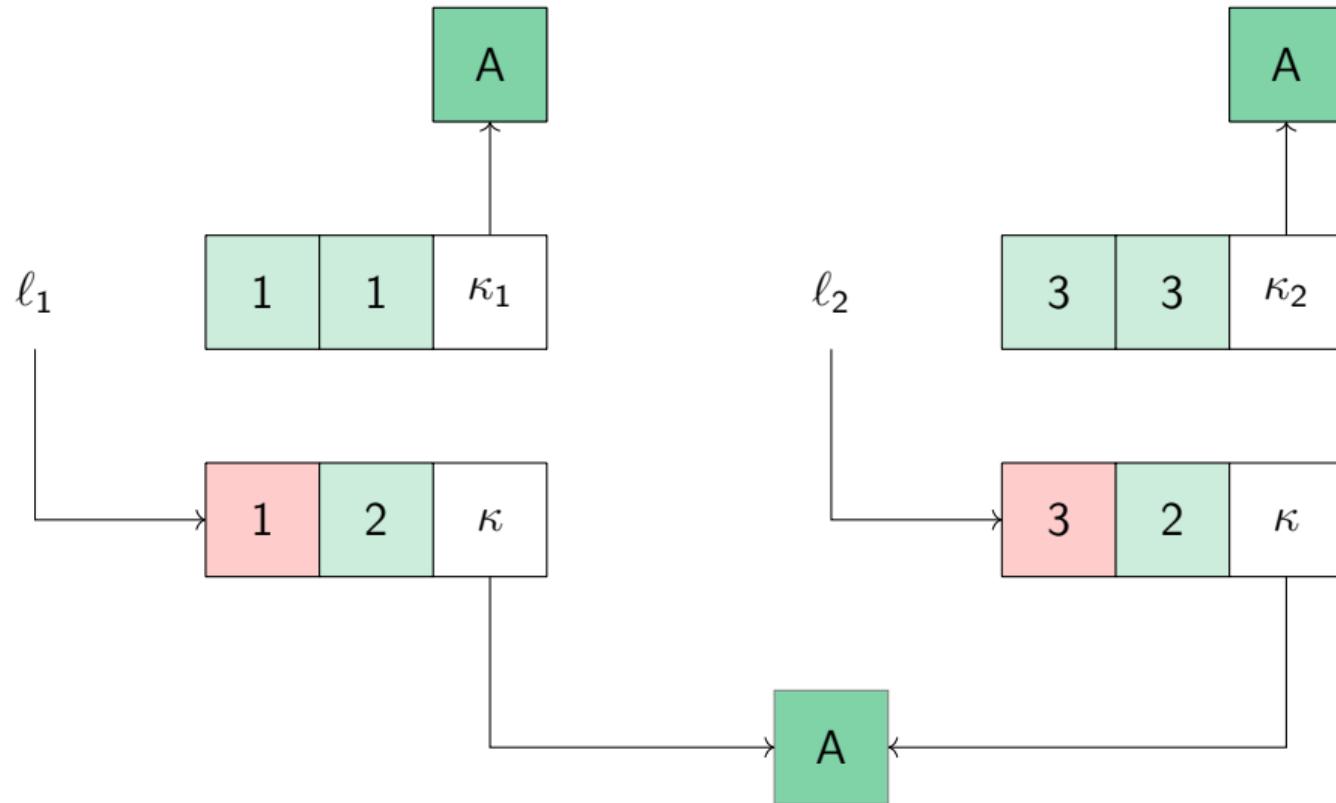
MCAS algorithm



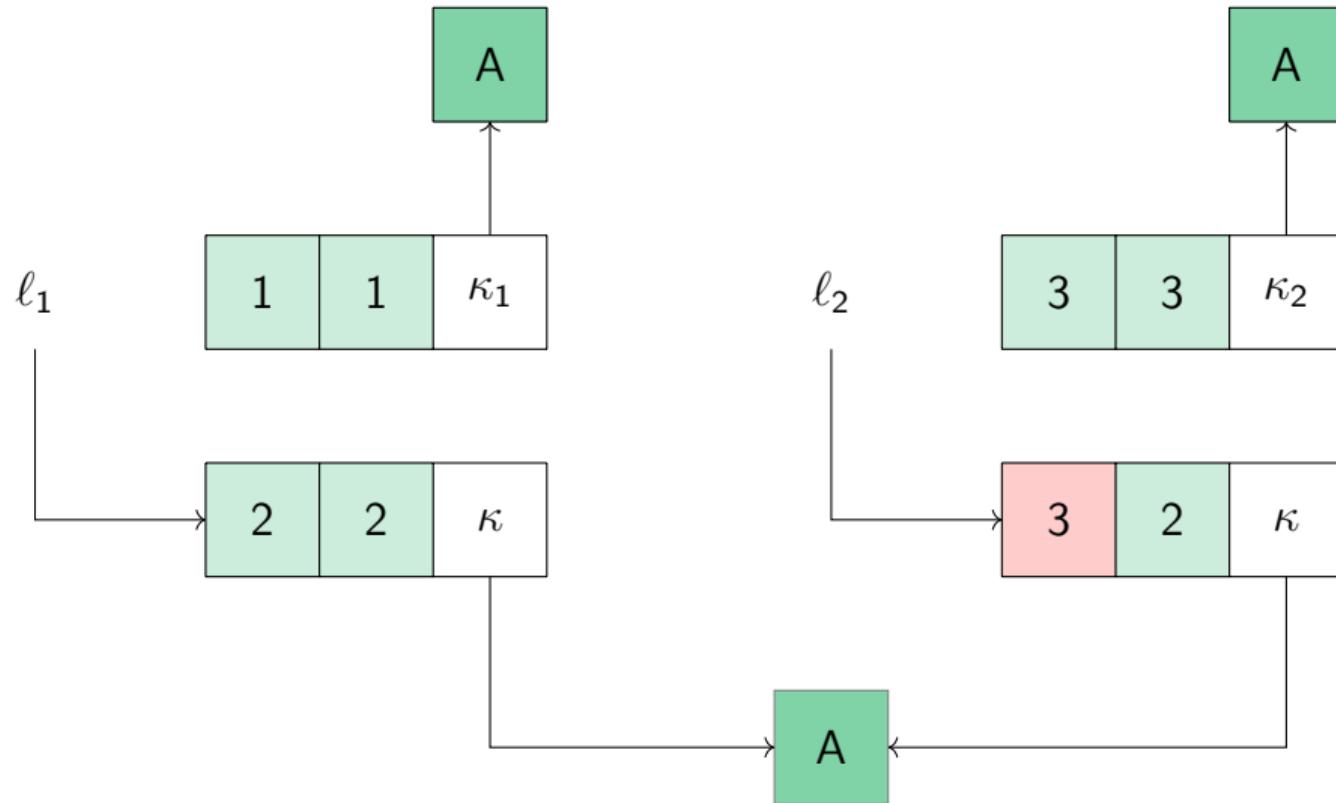
MCAS algorithm



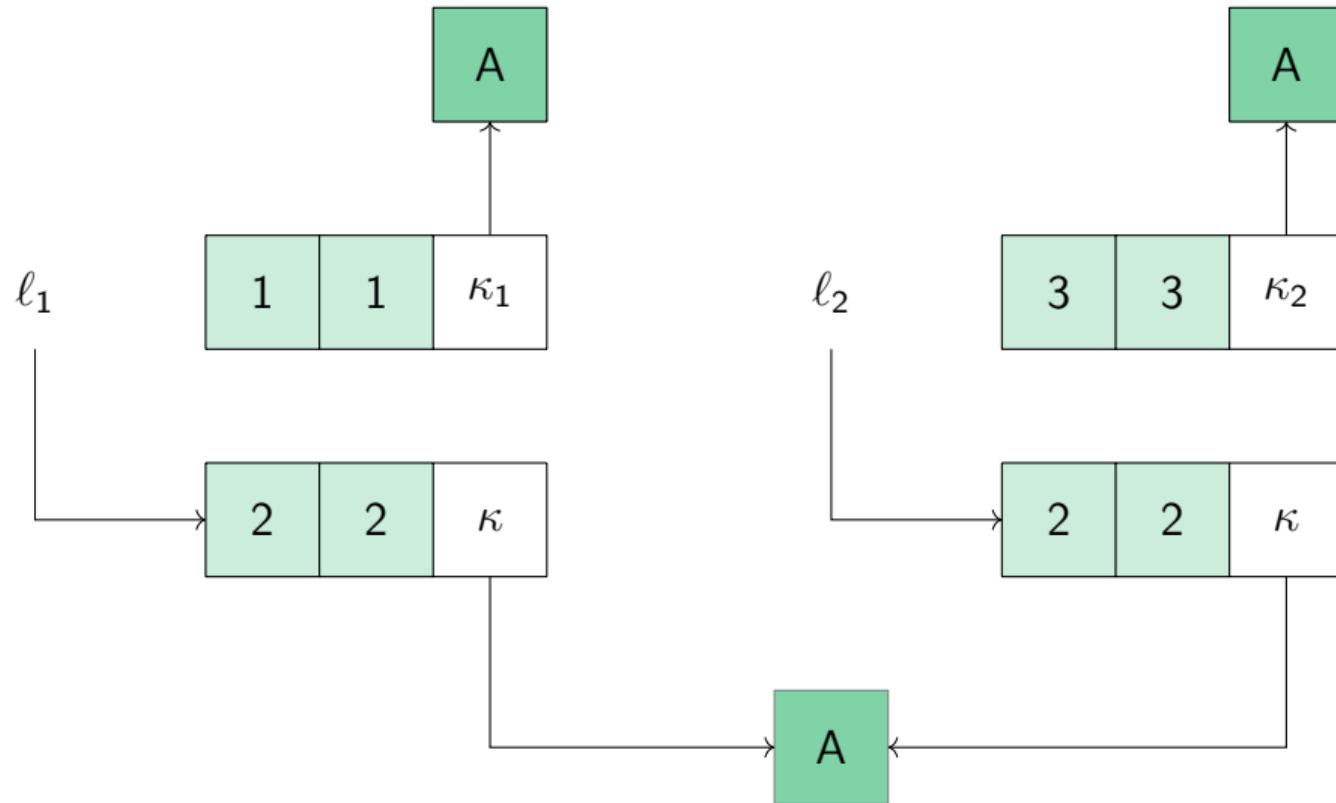
MCAS algorithm



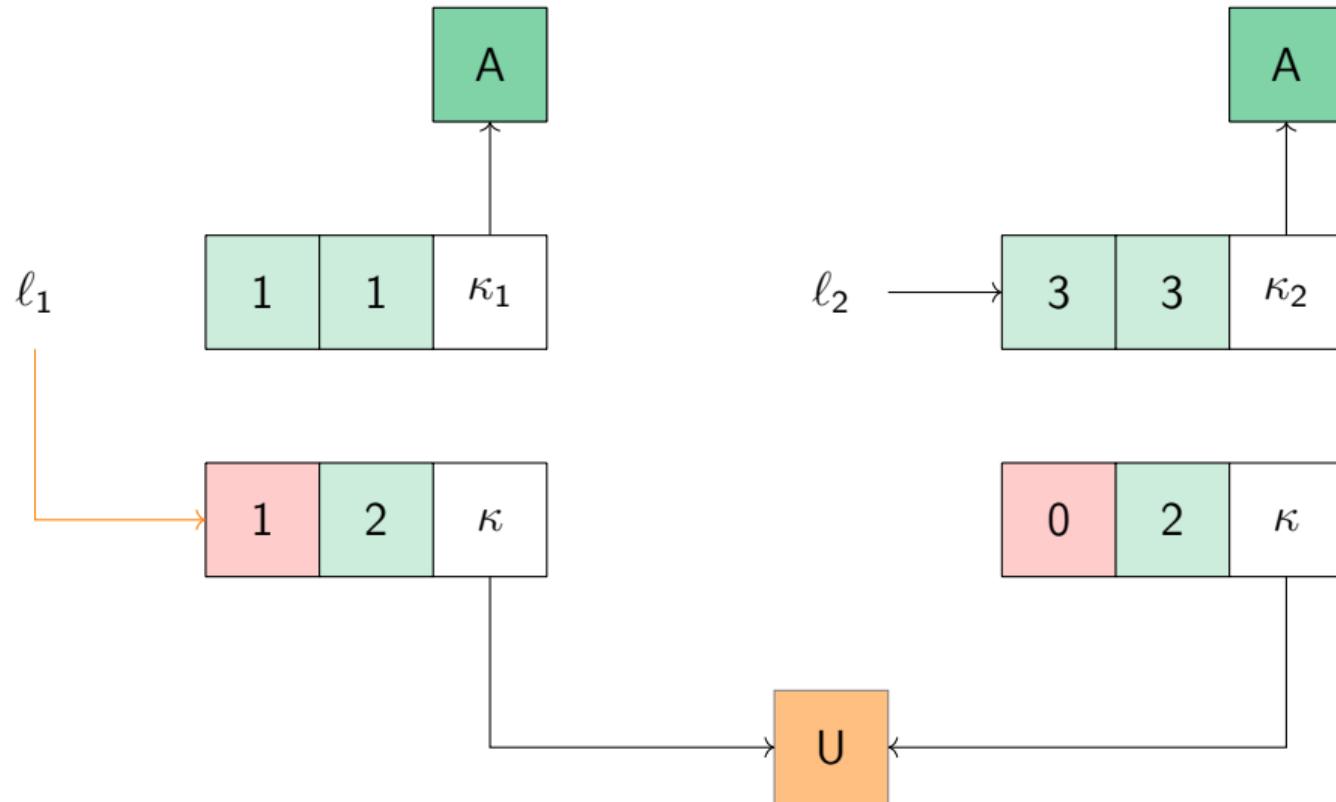
MCAS algorithm



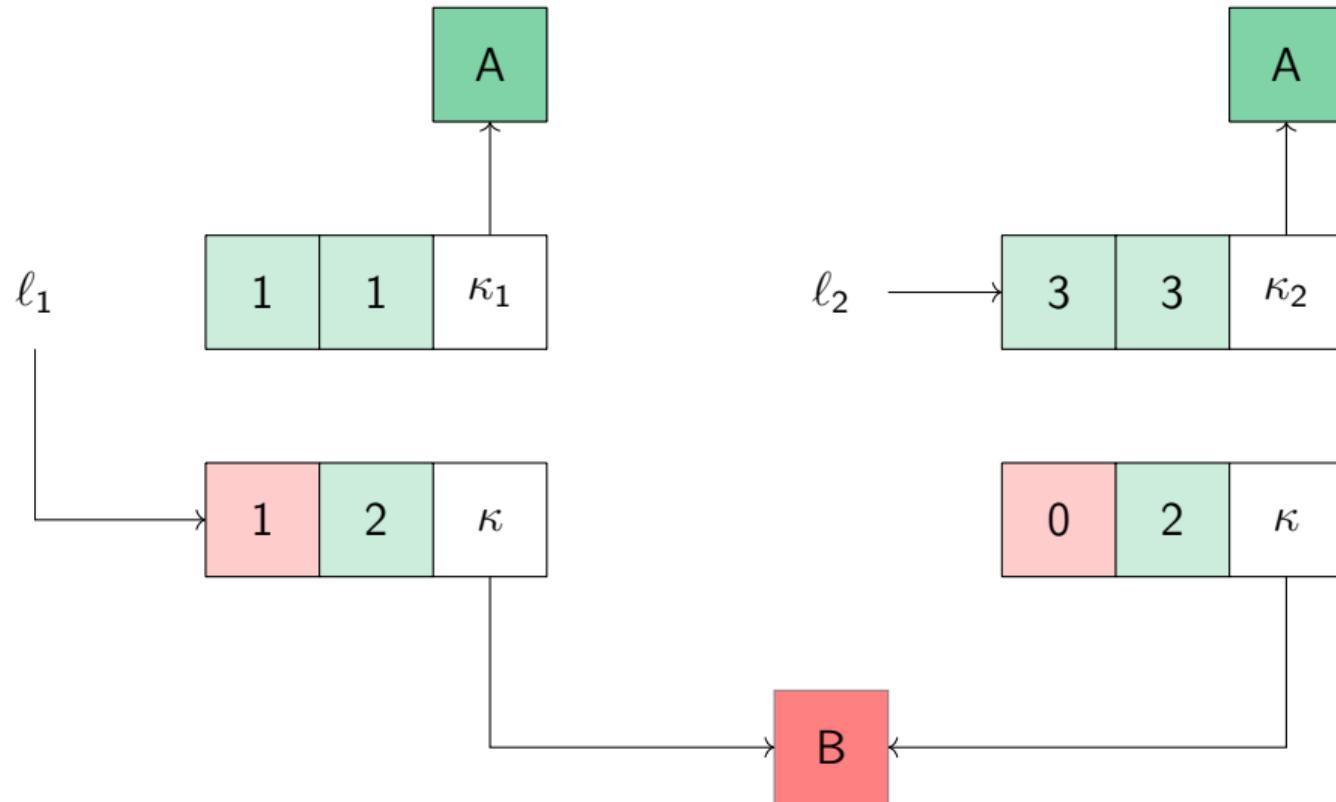
MCAS algorithm



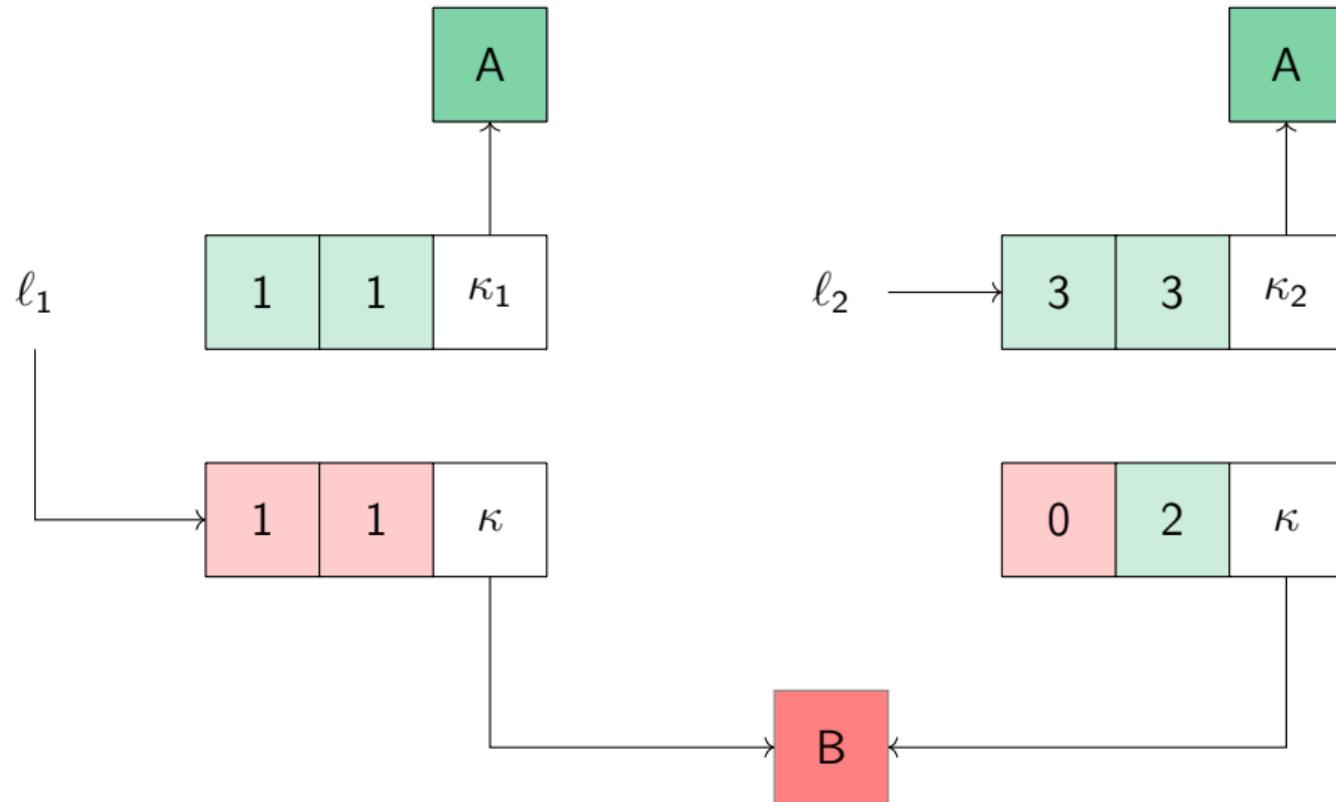
MCAS algorithm



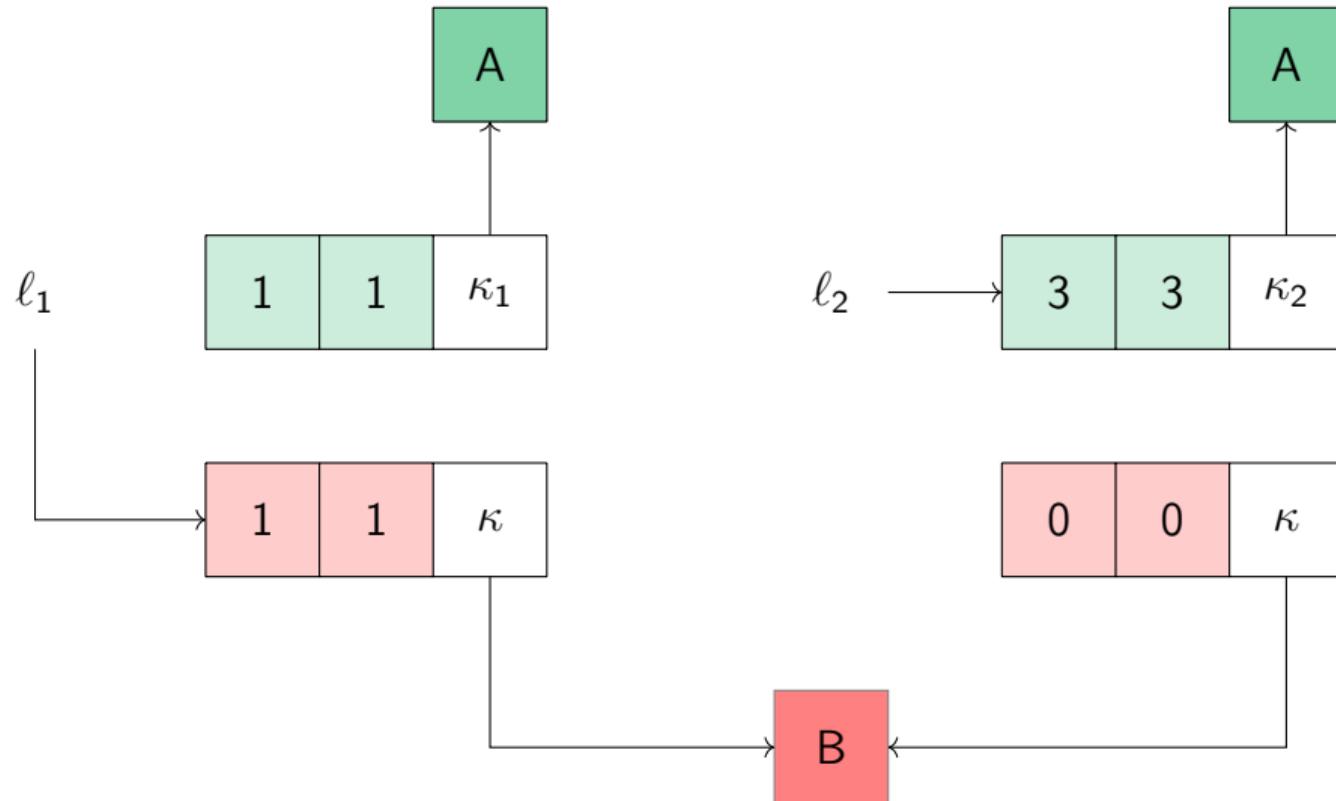
MCAS algorithm



MCAS algorithm



MCAS algorithm



Iris masterplan

Zoology

Physical equality

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Future work

Coupling with semi-automated verification (Gospel)

GOSPEL — Providing OCaml with a Formal Specification Language

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Abstract. This paper introduces GOSPEL, a behavioral specification language for OCaml. It is designed to enable modular verification of data structures and algorithms. GOSPEL is a contract-based, strongly typed language, with a formal semantics defined by means of translation into Separation Logic. Compared with writing specifications directly in Separation Logic, GOSPEL provides a high-level syntax that greatly improves conciseness and makes it accessible to programmers with no familiarity with Separation Logic. Although GOSPEL has been developed for specifying OCaml code, we believe that many aspects of its design could apply to other programming languages. This paper presents the design and semantics of GOSPEL, and reports on its application for the development

Thank you for your attention!