ST24 Distributed Algorithms (COMP60009 @ Imperial College London) by ck21

Broadcast

Broadcast Classes One Shot (each message considered separately from others). Unreliable Best-Effort Broadcast (BEB), Regular Reliable Broadcast (RB), Uniform Reliable Broadcast (URB). Multi Shot (involves all messages that are broadcast). FIFO Message Delivery, Causal Order Message Delivery, Total Order Message Delivery.

PL: Perfect P2P Links. Distributed processes communicate with each other using Pefect P2P Links. Reliable Delivery (L) - If A and B are correct processes then every message sent by A to B is eventually delivered by B. No Duplication (S) - No message is delivered to a process more than once. No Creation (S) - No message delivered unless it was sent.

BEB: Best Effort Broadcast. Given a list of all nodes and a msg, a process could broadcast the msg with multiple sends - for p < - processes do PL.send(p, message) end. If msg sending reliable, then for BEB, the message will be delivered to every correct process. If the broadcasting process crashes during sending, then some arbitrary subset of processes will receive the message (no delivery qurantee). Validity (L) - If a correct process broadcasts a message then every correct process eventually delivers it. No Duplication (S) - No message delivered to a process more than once. No Creation (S) - No message is delivered unless it was broadcast. BEB Basic Broadcast (fail-silent) - send message to each process using PL. Works because PL will ensure all correct processes will deliver the message if the sender of the message does not crash. Algo is fail-silent if process crashes can never be reliably detected. Performance - O(N), where N is the number of processes.

RB: Reliable Broadcast. For BEB, if the sending process crashes during a broadcast, then some arb subset of processes will receive the msg. BEB no delivery agreement guarantee - correct processes do not agree on the delivery of the message RB algos have this - reg RB all correct processes will agree on the messages they deliver, even if the broadcasting process crashes while sending. Validity, No Duplication and No Creation all same as BEB. Agreement (L) - If a correct process delivers message M then every correct process also delivers M. V and A together provide a Termination Property for broadcast. Only correct processes are required to deliver the message - faulty processes could deliver messages not delivered by correct processes.

Eager $\hat{R}B$ Fail-Silent. Every process re-broadcasts every message it delivers. So, if the broadcasting process crashes, the msg will be forwarded by other processes using BEB. Performance - O(N) BEB Broadcasts, $O(N^2)$ messages.

Lazy RB Fail-Stop. Uses BEB with failure detector to detect processes that have failed. Agreement derived from validity property of BEB, that every correct process BEB-broadcasts every message that it deleivered from a crashed process and

railure Detectors. PFD P - Provides processes with a list of suspected (detected) processes that have crashed. Makes timing assumptions (system no longer asynchronous). Never changes its view - suspected processes remain suspected forever. <> P - Eventually PFD. May make mistakes but will eventually accurately detect a crashed process. Strong Completeness (L), Strong Accuracy (S), Eventually strong accuracy (L). PFD Exclude on Timeout - Uses PL to exchange heartbeat messages (req repl) and uses a timeout period for replies to suspect a crashed process. PL performs reliable sending for correct processes. Timeout period needs to be large enough to send a heartbeat message to all processes, processing at the receiving processes and getting replies back (synchrony assumption).

PFD Safety and Liveness. Strong Completeness (L) - every process that crashes will eventually be permanently suspected by every correct process. If a process crashes it stops replying to heartbeat messages and no process will deliver its reply. PL ensures that no message is delivered unless sent. Every correct process will thus detect the crash. Strong Accuracy (S) - no process is suspected before it crashes. A process is suspected only if no heartbeat reply is delivered before timeout. Can only happen if the process has crashed under our timing assumption. Reply delivered before timeout.

URB: Uniform Reliable Broadcast. Validity, No Duplication and No Creation same as BEB and RB. Uniform Agreement (L) - if a process delivers message M then every correct process will also deliver M. Implies the set of messages delivered by a faulty process is always a subset of messages delivered by a correct process (stronger guarantee).

Majority-Ack URB. urb_deliver message only after the message has been beb_delivered by a majority of correct processes. Fail-silent algorithm where process crashes are not reliably detected. Does not use a FD. Assumes that majority of processes are correct. If f processes might crash then we need at least 2f+! processes. - i.e. we need to have a majority of at least f+1 correct processes. For correct process P and message M, if P behadelivers M, then eventually Purb_delivers M. If P beb_broadcasts M then all correct processes $beb_broadcast$ M. Given the majority assumption, P will eventually bebadeliver M from the majority of processes and will then urb_deliver M. Correctness - no creation (S) follows from BEB. No duplication (S) because algo keeps track of messages that have been urb_delivered. Performance - best case 2 steps. 1st step requires N messages. 2nd rebro step requires N(N-1) messages, i.e. we need N+N(N-1) messages. Validity (L) because P urb_broadcasts $M \to P$ eventually beb_delivers $M \to P$ urb_delivers M. Uniform agreement (L) because if Q is any process that urb_delivers M. Implies Q beb_delivered M from the majority of processes (which we assume is a correct majority) so at least 1 correct process must have beb_broadcast M. Hence all correct processes will eventually beb_deliver M(validity of BEB) and will also eventually urb_deliver M.

Ordering. FIFO, Causal Order (CO), Total Order (TO) - all correct processes deliver the same global seq of msg. Impossible in purely async system. FIFO delivery - proc broadcasts m1 before m2 then all correct processes will deliver m1 before m2. Perf same no of messages as m3 broadcast + extra memory. CO m3 msgs delivered in a way that respects causal order. Causal order relation m4 m5 states that message m1 may have caused m2 if: (1) FIFO order (A broadcasts m1 and later m2), (2) Local Order (A delivers m1 and later broadcasts m2), (3) Transitivity (m3 such that m1 m3 and m3 m7). Causal Delivery Property: if any process delivers m2 then it must previously have delivered every mesage m1

such that $m1 \to m2$. Causal delivery is ensured by every message carrying its causally past messages and ensuring they are crb_delivered before the message. No duplication, no creation, validity and uniform agreement are derived from the use and properties of URB. Performance - no additional messages over URB but message size grows linearly. Past grows very large. We can improve algo by GC messages from past.

CRB with Vector Clocks. Each CRB process keeps array of seq numbers indexed by integer process num 1 to N. VC entry for process P indicates num of messages from process P that have been crb_delivered. Each CRB process also maintains a count of the num of msgs that it has rb_broadcast. (=crb_broadcasts received). When CRB rb_broadcasts a message, it includes its VC with its own entry in the VC set to the num of crb_broadcasts it has previously made. When message M is rb_delivered, CRB compares the VC sent with the message with its own VC. Any difference in VC entries for the receiving process and the sending process indicates the number of messages that must be delivered before M can be crb-delivered. CRB must defer delivering if necessary - done with <= element wise comparison between VCs. Process P2 RB_delivers m1 and CRB_delivers m1 since its VC P1 < VC P2 $(VC1[0,0,0] \le VC2[0,0,0])$. After CRB_deliver P2 updates VC to VC2[1,0,0]. P3 RB_delivers m2 first. However it cannot CRB_deliver m2 since VC2[1,0,0] < VC3[0,0,0]. However when m1 is RB_delivered we have VC1[0,0,0] < VC3[0,0,0] so P3 can CRB_deliver m1, which will update VC3 to VC3[1,0,0]. P3 can then CRB_deliver m2 since $VC2[1,0,0] \le VC3[1,0,0]$ and update VC3 to VC3[1,1,0]ensuring CO is maintained. Agreement property of RB will ensure that if a correct process delivers message M then every correct process will also deliver M. This ensures that RB eventually delivers every message that causally preceded M, allowing the VC comparison to succeed and M to be crb_delivered. Causal delivery property ensured (correctness).

```
defmodule CRB_broadcast_vc
def start() do
 receive do
    { : bind, c, rb, pnum, processes }
     -> %{c: c, rb: rb, pnum: pnum, rb_broadcasts: 0,
    pending: empty_set(), vc: init_map(processes,0)}
    |> next()
 end
end
defp next(this) do
    { :crb_broadcast , msg } ->
      vc2 = Map.put(this.vc, this.pnum, this.rb_broadcasts)
      this |> rb_broadcasts(this.rb_braodcasts+1) |> next()
     :rb_deliver, sender, { :crb_data, s_msg, s_vc }} ->
    this |> pending_put({sender, s_msg, s_vc})
    |> check_pending_and_deliver()
    |> next()
 end
defp check_pending_and_deliver(this) do
 case Enum. find (this.pending, fn {_, _, s_vc} -> s_vc <=
    \{sender, s\_msg, s\_vc\} = data \rightarrow
      send this.c, {:crb_deliver, sender, s_msg}
      this |> vc_elem_put(sender, this.vc[sender]+1)
      |> pending.delete(data)
      > check_pending_and_deliver()
    _otherwise ->
     this
 end
end
```

TO/Atomic Broadcast. All correct processes must deliver all messages in same order. TO does not need to respect causality or FIFO. Essential when replicas need to treat requests in the same order to preserve consistency. Pubsub service. TO URB: Additional property. If a process (correct or crashed) delivers message M1 without previously delivering message M2, then no correct process delivers M2 before M1. TOB equivalent to consensus.

Consensus

Intro. Each process proposes a value. All correct processes agree on one of the proposed values in finite time. Values to be agreed are often actions to be carried out by replicated servers.

RAFT. Leader-based, decomposes problem (normal op, leader changes). Simplifies normal operation - no conflicts. More efficent than leader-less approaches. Leader - handles all client interactions, log rep. At most 1 viable leader at a time. Follower-completely passive (issues no RPCs, responds to incoming RPCs). Candidate - used to elect a new leader. Normal op - 1 leader, N-1 followers. Time divided into terms - election, normal op. Failed election - terms with no leader. Each server maintains current term value. Terms identify obsolete info. Persistent server state - currentTerm, votedFor, log[], state, leader, committndex, nextIndex[], matchIndex[]. RPCs. AppendEntries - add an entry to the log. Empty messages used as heartbeats. Vote - Message used by candidates to ask for votes and win elections. Heartbeats - servers start up as followers. Followers expect to receive RPCs from leaders or candidates. Leaders must send empty AppendEntries RPCs to maintain

authority. If $\Delta_{election}$ time units elapse with no RPCs - follower assumes leader crasshed. Follower starts new election. Timeouts 100-500ms. Election Start. Set new timeout in range $[\Delta_{election}, 2 \cdot \Delta_{election}]$. Increment current term. Change to candidate state. Vote for self. Send Vote RPCs to all other servers, retry until either - (1) receive votes from majority of servers, become leader and send AppendEntries heartbeats to all other servers, (2) receive AppendEntries heartbeats from valid leader and return to follower state, (3) no one wins election (election timeout elapses) - start new election.

Election Correctness. Safety - allow at most one winner per term. Each server gives out only one vote per term (persist on disk). Two different candidates can't accumulate majorities in the same term. Liveness - some candidates must eventually win. Choose election timeouts randomly in $[\Delta_{election}, 2 \cdot \Delta_{election}]$. One server usually times out and wins election before others wake up. Works well if $\Delta_{election} >$ broadcast time.

Logs. Logs stored on stable storage and survives crashes. Entry committed if known to be stored on majority of servers. Durable and eventually executed by state machines. Normal Op - client sends command to leader. Leader appends command to log. Leader sends AppendEntries RPCs to followers. Once new entry committed - leader passes command to state machine, returns result to client. Leader notifies followers of committed entries in subsequent AppendEntries RPCs. Followers pass committed commands to state machines. Crashed/slow followers - leaders retry RPCs until succeed. Perf optimal in common case - one successful RPC to any majority of servers. Log Consistency - if log entries on different servers have same index and term, they store the same command. The logs are identical in all preceding entries. If a given entry is committed, all preceding entries are also committed.

Consistency Check. Each AppendEntries RPC contains index, term of entry preceding new ones. Follower must contain matching entry, otherwise it rejects request. Implements induction step to ensure coherency. At beginning of new leaders term, old leader may have left entries partially replicated. No special steps by new leader - just start normal op. Leaders log is truth. Will eventually make follower's log identical to leaders. Multiple crashes can leave many extraneous log entries.

Safety Requirement. Once a log entry has been applied to a state machine, no other state machine must apply a different value for that log entry. RAFT safety property - if a leader has decided that a log entry is committed, that entry will be present in the logs of all future leaders. This guarantees the safety requirement. Leaders never overwrite their logs. Only entries in the leader's log can be committed. Entries must be committed before applying to state machine. Committed (restrictions on commitment) - present in future leader's logs (restrictions on leader election). Best leader - during elections choose candidate with log most likely to contain all committed entries. Candidates include index and term of last log entry in VoteReq. Voting server V denies vote if log is more complete $(lastLogTerm_c < lastLogTerm_v)$ or $(lastLogTerm_c = lastLogTerm_v)$ and $lastLogIndex_c < lastLogIndex_v)$. Leader has most complete log among electing majority.

New Commitment Rule. For a leader to decide that an entry is committed: must be stored on a majority of servers. At least one new entry from leader's term must also be stored on majority of servers. Once entry 4 committed: s_5 cannot be elected leader for term T_5 . Entries 3 and 4 both safe.

Repair Follower Log. New leader must make follower logs consistent with its own. Delete extraneous entries and fill in missing entries. Leader keeps nextIndex for each follower. Index of next log entry to send to that follower. Initialised to 1 + leader last index. When AppendEntries consistency check fails, decrement nextIndex and try again. When follower overwrites inconsistent entry, its deletes all subsequent entries.

Neutralise Old Leaders. Deposed leader not dead. Disconnected. Other servers elect new leader, Old leader reconnects and attempts to commit log entries. Use terms to detect stale leaders and candidates. Every RPC contains term of sender. If sender's term is older, RPCs is rejected, senders reverts to follower and updates its term. If receiver's term is older, it reverts to follower, updates its term and processes RPC normally. Election updates terms of majority of serves. Deposed server cannot commit new log entries.

Client Protocol. What if leader crashes after executing command but before responding - must not execute command twice. Client embeds unique ID in each command. Server includes ID and response in log entry. Before accepting command, leader checks log for entry with that ID. If ID found in log, ignore new command and return response from old command. Exactly-once semantics as long as client doesn't crash.

FLP Impossibility

Synchrony. Async - process exec steps and IPC can take place at arb time. No assumption that proc has phy clocks but sometimes useful to use logical clocks. Sync - upper bound on proc delays (time taken to exec step - receive msg do comp send msg). Upper bound on time for msg delivered (time from sending msg to it beign delivered to a destination proc). Perf may be constrained by bounds. Partially sync - real-world sys mostly sync with periods they aren't. Assume eventually sync. Comm assumptions - (1) async msg passing - proc sending msg continues after sending msg does not wait for msg to be delivered to dest proc. Build sync MP abstraction using async MP. (2) Reliable msg commms - assume msgs sent using reliable MP not dropped.

Result. Any algo for consensus in an async system has executions that do not terminate, even if only one process crashes. Result is true even if no process crashes. Valent Configs. 0V - configs with 0 decision value. 1V - 1 decision val. Univalent (k valent) - configs with k decision value. k is 0 or 1. Config C decides value V if some process has an output bit V. Bivalent - indecisive.

Proof. Execution in which algo A never reaches a decision - indecisive forever. First step - initial bivalent config. Second step - possible to keep doing steps without reaching univalent config. Forever bivalent. Lemma 1 - consider config C with schedules σ_1 and σ_2 . Config $C1 = \sigma_1(C)$ and $C2 = \sigma_2(C)$. 2 sets of processes

taking steps in σ_1 and σ_2 are disjoint - then $\sigma_2(C1) + \sigma_1(C2) = C3$. Commute Property. Lemma 2 - Algo A has initial bivalent config. Assumption: no initial bivalent config (equivalently all initial configs are univalent). Must be adjacent 0V and 1V configs which differ in the state of one process p. Apply a sequence where process p does not take any steps (p fails immed). Contradiction - correct process decides same val for adjacent configs. Contradiction since one config is 1V and other is 0V - so one must be bivalent. Lemma 3 - C bivalent for Algo A. E = (P, M)for event applicable to C. C is set of configs reachable from C without E. D is set of configs reachable from C by applying E = E(Cn) — Cn element of C and E is applicable to Cn . Then \mathcal{D} contains a bivalent config. Informal - if we delay a message that is pending by some amount from one event to arb many, then there will be one config in which you receive the message and end up in an undecided state. Essentially - we end up going from one bivalent config to another by delaying a message - the algo remains indecisive forever. Neighbouring Configs. Assume D only contains univalent configs then derive a contradiction to prove that D contains a bivalent config. 2 configs C0 and C1 are neighbours if one results from other in a single step.

Theorem. There is an execution in which Algo A never terminates. Any deciding run allows the construction of an ∞ non-deciding run. Combine sections of runs to avoid making a decision (univalent). Proof - Lemma 2 states taht there exists an initial bivalent config. Apply Lemma 3 repeatedly - take steps to go from one bivalent config to another bivalent config repeatedly. Operationally - start from C0 (bivalent). Put processes in a queue and schedule them Round Robin (every message eventually received). Let E=(P,M) be the first event with message M and process P be the first process in the process queue. By Lemma 3 we can reach C1 (bivalent) where E is the last message received. Keep repeating and never make a decision.

Temporal Logic of Actions

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TLA-
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            ASCII
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       e1 (/ e2
e1 = e2, e1 # e2
e1 -> e2
e1 <-> e2
                                                                                                                                                                                                                                                             \* equality, inequality
\* implication: e2 is true whenever e1 is true
i.e. ("e1 \/ e2) or (IF e1 THEM e2 ELSE TRUE)
\* equivalence: e1 is true iff e2 is true
                                                                                                                                                                                                                                                                    v for all elements wer in set 5, it holds that expression e is true. Always TRUE if S=() we there exists an element var in set 5, such that expression e is true. Always FALSE if S=() v for \( \) and \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \( \) \(
               CHOOSE war \in S : e
                                                                                                                                                                                                                                                                                    port integer operators with EXTEMD Integers
the set of all integers (an infinite set), and all natural numbers (includes 0)
integer; integers are unbounded
integer range; the set of all integers from et to e2 inclusive, {} if ei > e2
integer addition, subtraction, multiplication, also \div, \% (nod), \(^{\circ} (power)
                                                                                                                                                                                                                                                             \* less than, less than or equal
\* greater than, greater than or equal
       STRING
"", "a", "hello, world"
                                                                                                                                                                                                                                                                    import set operators with INTED Finitedats ... The requires elements to be of the same type ... we are constructor: the set constructor: the set constructor of S. v. act SI is a subset of set SI is a total an element of SI v. act SI is a subset of set SI is a constructor of SI v. act SI is a subset of set SI and SI v. act SI is a subset of set SI and SI v. act SI is a subset of set SI and SI v. act SI is a subset of set SI and SI v. act SI is a set of set SI and SI v. act SI is a set of all with set of SI is act SI v. act SI is a set of set SI and SI v. act SI is a set of set SI and SI v. act SI is a set of set SI act SI v. act SI is a set of set SI v. act SI is a set of set SI v. act SI
\* Finite sets
(a, b, c)
Cardinality(S)
e \in S, e \notin S
Si \unbesteq S2
S \union S2
                                                                                                                                                                                                                                                                            maps keys to values

'Acaction constructor: maps all keys & in KeyS to expression of (e may include &)

'Acaction constructor: maps all keys & in KeyS to expression of (e may include &)

'Acaction of suits the set of keys of function p (expression of the contribution of the contributio
                   [KevS -> Val
                                                                                                                                                                                                                                                                    records are functions/maps internally (field masses string values are the keys...

V record field access: the value of field f of record rec.

V record field access: the value of field f of record rec.

V record rec with fieldsman f remapped to expression a Comy reference of, the original rec.f)

V record rec with multiple fields remapped.

V record rec with multiple fields remapped.

J to e2 (8 is e3 is equal to rec.f)

V record record reconstructor: set of all records with fieldsmans f/1 is set 52,
\* Records

[f1 |-> e1, f2 |-> e2,

rec.f

[rec EXCEPT !.f = e]

[rec EXCEPT !.f1 = e1,
                                                                                                                                                                                                                                                                        import sequence operators with EXIEUD Sequences. Sequences are functions in 

's sequence constructor: sequence of alments et, e2, e3 Sequence sleens 

the bit is desemble of the sequence of (indeed from 1) 

'the bit is desemble of the sequence seps' 

'the length of sequence seq 

the sequence seq ut the added to the end. Also SubSeq() and SelectSeq() 

'the first wineart of sequence seq 

'exe of all first expenses or ever est 3
           <e1, e2, e3>:
seg[i]
seg! \o seg8
Len(se*)
\* Tuples -----
<<e1, e2, e3>>
tup[i]
S1 \X S2 \X S3
                                                                                                                                                                                                                                                                        \* local definition: every occurrence of war in e2 is replaced with e1
\* e1 if e is true, otherwise e2 if e is false
                                                                                                                                                                                                                                                                    \* primed variable: denotes value of non-primed variable ver in the next state
\* variables vf, v2, v3 are unchanged in the next state; name as vi'=vi / \n2^1 v2^1 \n2^3 \n2^3 v3^1 v2^3
\* stuttering action A, or all variables vf, v3, v3 unchanged in successor state
\* non-cuttering A and some variables vf, v2, v3 changed in successor state
\* non-cuttering A in sealled e.g. pre-conditions are true, there exists in true, if condition true
                                                                                                                                                                                                                                                                    \* F is eventually true
\* Fi leads to F2
\* weak/strong fairness for action A
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```
1. \* Module structure
        ---- MODIII E module ----
                                  \* starts TLA+ module (should be in file module.tla). Min 4 '-' at start of and end of of line
        EXTENDS mod1, mod2, ...
                                  \* Imports TLA+ modules mod1, mod2, ... Can also use singular EXTEND, CONSTANT, VARIABLE
                                  \* declares constants c1, c2, ...
        CONSTANTS c1. c2. ...
                                                                         Defined in configuration file when doing model checking
        ASSIME e
                                   \* asserts expression e as an assumption
        VARIABLES werl war2....
                                  \* declares variables ugr1. ugr2.
        OnVane == e
                                   \* defines operator OpName as expression e without parameters. Operators are applied likes macros
        OpName (v1, v2, ...) == e \* defines operator OpName as expression e with parameters v1, v2, ...
       THEOREM e
                                   \* asserts expression e can be proved from the definitions and assumptions and rules of TLA+
10
                                   \* ends TLA+ module (everything after that is ignored). Min 4 '=' at start of line
11. \* Comment
                                   \* to end of line
12. (* Comments *)
                                   \* multi-line comment or boxed comment
                                  \* horizontal separator line
```

BLUE sets, RED booleans, YELLOW integers, GREEN functions/records, PURPLE sequences/tuples, BLACK any/other

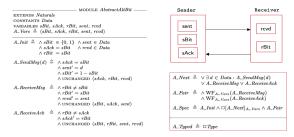
Structure of our TLA⁺ specifications

```
--- MODULE name ---
                                      \* .tla file
                                                                                       Configuration File \* .cfg file
EXTENDS m1, ..., mN
                                       \* can also use plural keyword
                                                                                            SPECIFICATION Spec
PROPERTY NotDeadlock Typed
CONSTANTS c1, ..., cN
                                      \* defined in .cfg file
Vars -- << y1, ..., yN >>
                                                                                       TLC checks the absence of deadlock by default so we will omit the NotDeadlock property from our .tla and .cfg files.
Type == v1_formula /\ ... /\ vN_formula
                                               \* Spec. for state machine
Init == formula
                                               \* Initial state
                                               \* Definitions
DefN -- formula
Action1 -- action_formula
                                                                                            SPECIFICATION Spec
INVARIANT Type \* mapped to PROPERTY []Type
ActionN == action_formula
Next == Action1 \/ ... \/ ActionN
                                             \* Next state
                                                                                            INVARIANT Type \* mapped to PROPERTY []Type
INIT Init \* assumes specification is
NEXT Next \* Init / [][Next]_declaredus
Fair == fairness formula / ... / fairness formula
                                                                                      Note: None of the names Vars, Type, Init, Next,
Deadlock, Typed, Fair, Spec are special. They're just
what we've decided to use in our examples.
NotDeadlock us [](ENARLED Next) \* Properties
Typed - []Type
```

Some LTL Equivalences

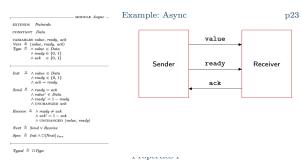
```
• Distribution:
                                                    \bigcirc (p \lor q) \equiv \bigcirc p \lor \bigcirc q \quad p \ \mathcal{U} (q \lor r) \equiv (p\mathcal{U} q) \lor (p\mathcal{U} r)
    \Box(p \land q) \equiv \Box p \land \Box q
     \Diamond (p \lor q) \equiv \Diamond p \lor \Diamond q
                                                    \bigcirc (p \land q) \equiv \bigcirc p \land \bigcirc q \quad (p \land q) \quad \mathcal{U} r \equiv (p \mathcal{U} r) \land (q \mathcal{U} r)
Duals:
                                                        ¬Op ≡ O¬r
     ¬♦n = □¬n
     \neg \Box n \equiv \Diamond \neg n
· Idempotency:
                                                     p U (q U r) \equiv (p U q) U r) \equiv p U r
    q \square \square \equiv q \square
     \Diamond_D \equiv \Diamond \Diamond_D
Absorption:
    \Box \Diamond_p \equiv \Diamond \Box \Diamond_p
     \Diamond \Box p \equiv \Box \Diamond \Box p
Other:
                                                     TRUE Up ≡ ♦p
```

Example – Abstract Alternating Bit Protocol p229



Some LTL Patterns

```
· Invariance
                   Πp
                                 always p
Guarantee
                    Φp
                                 eventually p
Response
                    p ⇒ ◊q
                                 p implies eventually q
                   \Box \Diamond p
                                 always eventually p (may need to wait)
· Progress
Stability
                    Ф□р
                                 eventually always p
                                 eventually p implies eventually q
```



Each server's currentTerm monotonically increases

```
\forall \; i \in Server : \\ currentTerm[i] \leq currentTerm'[i]
```

 Election Safety Property. There is at most one leader per term. Note: elections is a "history variable" that keeps track of elections and is used in the TLAPS proof.

```
\label{eq:definition} \begin{array}{l} \forall \ e, f \in \mathit{elections}: \\ e.\mathit{eterm} = f.\mathit{eterm} \Rightarrow e.\mathit{cleader} = f.\mathit{eleader} \end{array}
```

• Leader Append-Only Property. Leaders logs grow monotonically during their term.

```
\forall \ e \in elections:
currentTerm[e.leader] = e.term \Rightarrow
\forall \ index \in 1...Len(log[e.leader]):
log'[e.leader][index] = log[e.leader][index]
Distributed Alterithms 60009

Mobiline Rati Commune

N. Dalay, Side 24
```

• State Machine Safety Property. Servers only apply entries that are committed in their current

```
\forall i \in Server:
\land commitIndex[i] \le Len(log[i])
\land \forall (index, term) \in log[i]:
index \le commitIndex[i] \Rightarrow
(index. term) \in committed(current Term[i])
```

 Committed. An entry <index, term> is committed at term t if it present in every leader's log following term t.

```
committed(t) \triangleq \{(index, term) : \\ \forall election \in elections : \\ election.eterm > t \Rightarrow \\ (index, term) \in election.elog\}
Distributed Absolution 6009

Mobiling Rish Commune

N. Dalay, Sibe 28
```

Temporal Logic Operators. Atom a - a true in current moment/state. Next opp is true in next moment/state. Next not available in TLA+. Always $\square p$ - true now and in all future moments/states. Eventually $\lozenge p$ - p is true now or in some future moment/state. Always Eventually $\square \lozenge p$ - p is true infinitely often (infinitely many times). p is repeatedly true - moments or states where p isn't true. Hungry, Redlight. Eventually Always $\lozenge \square p$ - p will be true from some state. There will be a moment/state where p will be true and will remain true. p will become continuously true. Terminated, for example.

Fairness. Constraints we assume the scheduler of the system will enforce - to fairly select the next enabled action to execute. Without fairness constraints its possible that some liveness properties may never progress. E.g. if an enabled action is never executed, so are used to guarantee that parts of the system make progress. Expressed in temporal logic. Weak Fairness - $\Diamond A \Rightarrow \neg \Diamond A$. If an action A is continuously (Eventually Alw) enabled then it will be executed infinitely often. Strong Fairness $\neg \Diamond A = \neg \neg \Diamond A$. If action A is repeatedly (Alw Eventually) enabled then it will be executed infinitely often when in enabled state. Repeatedly - times when A not enabled. Absolute Fairness $\neg \Diamond A - A$ will be executed infinitely often whether enabled or not. Properties true under WF also true under SF and AF. TLA+ Fairness. WF constraint

 $WF_v(A) \triangleq \Diamond \square (ENABLED < A >_v) => \square \Diamond < A >_v$ - if non-stuttering action A is continuously enabled then it will be executed repeatedly. A must eventually occur if A is continuously enabled. Useful for preventing unfairness due to worst-case interleaving/scheduling of actions. SF Constraint

 $SF_v(A) \triangleq \Box \Diamond (ENABLED < A >_v) => \Box \Diamond < A >_v$. If non-stuttering action A is repeatedly enabled then it will be executed repeatedly (inf often) when in enabled state. A must eventually occur if A is repeatedly enabled. Useful for handling contention. $Typed \triangleq \Box Type$ - not type.

Safety. Nothing bad ever happens. Assert what may happen. If a safety property is violated by a behaviour it will have been violated in a particular step from which we can produce a counterexample that shows an error trace to the violating step. Liveness. Something good eventually happens. Liveness properties assert what must happen - cannot be violated by a particular step - ie rest of behaviour can always make the liveness property true. Check for inf behaviours - clock stopped clicking or message never delivered. Requires temporal logic but ad-hoc temporal formulas can be error-prone. Conjunction of $WF_v(A)$ and $SF_v(A)$ formulas. Next constraints what steps may occur while Fairness only defines what steps must eventually occur. Fairness should not constrain lnit or Next (machine closed spec). Typically, actions used in Fairness formula are subactions of Next that define how behaviours keep executing Next actions when Next is enabled.