

Kernel debug

```
## Print modules();

| dump_stack();

| print_oops_end_marker();

| interest | summaried | set long | set
```

```
Kernel — BUG_ON/BUG

printk('BUG: failure at %s:%d/%s()\f\n',_FiLE_,_LINE_,_func_);\
paric('BUGI');\
```

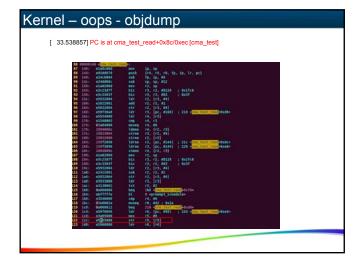
```
/kernel — oops - bug

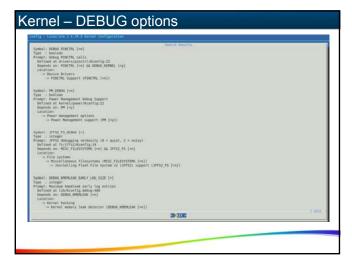
/kernel$ git diff
diff —git a/tools/cma/cma_test.c b/tools/cma/cma_test.c
index 7eb96db. ba02d35 100644
--- a/tools/cma/cma_test.c
+++ b/tools/cma/cma_test.c
+++ b/tools/cma/cma_test.c
@@ -44,6 +44,9 @@ cma_test_read(struct file *file, char __user *buf, size_t count, loff_t *ppos)

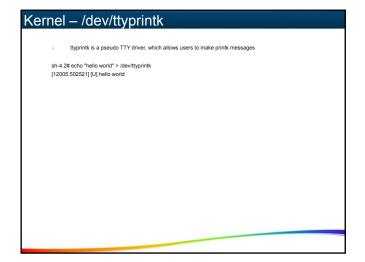
if (!alloc)
    return -EIDRM;
+
+ volatile int *p=0;
+ *p = 0;

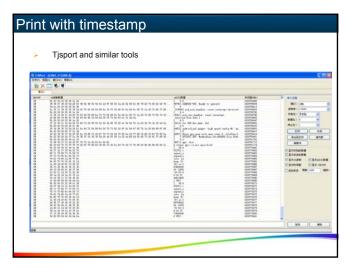
dma_free_coherent(cma_dev, alloc->size, alloc->virt, alloc->dma);
```

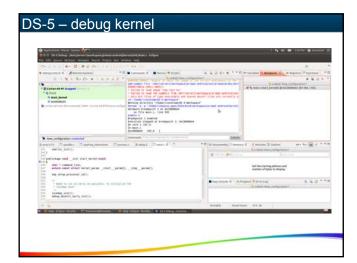
```
| 34.152437| Backtrace: | 34.152437| Backtrace: | 34.152482| [< br/>
| 54.152482| [< br/>
| 54.152482| [< br/>
| 54.152482| [< br/>
| 54.163982| | 6: beartid890 r5: cc8e6e20 r4:00001000 | 34.168592| [< c09304549-] (vfs_ read+0xb/0x144) from [< c039d724>] (sys_ read+0x4c/0x108) | 34.176394| r5: beartid890 r7:00001000 r6: beartid890 r5: cc8e6e20 r4:00008c34 | 34.183093] [< c039d689-] (sys_ read+0x0/0x108) from [< c02dc720-] (ret_fast_syscall+0x0/0x30) | 34.191416| Code: 03e00022 03e00012 e59f6058 e3a05000 (e5855000) | 34.294365] -- [ end trace f0d7620b9f61d90d ]---
```

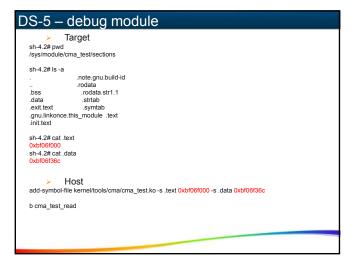


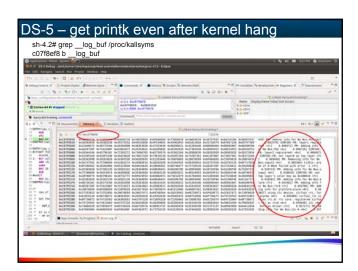


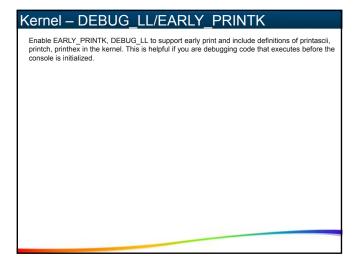




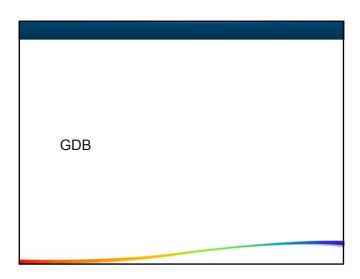








Kernel — initcall_debug Passing the option "initcall_debug" on the kernel command line will cause timing information to be printed to the console for each initcall. You will need to enable CONFIG_PRINTK_TIME and CONFIG_KALLSYMS in your kernel configuration for this to work correctly. calling ipc_init+0x0/0x28 @ 1 msgmni has been set to 42 initcall ipc_init+0x0/0x28 returned 0 after 1872 usecs dmesg -s 128000 | grep "initcall" | sed "s/\((.*\)after\((.*\)\)\()\)\()\)\()\)\(1/g* | sort -n



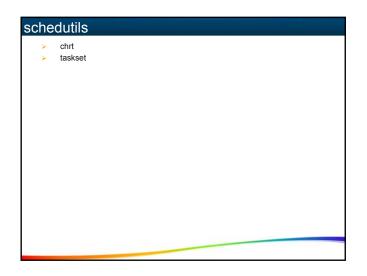
```
GDB — ARM Native GDB

Target
sh-4.28 gdb /system/bin/2c-util
...
Reading symbols from /system/bin/2c-util.done.
(gdb) r
Starting program: /system/bin/2c-util
[1253, 81508] [nit untracked pid 1032 exited
BFD: /system/bin/linker: warning: sh_link not set for section 'ARM exide'
BFD: /system/bin/linker: warning: sh_link not set for section 'ARM exide'
warning: Unable to find dynamic inker breatpoint function.
GDB will be unable to debug shared birrary initializers
and track explicitly loaded dynamic code.

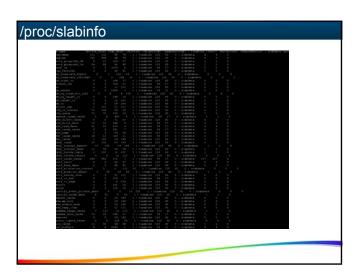
Program received signal SIGSEGV, Segmentation faut.
main (argo-1, argy=0bbes14a54) at external/2c-util.c.131
131 external/2c-util/2c-util.c. vis such file or directory.
in external
```

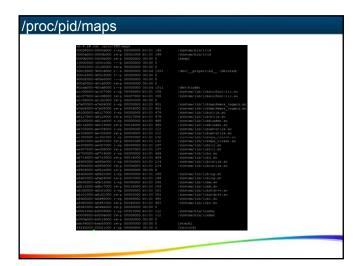
```
For the product of th
```

```
Figure 1-4.29 ps
PID USER VSZ STAT COMMAND
599 root 40258 S. //system/bin/mediasever
sh-4.29 gdmewr=5039 --attach 599
Attached; pid = 599
Attached
```

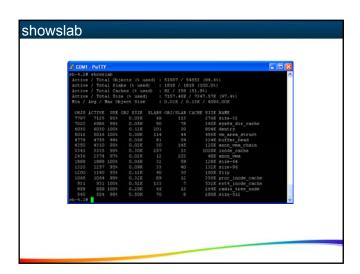



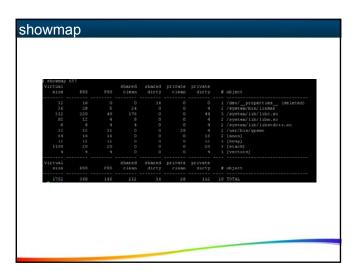
Memory usage and leak check



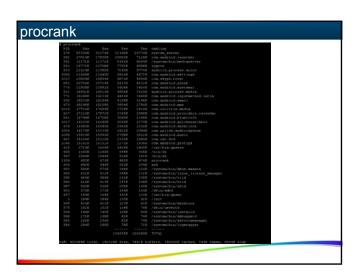


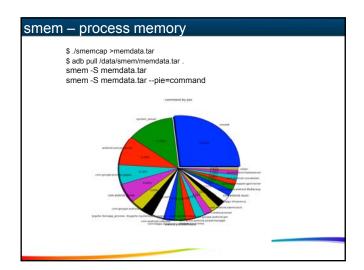


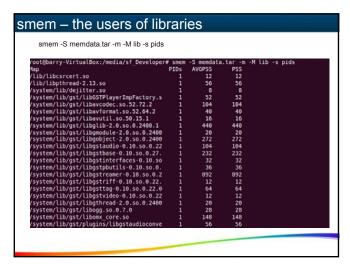


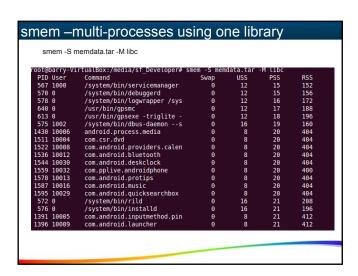


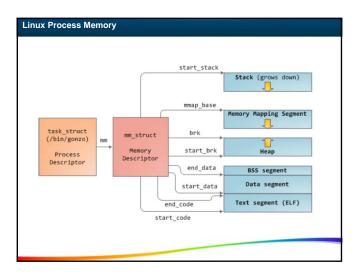
Vas	rocmem 506 Pas							Nome
con and	ting wase	for man						
56K	568	568						/system/bin/vold
								/system/bin/vold
								(heap)
								(beap)
								(beap)
								/dev/ properties
								/dev/ properties
								/dev/ properties
								/dev/_properties_
								/dev/ properties
								/system/lib/libdiskconfig.so
								/system/lib/libdiskconfig.so
								/system/lib/libsysutils.so
								/system/lib/libsysutils.so
								/system/lib/liberypto.so
								/system/lib/liborypto.so
								/system/lib/liborypto.so
								/system/lib/libz.so
4E 20E	4K 20K	48	430	0K 20K	OK OK	4K OK	30 30	/system/lib/libz.so /system/lib/libcutils.so
48	48	48	480	0K	OK	98	OK.	/system/lib/libcutils.so /system/lib/libcutils.so
080	080	090	360	080	OK.	OK.	OK.	/system/lib/libcutils.so
12 K	128	080	080	12K	OK.	OK.	OK.	/system/lib/liblog.so
48	48	48	48	30	OK.	48	OK.	/system/lib/liblog.so
	98			38	OK.	OK.	OK.	/system/lib/libm.so
48	48	48	48	080	OK.	4K	OK	/system/lib/libm.so
48	48	080		48	OK.	OK.	OK.	/system/lib/libstdc++.so
48	48	48	48			4K		/system/lib/libstdo++.so
160%	160%	0.00	48	156K	OK.	410	OK	/system/lib/libc.so
12 K	128			48				/system/lib/libc.so
12 K	12K	128				12K		/system/lib/libc.so
248	248							/system/bin/linker
								/system/bin/linker
								/system/bin/linker
								(stack)
								(a count)

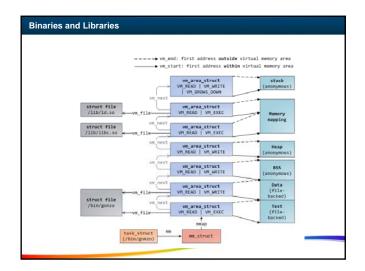


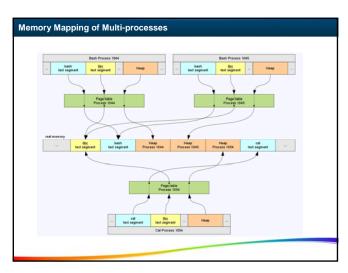


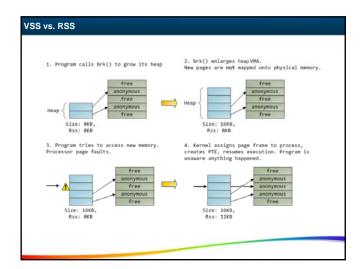


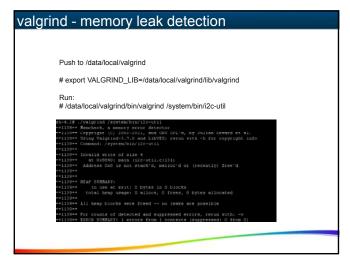




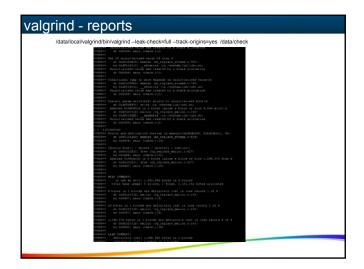


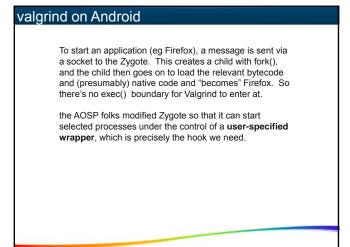


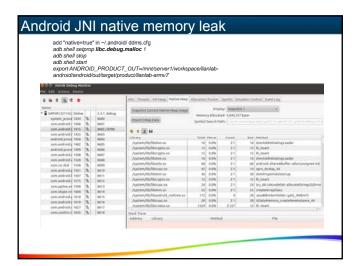


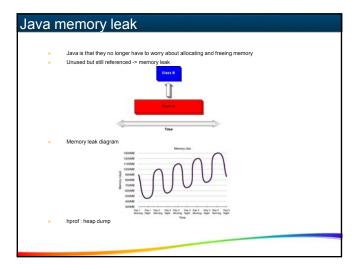


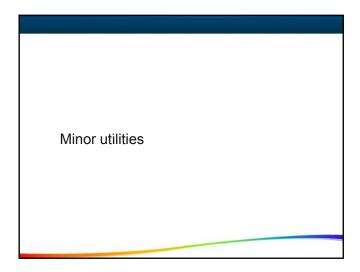
```
    Valgrind - memory check
    Illegal read / Illegal write errors
    Use of uninitialised values
    Use of uninitialised or unaddressable values in system calls
    Illegal frees
    When a heap block is freed with an inappropriate deallocation function
    Overlapping source and destination blocks
    Memory leak detection
```

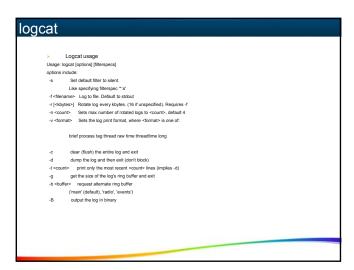












Init will redirect service stdin, stdout, stderr to /dev/null Logwapper will read all printf/perror and call LOG(LOG_INFO, tag, "%s", &buffer[a]); dup2(child_ptty, 2); Examples: service icense-manager /system/bin/logwrapper /system/bin/linux_license_manager ones/hot

```
Log in CPP

* Simplified macro to send a verbose log message using the current LOG_TAG.
*/
#finder LOGV
#ft LOG_NDEBUG
#define LOGV(...) ((vold)0)
#felse
#define LOGV(...) ((vold)0, LOG_LOG_VERBOSE, LOG_TAG, _VA_ARGS_))
#fendf
#finder LOGV [F]
#ft LOG_NDEBUG
#define LOGV_F(cond...) ((vold)0)
#felse
#fel
```

```
DownloaderActivity java: private final static String LOG_TAG = "Downloader";

quakeferocom/androidquakeDownloaderActivity java:
```

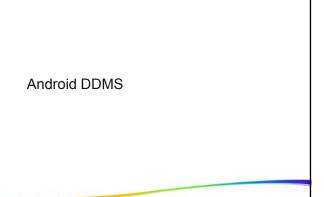
I2C testing utility (using i2c-dev driver)

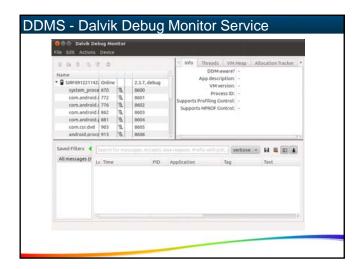
Read/write registers in I2C client at userspace
/system/bin/i2c-util /dev/i2c-2 0x11 0x22 0
/system/bin/i2c-rw /dev/i2c-2 0x11 0x22 1 0xff

```
SPI testing utility (using spidev driver)

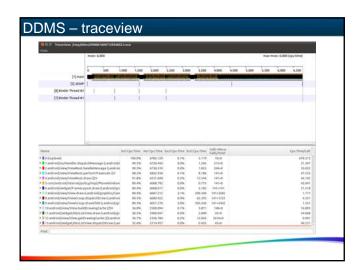
Documentation/spi/spidev_test.c
int main(int argc, char *argv[])
{
    fd = open(device, O_RDWR);
    ...

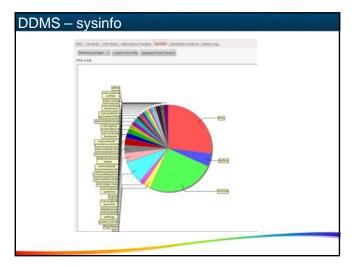
    ret = ioctl(fd, SPI_IOC_WR_MODE, &mode);
    ret = ioctl(fd, SPI_IOC_RD_MODE, &mode);
    ret = ioctl(fd, SPI_IOC_RD_BITS_PER_WORD, &bits);
    ret = ioctl(fd, SPI_IOC_RD_BITS_PER_WORD, &bits);
    ret = ioctl(fd, SPI_IOC_WR_MAX_SPEED_HZ, &speed);
    ret = ioctl(fd, SPI_IOC_RD_MAX_SPEED_HZ, &speed);
    transfer(fd);
}
```

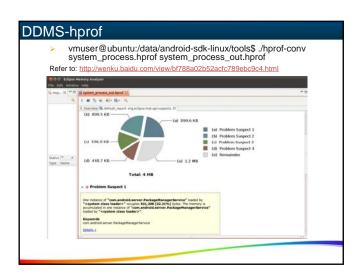


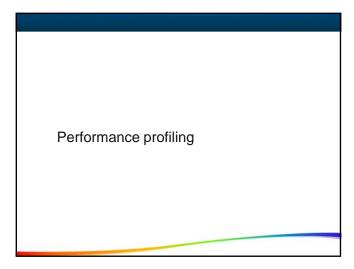




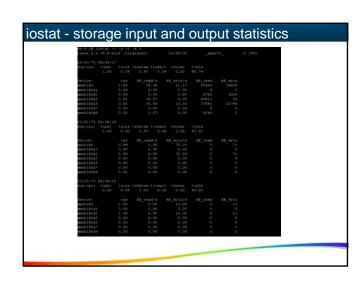


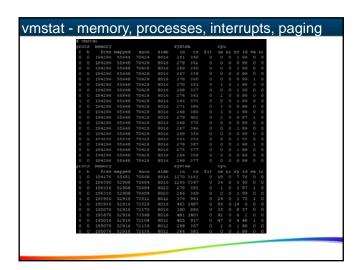


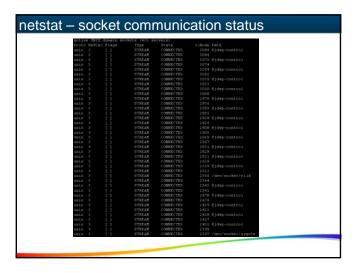




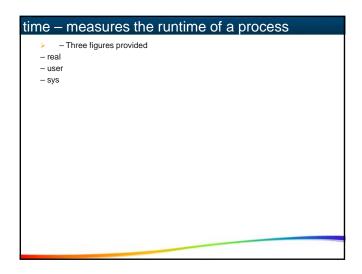
```
top
   User 0%, System 0%, IOW 0%, IRQ 0%
   User 2 + Nice 0 + Sys 3 + Idle 297 + IOW 1 + IRQ 0 + SIRQ 0 = 303
    PID CPU% S #THR VSS RSS PCY UID
   1017 1% R 1 964K 376K fg root top
615 0% S 11 16076K 1796K fg root /usr/bin/gpsexe
   574 0% S 10 39756K 7576K fg root /system/bin/mediaserver
   555 0% S 1 0K 0K fg root jbd2/mmcblk0p2-
584 0% S 10 14756K 2356K fg root
   /system/bin/synergy_service
                    0K 0K fg root khelper
     6 0% S 1
    13 0% S
               1
                    0K
                          0K fg root
                                       suspend
                           0K fg root
                                       kworker/0:1
                    0K
    198 0% S
                    0K
                           0K fg root
                                       sync_supers
   200 0% S
                     0K
                          0K fg root
                                       bdi-default
```

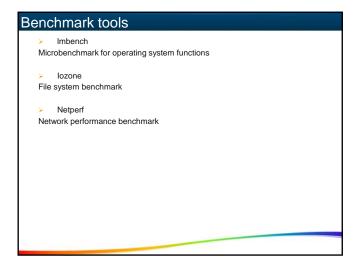


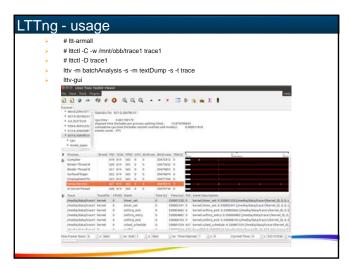


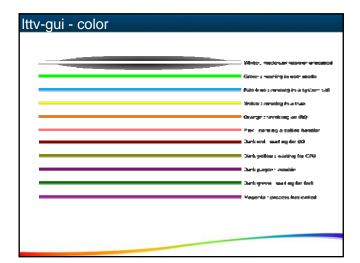


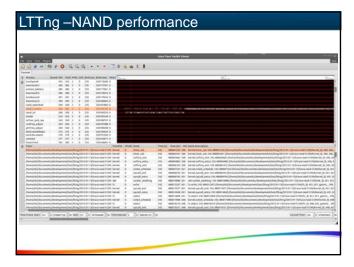
```
mpstat - show multi-core CPUs
  barry@lianlab:~$ mpstat -P ALLLinux 2.6.32-39-server (lianlab)
                                        (16 CPU)01:54:06 PM CPU %usr
         04/12/2012 __x86_64_ (16 CPU)01:54:06 PM CPU % wsys %iowait %irg %soft %steal %guest %idle01:54:06 PM all
   1.49 0.00 0.16 0.08 0.00 0.01 0.00
                                           0.00 98.2701:54:06 PM 0
       0.00 0.23 0.49 0.00 0.03 0.00 0.00 97.2701:54:06 PM
  1.98
        0.00 0.28
                   0.48
                                           0.00 97.1701:54:06 PM
  2.05
                         0.00
                               0.02
                                     0.00
   1.79
        0.00 0.27
                   0.03 0.00 0.00
                                     0.00
                                           0.00 97.9101:54:06 PM
        0.00 0.29
                         0.00
                               0.00
                                     0.00
                                           0.00 97.7401:54:06 PM
   1.93
                   0.03
   1.44
        0.00 0.15
                   0.02
                         0.00
                               0.00
                                     0.00
                                           0.00 98.4001:54:06 PM
   1.38
        0.00 0.15
                   0.02
                         0.00
                               0.00
                                     0.00
                                           0.00 98.4501:54:06 PM
        0.00
              0.13
                    0.01
                         0.00
                               0.00
                                     0.00
                                           0.00
                                                98.5201:54:06 PM
                         0.00
                               0.00
0.01
   1.52
        0.00 0.16
                   0.01
                                     0.00
                                           0.00 98.3001:54:06 PM
                                           0.00 98.5501:54:06 PM
                   0.04
   1.28
        0.00
             0.12
                                     0.00
   1.34
        0.00
              0.13
                    0.04
                         0.00
                               0.00
                                     0.00
                                           0.00 98.4901:54:06 PM
                   0.02
   1.37
        0.00 0.14
                         0.00
                               0.00
                                     0.00
                                           0.00 98.4701:54:06 PM
  1.38
        0.00
             0.13
                   0.02
                         0.00
                               0.00
                                     0.00
                                           0.00
                                                98.4601:54:06 PM
   1.31
        0.00 0.13
                   0.01
                         0.00
                               0.00
                                     0.00
                                           0.00 98.5501:54:06 PM
                                                                 13
                                          0.00 98.6701:54:06 PM 14
        0.00 0.11 0.01
                         0.00 0.00
                                     0.00
  1.20
             0.10
                    0.01
                         0.00
                               0.00
                                     0.00
                                           0.00
                                                98.7201:54:06 PM 15
  1 29
        0.00 0.12 0.01
                         0.00 0.00
                                     0.00
                                           0.00 98.58
```











```
Oprofile -usage

# motoprobe oprofile

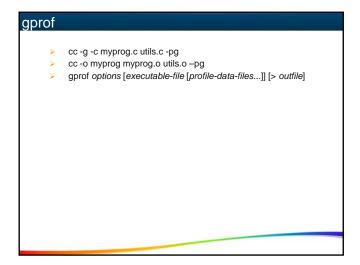
# grep*_lext*/proc/kallsyms

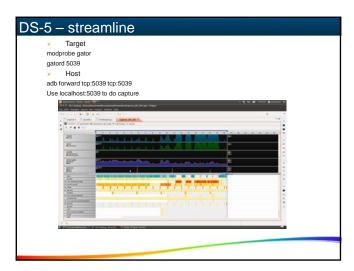
d02x5000 T_set

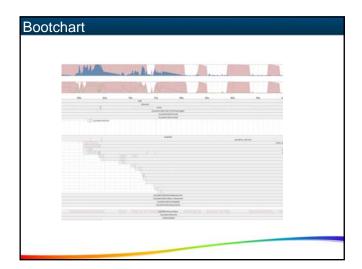
# grep*_lext*/proc/kallsyms

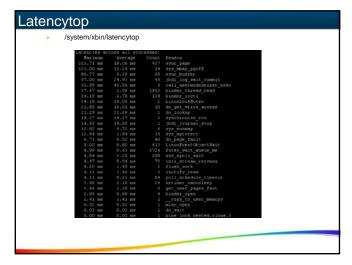
d02x5000 A_etext

# generical-formituse-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-influence-inf
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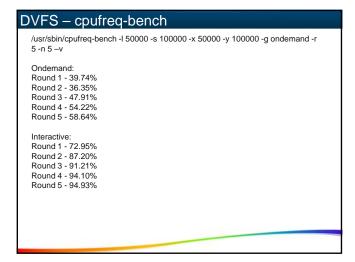


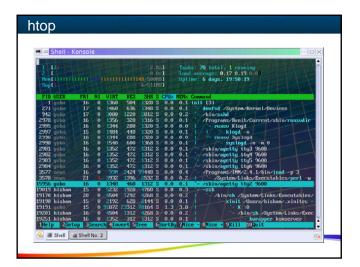


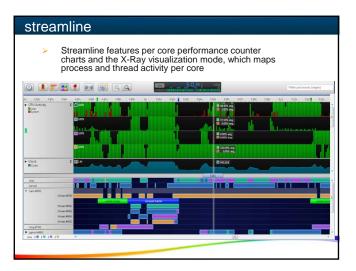




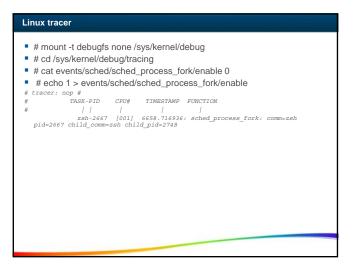








■ This tool acquires timer jitter by measuring accuracy of sleep and wake operations of highly prioritized realtime threads. # ./cyclictest -p 80 -t5 -n 1.58 1.61 1.62 3/68 4079 T: 0 (3131) P:80 I: 1000 C:16469865 Min: 8 Act: 13 Max: 62 T: 1 (3132) P:79 I: 1500 C: 9979903 Min: 8 Act: 18 Max: 75 T: 2 (3133) P:78 I: 2000 C: 7934887 Min: 9 Act: 22 Max: 83 T: 3 (3134) P:77 I: 2500 C: 6587910 Min: 9 Act: 25 Max: 81 T: 4 (3135) P:76 I: 3000 C: 5489925 Min: 9 Act: 27 Max: 86



Kernel programming

Partiers: Motivation The compiler can: Reorder code as long as it correctly maintains data flow dependencies within a function and with called functions. Reorder the execution of code to optimize performance. The processor can: Reorder instruction execution as long as it correctly maintains register flow dependencies. Reorder memory modification as long as it correctly maintains data flow dependencies. Reorder the execution of instructions (for performance optimization)

Barrier Operations

- . barrier prevent only compiler reordering
- . mb prevents load and store reordering
- · rmb prevents load reordering
- . wmb prevents store reordering
- smp_mb prevent load and store reordering only in SMP kernel
- smp_rmb prevent load reordering only in SMP kernels
- smp_wmb prevent store reordering only in SMP kernels
- set_mb performs assignment and prevents load and store reordering

Atomic Operations

- Many instructions not atomic in hardware (smp)
 - Read-modify-write instructions: inc, test-and-set, swap
 - Unaligned memory access
- Compiler may not generate atomic code
 - Even i++ is not necessarily atomic!
- If the data that must be protected is a single word, atomic operations can be used. These functions examine and modify the word atomically.
- The atomic data type is atomic_t.

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Atomic Ops

- Execute in a single instruction
- Can be used in or out of process context (i.e., softirgs)
- Never sleep
- Don't suspend interrupts

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Atomic Operations

```
ATOMIC_INIT – initialize an atomic_t variable atomic_read – examine value atomically atomic_set – change value atomically atomic_inc – increment value atomically atomic_dec – decrement value atomically atomic_add - add to value atomically atomic_sub – subtract from value atomically atomic_inc_and_test – increment value and test for zero atomic_dec_and_test – decrement value and test for zero atomic_sub_and_test – subtract from value and test for zero atomic_set_mask – mask bits atomically atomic_clear_mask – clear bits atomically
```

Atomic Bit Operations

Perform bit operations atomically. Can be done without disabling interrupts on most platforms.

set_bit change_bit
clear_bit test_bit

test_and_set_bit find_first_bit
test_and_clear_bit find_first_zero_bit
test_and_change_bit find_next_zero_bit

There also exist non_atomic versions with __ prefix, e.g., __set_bit. These are slightly faster than the atomic versions.

Serializing with Interrupts

- Basic primitive in original UNIX
- Doesn't protect against other CPUs
- Intel: "interrupts enabled bit"
 - cli to clear (disable), sti to set (enable)
- Enabling is often wrong; need to restore
 - local_irq_save()
 - local_irq_restore()

Interrupt Operations

local_irq_enable - enable interrupts on the current CPU

local_save_flags - return the interrupt state of the
processor

local_restore_flags - restore the interrupt state of the processor

 Dealing with the full interrupt state of the system is officially discouraged. Locks should be used.

IRQ request and enable

- request_irq()
- disable_irq()
- disable_irq_nosync()
- enable_irq()

Interrupt bottom half

- softirg/ksoftirgd
- tasklet
- work queue

Threaded_irq

 int request_threaded_irq (unsigned int irq, irq_handler_t handler, irq_handler_t threa d_fn, unsigned long irqflags, const char * devname, void * dev_id);

ARM GIC

- PPI : Private Peripheral Interrupt specific to a single processor
 - SPI : Shared Peripheral Interrupt

Distributor can route to any of a specified combination of processors

Software-generated interrupt (SGI)

This is an interrupt generated by software writing to a GICD_SGIR register in the GIC. The system uses SGIs for interprocessor communication

- ▶ ID0-ID15 are used for SGIs
- > ID16-ID31 are used for PPIs
- ▶ ID32+ are used for SPIs

gic_raise_softirq

- void <u>gic_raise_softirq</u>(const struct <u>cpumask</u> *<u>mask</u>, unsigned int <u>irq</u>)
- Platform:

set_smp_cross_call(gic_raise_softirq);

Kernel:

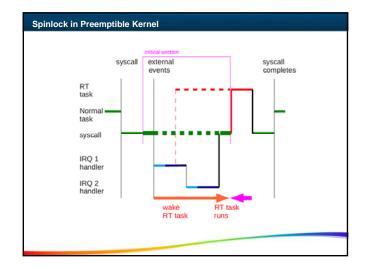
smp_cross_call(cpumask_of(cpu), IPI_RESCHEDULE);

```
    Kernel space
        irq_set_affinity(I2C0_IRQ, cpumask_of(0));
        irq_set_affinity(GPI00_IRQ, cpumask_of(1));

    Userspace
        [root@boss ~]# echo 01 > /proc/irq/145/smp_affinity
        [root@boss ~]# cat /proc/irq/145/smp_affinity
        00000001
```

Spin Locks

- A spin lock is a data structure (spinlock_t) that is used to synchronize access to critical sections.
- Only one thread can be holding a spin lock at any moment. All other threads trying to get the lock will "spin" (loop while checking the lock status).
- Spin locks should not be held for long periods because waiting tasks on other CPUs are spinning, and thus wasting CPU execution time.



Spin Locks & Interrupts

The spin lock services also provide interfaces that serialize with interrupts (on the current processor):

spin_lock_irq - acquire spin lock and disable interrupts spin_unlock_irq - release spin lock and reenable spin_lock_irqsave - acquire spin lock, save interrupt state, and disable

spin_unlock_irgrestore - release spin lock and restore interrupt state

RW Spin Locks

- A read/write spin lock is a data structure that allows multiple tasks to hold it in "read" state or one task to hold it in "write" state (but not both conditions at the same time).
- This is convenient when multiple tasks wish to examine a data structure, but don't want to see it in an inconsistent state.
- A lock may not be held in read state when requesting it for write state.
- The data type for a read/write spin lock is rwlock_t.
- Writers can starve waiting behind readers.

RW Spin Lock Operations

Several functions are used to work with read/write spin locks:

rwlock_init – initialize a read/write lock before using it for the first time

read_lock - get a read/write lock for read

write_lock - get a read/write lock for write

read_unlock - release a read/write lock that was held for read write_unlock - release a read/write lock that was held for write

read_trylock, write_trylock - acquire a read/write lock if it is currently free, otherwise return error

RW Spin Locks & Interrupts

 The read/write lock services also provide interfaces that serialize with interrupts (on the current processor):

read_lock_irq - acquire lock for read and disable
interrupts

read_unlock_irq - release read lock and reenable
read_lock_irqsave - acquire lock for read, save interrupt
state. and disable

read_unlock_irgrestore - release read lock and restore interrupt state

 Corresponding functions for write exist as well (e.g., write_lock_irqsave).

Semaphores

- A semaphore is a data structure that is used to synchronize access to critical sections or other resources.
- A semaphore allows a fixed number of tasks (generally one for critical sections) to "hold" the semaphore at one time. Any more tasks requesting to hold the semaphore are blocked (put to sleep).
- A semaphore can be used for serialization only in code that is allowed to block.

Semaphore Operations

Operations for manipulating semaphores:

up – release the semaphore

down - get the semaphore (can block)

down_interruptible - get the semaphore, but return
whether we blocked

down_trylock - try to get the semaphore without blocking, otherwise return an error

Semaphores

- optimized assembly code for normal case (down())
 - C code for slower "contended" case (__down())
- up() is easy
 - atomically increment; wake_up() if necessary
- uncontended down() is easy
 - atomically decrement; continue
- contended down() is really complex!
 - basically increment sleepers and sleep
 - loop because of potentially concurrent ups/downs
- still in down() path when lock is acquired

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Mutexes

- A mutex is a data structure that is also used to synchronize access to critical sections or other resources, introduced in 2.6.16.
- Core difference: only 1 owner, while semaphores can have multiple owners
- Historically, semaphores have been used in the kernel, but now mutexes are encouraged, unless counting feature is really required
- As of 2.6.26, major effort to eliminate semaphores completely, and may eventually disappear
- Replace remaining instances with completions

Why Mutexes?

Documentation/mutex-design.txt

- Pros
 - Simpler (lighter weight)
 - Tighter code
 - Slightly faster, better scalability
 - No fastpath tradeoffs
 - Debug support strict checking of adhering to semantics (if compiled in)
- Cons
 - Not the same as semaphores
 - Cannot be used from interrupt context
 - Owner must release

Mutex Operations

Operations for manipulating mutexes:

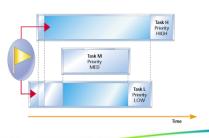
mutex_unlock - release the mutex
mutex_lock - get the mutex (can block)
mutex_lock_interruptible - get the mutex, but allow
interrupts
mutex_trylock - try to get the mutex without blocking,

otherwise return an error

mutex_is_locked – determine if mutex is locked

Priority inversions

when a medium-priority task preempts a lower-priority task using a shared resource on which the higher-priority task is pending. If the higher-priority task is otherwise ready to run, but a medium-priority task is currently running instead, a priority inversion is said to occur.



Priority ceiling protocol

• give each shared resource a predefined priority ceiling. When a task acquires a shared resource, the task is hoisted (has its priority temporarily raised) to the priority ceiling of that resource. It will not see whether the job has been blocked or not, simply it raises to the priority of the shared resource.

RT-mutex - priority inheritance

- RT-mutexes extend the semantics of simple mutexes by the priority inheritance protocol.
- A low priority owner of a rt-mutex inherits the priority of a higher priority waiter until the rt-mutex is released. If the temporarily boosted owner blocks on a rt-mutex itself it propagates the priority boosting to the owner of the other rt_mutex it gets blocked on. The priority boosting is immediately removed once the rt_mutex has been unlocked.

rt_mutex - APIs

- void rt_mutex_init(struct rt_mutex *lock);
- void rt_mutex_destroy(struct rt_mutex *lock);
- void rt_mutex_lock(struct rt_mutex *lock);
- int rt_mutex_lock_interruptible(struct rt_mutex *lock, int detect_deadlock);
- int rt mutex timed lock(struct rt mutex *lock, struct hrtimer sleeper *timeout,int detect deadlock);
- int rt_mutex_trylock(struct rt_mutex *lock);
- void rt mutex unlock(struct rt mutex *lock);
- int rt_mutex_is_locked(struct rt_mutex *lock);

PI-futex - Lightweight PI-futexes

- in the user-space fastpath a PI-enabled futex involves no kernel work (or any other PI complexity) at all. No registration, no extra kernel calls - just pure fast atomic ops in userspace.
- even in the slowpath, the system call and scheduling pattern is very similar to normal futexes.
- the in-kernel PI implementation is streamlined around the mutex abstraction, with strict rules that keep the implementation relatively simple: only a single owner may own a lock (i.e. no read-write lock support), only the owner may unlock a lock, no recursive locking, etc.

PTHREAD_PRIO_INHERIT

- A real-time system cannot be real-time if there is no solution for priority inversion, this will cause undesired latencies and even deadlocks. On Linux there is a solution for it in user-land since kernel version 2.6.18 together with Glibc 2.5 (PTHREAD_PRIO_INHERIT).
- int pthread_mutexattr_setprotocol(pthread_mutexattr_t
 *attr, int protocol);
- ✓ PTHREAD PRIO NONE: A thread's priority and scheduling are not affected by the mutex ownership.
- allected by the mutex ownership.

 **PTHREAD_PRIO_INHERIT: This protocol value affects a thread's (such as thrdī) priority and scheduling when higher-priority threads block on one or more mutexes owned by thrdī where those mutexes are initialized with PTHREAD_PRIO_INHERIT. thrdī runs with the higher of its priority or the highest priority of any thread waiting on any of the mutexes owned by thrdī.

Completions

- Higher-level means of waiting for events
- Optimized for contended case

init_completion // replaces sema_init
complete // replaces up
wait_for_completion // replaces down
wait_for_completion_interruptible
wait_for_completion_timeout
wait_for_completion_interruptable_timeout

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The Big Kernel Lock (BKL)

- For serialization that is not performance sensitive, the big kernel lock (BKL) was used
 - This mechanism is historical and should generally be avoided.
 - The function *lock_kernel* gets the big kernel lock.
 - The function unlock_kernel releases the big kernel lock.
 - The function kernel_locked returns whether the kernel lock is currently held by the current task.
 - The big kernel lock itself is a simple lock called kernel_flag.

2.6.39: Ding Dong, the Big Kernel Lock is Dead

- The Big Kernel Lock was almost removed in the 2.6.37 kernel. With the 2.6.39 kernel, the BKL is finally gone with a patch from Arnd Bergmann.
- The big kernel lock (BKL) is an old serialization method that we are trying to get rid of, replacing it with more fine-grained locking, in particular mutex, spinlock and RCU, where appropriate.

Jiffies

- In /usr/src/linux/include/linux/jiffies.h: - unsigned long volatile jiffies
- Until 2.6.21, jiffies was just a counter that was incremented every clock interrupt
- Tickless kernels removed need for kernels to process timer interrupts on idle systems
 - http://www.lesswatts.org/projects/tickless

ΗZ

- Determines how frequently the clock interrupt fires
- Default is 1000 on x86, or 1 millisecond
- Configurable at compile time or boot time
 - Other typical values are 100 (10 ms) or 25 (4)
- What's a good value for HZ?
 - Low values: less overhead
 - Good for idle systems (power)
 - Good for busy systems (thpt)
 - High values: better resolution
 - Ability to retake control in scheduler (clock interrrupt)
 - · Good for interactivity
 - Tradeoff between responsiveness and throughput, running debate

Jiffies and HZ

- Again, incremented HZ times a second
- Jiffies can wrap around depending on platform
 - 32 bits, 1000 HZ: about 50 days
 - 64 bits, 1000 HZ: about 600 million years
- Jiffies_64:
 - On 32 bits, jiffies points to low-order 32 bits, jiffies_64 to high-order bits (be careful about atomicity!)
 - On 64 bit machines, jiffies == jiffies_64

Some Useful Functions

- Macros to compare jiffies values:
 - time_after(a,b)
 - time before(a,b)
 - time_after_eq(a,b)
- time before(a,b)
- Macros to convert jiffies to/from others:
 - timespec_to_jiffes(struct timespec * ts)
 - jiffies_to_timespec(unsigned long jiffies, struct
 timespec * ts)
 timeval_to_jiffes(struct timeval * tv)

 - jiffies_to_timeval(unsigned long jiffies, struct timeval * tv)
 - jiffies_to_msecs(unsigned long)
 - msecs_to_jiffies(int)
 - jiffies_to_usecs(unsigned long)
 usecs_to_jiffies(int)

Inserting Delays

- #include <linux/delay>
- void mdelay(unsigned long milliseconds)
- void udelay(unsigned long microseconds)
- void ndelay(unsigned long nanoseconds)
- void msleep(unsigned int milliseconds)
- unsigned long msleep_interruptible(unsigned int milliseconds)
 - returns number of seconds left if interrupted

Timers

- Runs via softirq like tasklets, but at a specific time
- A timer is represented by a timer_list:

```
struct timer_list {
  struct list_head entry; /* dbly linked */
  unsigned long expires; /* In jiffies */
  void (*function)(unsigned long);
  unsigned long data; /* optional */
  struct tvec_t_base_s *base;
);
```

expires is an absolute value, not a relative one

```
void init_timer(struct
    timer_list *timer);

void add_timer(struct
    timer_list *timer);

int del_timer(struct
    timer_list *timer);

int del_timer_sync(struct
    timer_list *timer);

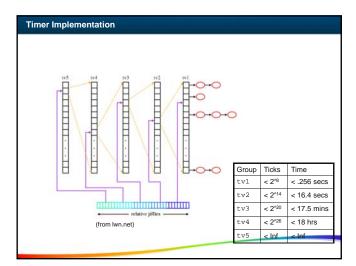
void mod_timer(struct
    timer_list *timer);

void mod_timer(struct
    timer_list *timer, unsigned
    long expires);

int timer_pending(const
    struct timer_list *timer);

// Changes expiry to new value

// 1 if timer is pending, 0
    otherwise
```



High Resolution Timers

- Motivated by the observation of 2 types of timers:
 - Timeout functions, which we don't expect to actually happen (e.g., retransmisison timer for packet loss). Have low resolution and are usually removed before expiration.
 - Timer functions, which we do expect to run. Have high resolution requirements and usually expire
- Original timer implementation is based on jiffies and thus depends on HZ. Works well for timeouts, less so for timers.
 - Resolution no better than HZ (e.g., 1 millisecond)
- High resolution timers, introduced in 2.6.16, allow 1 nanosecond resolution
- Implemented in an red-black tree (rbtree)
 - $-\,$ Insert, delete, search in O(log $\it n$) time

```
* extern void hrtimer init(struct
hrtimer *timer, clockid_t
which_clock, enum hrtimer_mode
mode),
int hrtimer cancel(struct
hrtimer *timer);
int
hrtimer_try to_cancel(struct
hrtimer *timer);
extern ktime_t
hrtimer_get_remaining(const
struct hrtimer *timer);
extern int
hrtimer_get_res(const clockid_t
which_clock, struct timespec
*tp);
*static inline int
hrtimer_active(const struct
hrtimer_active(const struct
hrtimer *timer)
// initialize hrtimer
// cancel timer (wait if
necessary)

// cancel timer (don' t
wait)

// time left before it
expires
// get resolution in ns

// 1 if active, 0
otherwise
```

Restarting Timers

- Many timers want to restart
- Function passed to hrtimer start needs to return a particular value:

```
num hrtimer_restart {
   HRTIMER_NORESTART,
   HRTIMER_RESTART,
                                                /* Timer is not restarted *
```

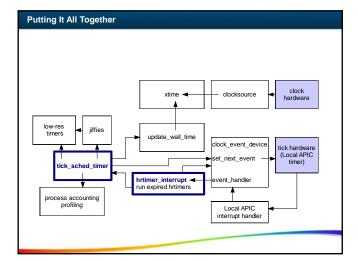
- RESTART used by functions that need a callback at a regular interval.
- The hrtimer code provides a function for advancing the expiration time to the next such interval:

unsigned long hrtimer_forward(struct hrtimer *timer, ktime_t
 interval);

- Advances the timer's expiration time by the given interval.
- The need to add the interval more than once usually means that the system has overrun its timer period, perhaps as a result of high system load.
 The return value from hrtimer_forward() is the number of missed intervals, allowing code which cares to detect and respond to the situation.

Dyntick/`Tickless'' Kernel

- Observation: there is no need for a periodic tick in the system, particularly when the system is idle
 - Idle CPUs save power
 - Why wake up an idle CPU 1000 times a second?
 - Especially if it is virtualized?
- Instead, when going into idle loop, check next pending timer:
 - If event is further away than one tick, re-program clock timer to fire when that event is due
 - Other interrupts can still wake up the system
- Added in 2.6.21
- CONFIG NO HZ
- New function added:
 - void init timer deferrable(struct timer list *timer);
 - These timers are ignored for purposes of calculating wakeup time



hrtimer -users

- The primary users of precision timers are user-space applications that utilize nanosleep, posix-timers and itimer interfaces. Also, in-kernel users like drivers and subsystems which require precise timed events(e.g. multimedia) can benefit from the availability of a separate highresolution timer subsystem as well.
- The hrtimer patch converts the following kernel functionality to usehrtimers:
- nanosleep
- Itimers
- posix-timers

The conversion of nanosleep and posix-timers enabled the unification of nanosleep and clock_nanosleep.

Is it still ticking?

- The timer tick is now an hrtimer
 - This allows it it coexist with the other hrtimers
 - Can be programmed in a flexible way, e.g. can be delayed for a while when the system is idle. (NO_HZ)
- So, is it still ticking?
 - hrtimers will make the hardware tick
 - Timer ticks, however, will happen only if needed, therefore less frequently

What's Your Kernel Running?

- grep for HZ in the .config for your kernel
 - What is the value of CONFIG HZ?
 - Is CONFIG_NOHZ set? (tickless kernel)
- cat /proc/interrupts
 - Look at the timer interrupt (irq 0). If you see a low number at irq 0, one of the local APICs is handling the global timer tick duties (local timer interrupts)
 - If CONFIG_NOHZ is not set, you can divide the number of timer or local timer interrupts by the number of seconds uptime, the result should be larger than HZ.
 - If CONFIG_NOHZ is set the number of timer interrupts will most likely be lower

Wait Queue

- Wait queues implement conditional waits on events:
 - a process wishing to wait for a specific event
 - places itself in the proper wait queue and
 - relinquishes control.
- Therefore, a wait queue represents a set of sleeping processes, which are woken up by the kernel when some condition becomes true.
- The condition could be related to:
 - an interrupt, such as for a disk operation to terminate
 - process synchronization
 - timing: such as a fixed interval of time to elapse

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Wait Queue Implementation

- Wait queues are implemented as doubly linked lists whose elements include pointers to process descriptors.
- Each wait queue is identified by a wait queue head, a data structure of type wait_queue_head_t:

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Wait Queue Synchronization

- Since wait queues are modified
 - by interrupt handlers
 - as well
 - by major kernel functions,

the doubly linked lists must be protected from concurrent accesses, which could induce unpredictable results.

 Synchronization is achieved by the lock spin lock in the wait queue head.

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Declare a New Wait Queue Head

- A new wait queue head may be defined by using the DECLARE_WAIT_QUEUE_HEAD(name) macro, which
 - statically declares a new wait queue head variable called name
 and
 - initializes its lock and task_list fields.

```
#define __WAIT_QUEUE_HEAD_INITIALIZER(name) {
    .lock = SPIN_LOCK_UNLOCKED,
    .task_list = { &(name).task_list, &(name).task_list } }
#define DECLARE_WAIT_QUEUE_HEAD(name)
wait_queue_head_t name=__WAIT_QUEUE_HEAD_INITIALIZER(name)
```

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Declare a New Wait Queue Element

- Alternatively, the **DEFINE_WAIT** macro:
 - declares a new wait_queue_t variable.
 - initializes it with the descriptor of the process currently executing on the CPU.

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Functions That Can Put a Process to a Wait Queue

- A process wishing to wait for a specific condition can invoke any of the functions shown in the following list.
 - -sleep_on()
 - interruptible_sleep_on()
 - sleep_on_timeout()
 - interruptible_sleep_on_timeout()
 - wait_event and wait_event_interruptible
 macros

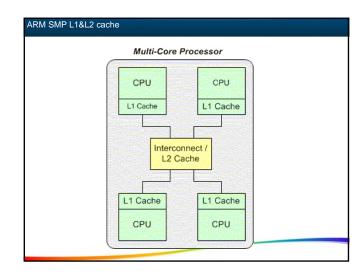
```
The sleep_on() function
void sleep_on(wait_queue_head_t *wq)
{ wait_queue_t wait;

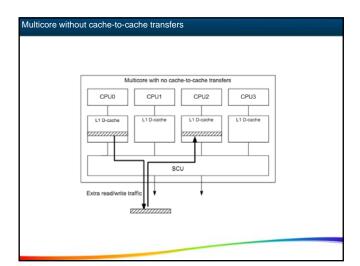
init_waitqueue_entry(&wait, current);
    current->state = TASK_UNINTERRUPTIBLE;
    add_wait_queue(wq, &wait);

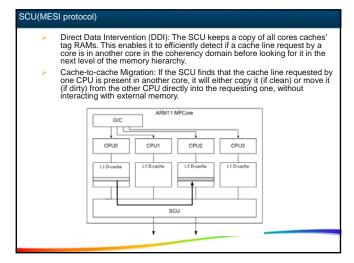
/* wq points to the wait queue head */
    schedule();
    remove_wait_queue(wq, &wait);
}

(1) Remove current from the runqueue.

(2) In order to make schedule() resume its execution, there must be some other kernel control path setting this process back to TASK_RUNNING state and putting it back to the runqueue after (1) is executed.
```







CP15 cache maintenance operations on the current CPU don't affect the cache of the other CPU Another CPU writes the data to a cached memory buffer. The DMA restart operation is done by the current CPU which does a cache clean operation. None of the modified cache lines on the other CPU are cleaned, causing stale data to be transferred. Current CPU invalidates the cache before an incoming DMA transfer writes new data. The CPU then reads the transferred data from the cacheable memory location. If the cache of another CPU contains an address within the DMA buffer, the SCU may take the stale data directly from the cache on that CPU with no access to external memory. Current CPU writes to the uncached mapping but cache lines in the modified state on the other CPU may be evicted to Level 2 cache or main memory, corrupting part of the data written by the current CPU. An external device writes to memory, but cache lines in the modified state on another CPU may be evicted, over-writing parts of the data written by the external device.

DMA coherent(Cortex-A9 MPCore) On Cortex-A9 MPCore, cache maintenance operations can be broadcast by hardware to other CPUs in the inner shareable domain. The Accelerator Coherency Port (ACP) and I/O Coherency The Accelerator Coherency Port (ACP) is a optional feature of Cortex-A9, which provides an 64-bit AXI slave port that can be connected to a DMA engine, providing the DMA access to the SCU of Cortex-A9. Addresses on the ACP port are physical addresses which can be snooped by the SCU to provide full I/O coherency. Reads on the ACP port will hit in any CPU's L1 D-cache, and writes on the ACP port will invalidate any stale data in L1 and write through to L2.

Using large dma-coherent buffers

- void *dma_alloc_coherent(struct device *dev, size_t size, dma_addr_t *dma_handle, gfp_t flag)
- void dma_free_coherent(struct device *dev, size_t size, void *cpu_addr, dma_addr_t dma_handle)

Streaming DMA mappings

- dma_addr_t dma_map_single(struct device *dev, void *cpu_addr, size_t size, enum dma_data_direction direction)
- void dma_unmap_single(struct device *dev, dma_addr_t dma_addr, size_t size, enum dma_data_direction direction)
- int dma_map_sg(struct device *dev, struct scatterlist *sg, int nents, enum dma_data_direction direction)
- void dma_unmap_sg(struct device *dev, struct scatterlist *sg, int nhwentries, enum dma_data_direction direction)

Infrastructural APIs

- Device tree based BSP and drivers model
- Device tree
- Common board files
- Platform device
- I²C client
- SPI client
- New Infrastructural APIs
- Pinctrl API
- Clock API
- DMA API
- Irq API

GPIO API

Linux system call

Linux System Calls

- System calls are low level functions the operating system makes available to applications via a defined API (Application Programming Interface)
- System calls represent the interface the kernel presents to user applications.
- In Linux all low-level I/O is done by reading and writing file handles, regardless of what particular peripheral device is being accessed—a tape, a socket, even your terminal, they are all files.
- Low level I/O is performed by making system calls.

From a Wrapper Routine to a System Call

- Unix systems include several libraries of functions that provide APIs to programmers.
- Some of the APIs defined by the libc standard C library refer to wrapper routines (routines whose only purpose is to issue a system call).
- Usually, each system call has a corresponding wrapper routine, which defines the API that application programs should employ.

Example of Different APIs Issuing the Same System Call

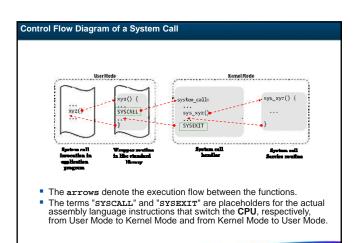
- In Linux, the malloc(), calloc(), and free()APIs are implemented in the libc library.
- The code in this library keeps track of the allocation and deallocation requests and uses the brk()/mmap() system call to enlarge or shrink the process heap.

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Operations Performed by a System Call

- The system call handler, which has a structure similar to that of the other exception handlers, performs the following operations:
 - Saves the contents of most registers in the Kernel Mode stack.
 - This operation is common to all system calls and is coded in assembly language.
 - Handles the system call by invoking a corresponding C function called the system call service routine.
 - Exits from the handler:
 - the registers are loaded with the values saved in the Kernel Mode stack
 - the CPU is switched back from Kernel Mode to User Mode.
 - This operation is common to all system calls and is coded in assembly language.

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System Call Dispatch Table

- To associate each system call number with its corresponding service routine, the kernel uses a system call dispatch table, which is stored in the sys_call_table array and has NR_syscalls entries.
- The nth entry contains the service routine address of the system call having number n.

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Ways to Invoke a System Call on x86

- Applications can invoke a system call in two different ways:
 - By executing the int \$0x80 assembly language instruction; in older versions of the Linux kernel, this was the only way to switch from User Mode to Kernel Mode.
 - By executing the sysenter assembly language instruction, introduced in the Intel Pentium II microprocessors; this instruction is now supported by the Linux 2.6 kernel.

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ARM EABI

- Old: The system call number was passed as part of the swi instruction, the kernel had to read and decode the swi instruction, polluting the data cache with instructions
- Now, the system call number is passed in r7
- CONFIG_OABI_COMPAT: In an EABlable kernel, provides compatibility with old ABI userspace binaries

Tracing System Calls

- Linux has a powerful mechanism for tracing system call execution for a compiled application
- Output is printed for each system call as it is executed, including parameters and return codes
- The ptrace() system call is used (same call used by debuggers for single-stepping applications)
- Use the "strace" command (man strace for info)
- You can trace library calls using the "Itrace" command

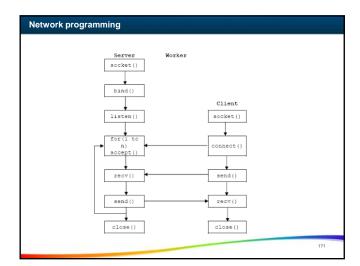
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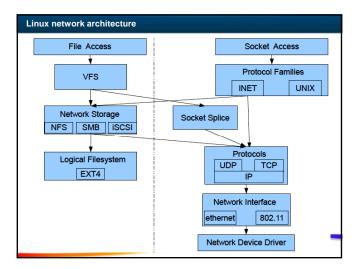
Linux socketcall

■ socketcall() is a common kernel entry point for the socket system calls. *call* determines which socket function to invoke. *args* points to a block containing the actual arguments, which are passed through to the appropriate call. User programs should call the appropriate functions by their usual names. Only standard library implementors and kernel hackers need to know about socketcall().

int socketcall(int call, unsigned long *args);

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Protocol family

- Implements different socket families INET, UNIX
- Socket Splice: Linux supports to send entire files between file descriptors, A descriptor can be a socket
- Protocols:Families have multiple protocols
 - INET: TCP, UDP
- pcap library for Linux uses PF_PACKET sockets

