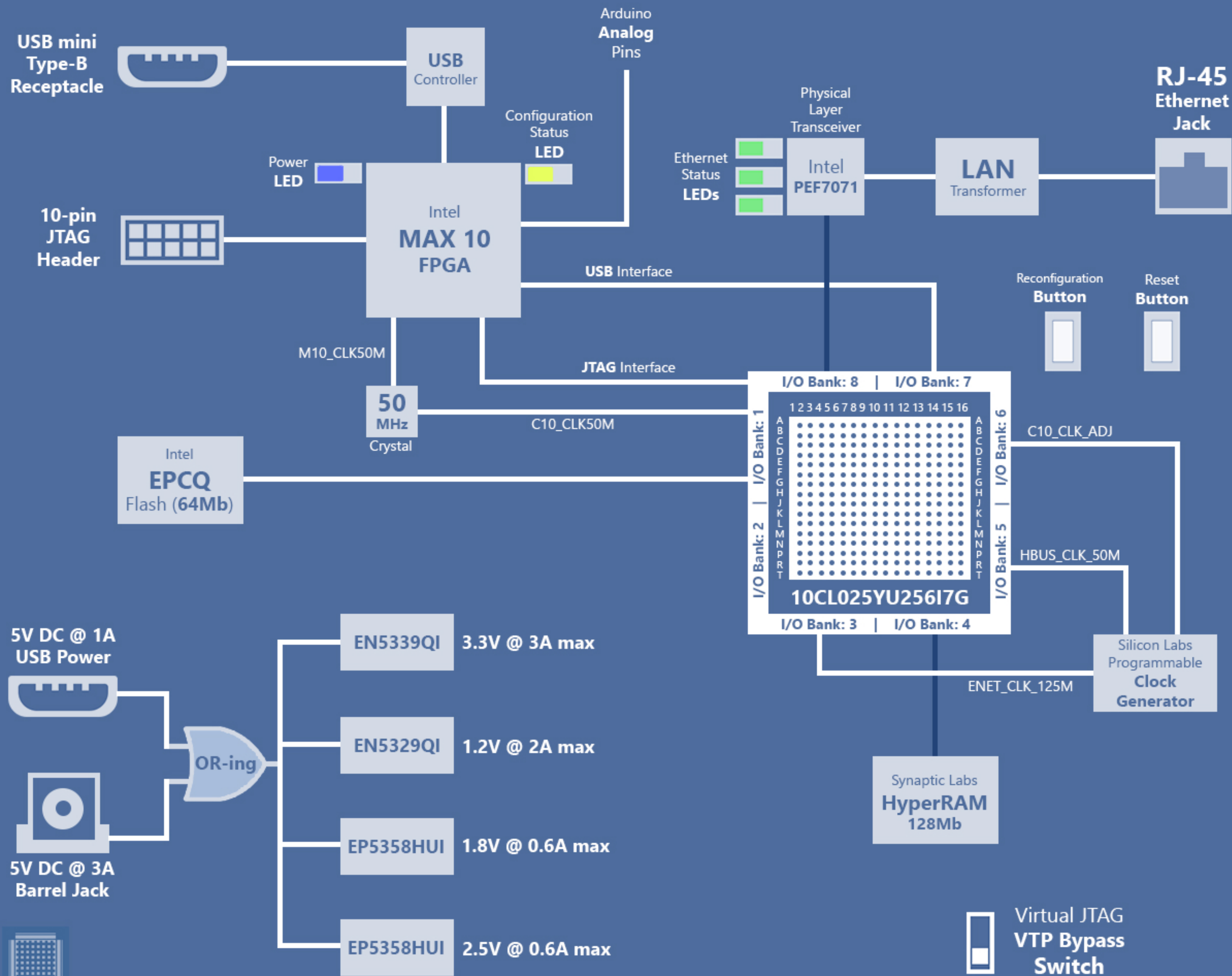


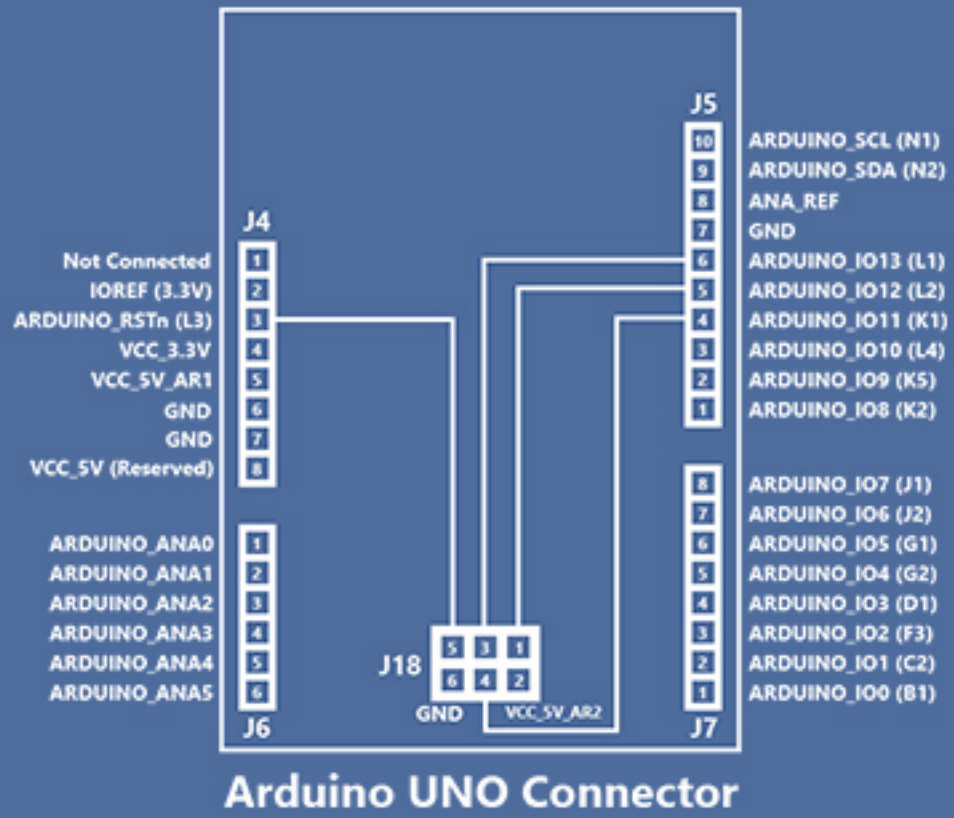
# Intel Cyclone 10 LP

## FPGA Evaluation Kit



**2x20 GPIO Header**

GPIO0 (L13)	1	2	GPIO1 (L16)
GPIO2 (L15)	3	4	GPIO3 (K16)
GPIO4 (P16)	5	6	GPIO5 (R16)
GPIO6 (N16)	7	8	GPIO7 (N15)
GPIO8 (N14)	9	10	GPIO9 (P15)
VCC_5V_GPIO	11	12	GND
GPIO10 (N8)	13	14	GPIO11 (P8)
GPIO12 (M8)	15	16	GPIO13 (L8)
GPIO14 (R7)	17	18	GPIO15 (T7)
GPIO16 (L7)	19	20	GPIO17 (M7)
GPIO18 (R6)	21	22	GPIO19 (T6)
GPIO20 (T2)	23	24	GPIO21 (M6)
GPIO22 (R5)	25	26	GPIO23 (T5)
GPIO24 (N5)	27	28	GPIO25 (N6)
VCC_3.3V	29	30	GND
GPIO26 (R4)	31	32	GPIO27 (T4)
GPIO28 (N3)	33	34	GPIO29 (P3)
GPIO30 (R3)	35	36	GPIO31 (T3)
GPIO32 (P6)	37	38	GPIO33 (P2)
GPIO34 (P1)	39	40	GPIO35 (R1)



### 2x20 GPIO Header

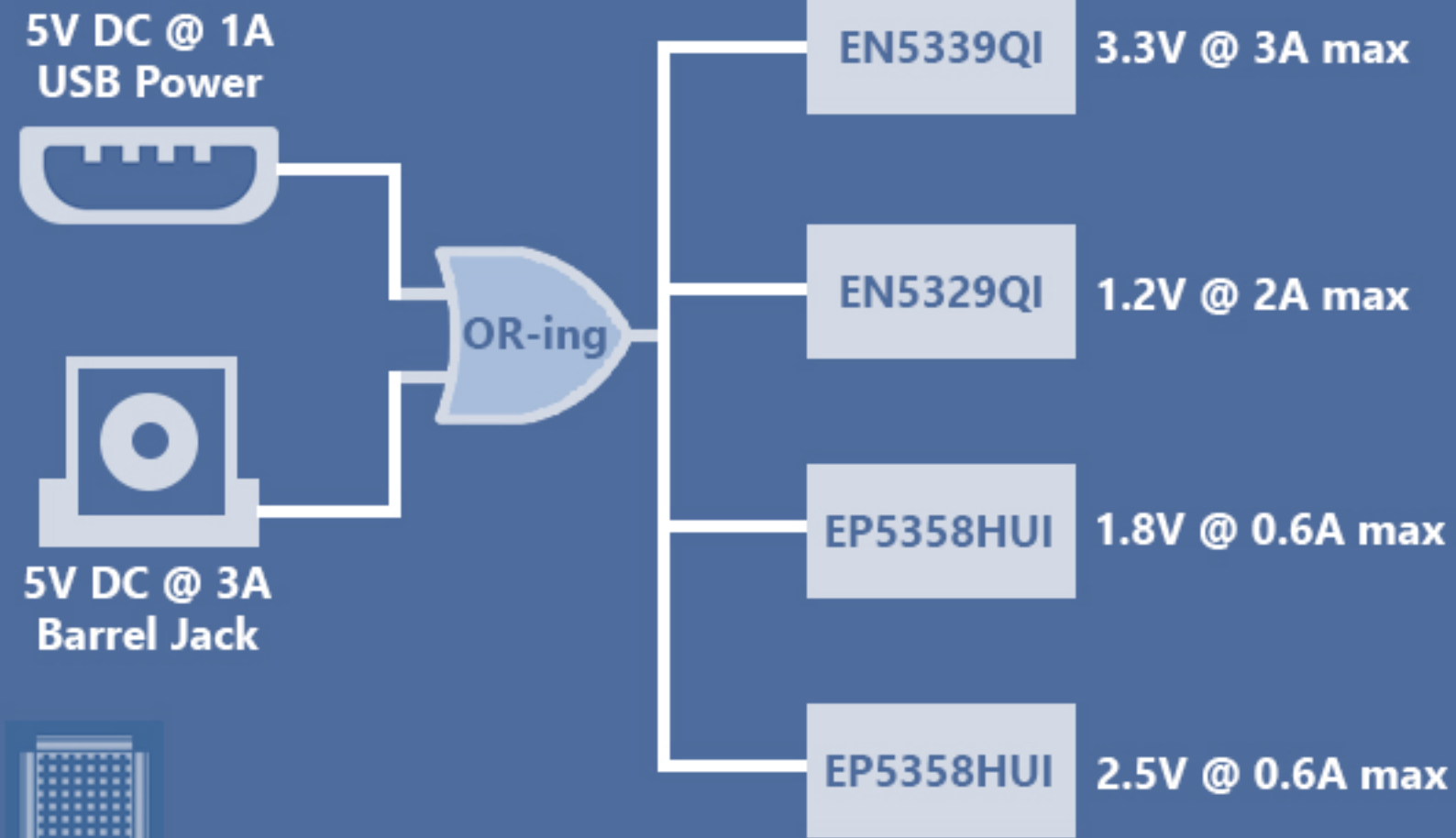
**Digilent PMOD Connector**

PMOD_D0 (D16)	1	7	PMOD_D4 (C16)
PMOD_D1 (F13)	2	8	PMOD_D5 (F15)
PMOD_D2 (D15)	3	9	PMOD_D6 (C15)
PMOD_D3 (F16)	4	10	PMOD_D7 (B16)
GND	5	11	GND
VCC_3.3V	6	12	VCC_3.3V

### User LEDs

### User Switches

### User Buttons



# Board Block Diagram