Kyle Clocker

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Education

2018 – Present
- **Ph.D., Electrical and Computer Engineering, Oregon State University** in Analog Mixed Signal Sensor and Integrated Circuit Design.

2015 – 2018
- **M.S., Electrical and Computer Engineering, Oregon State University** in Analog Mixed Signal Sensor and Integrated Circuit Design.
  Thesis title: *CMOS-Integrated Single-Element Thermal Flow Sensors.*

2011 – 2015
- **B.S., Electrical Engineering, University of North Texas** *Summa Cum Laude*
  Department Award for Outstanding Senior Student.
  College Award for Outstanding Graduating Senior.

Experience

2015 – Present
- **Oregon State University, Sensors and Integrated Microelectronics Lab** Graduate Research Assistant Advisor: Dr. Matthew L. Johnston
  - Design and test of impact detection unit for eagle collisions on wind turbines
  - Design and test of a single-element flow sensor integrated in a standard 180 nm CMOS process
  - Teaching Assistant for VLSI design and analog circuit design courses

2017
- **Intel** Graduate Technical Intern
  - Test and validation integrated ESD protection for future products

2016
- **Insitu** Electrical Hardware Itern
  - Designed schematic blocks for new avionics
  - Developed and trained team on LabVIEW test platform for automated use of test equipment
  - Evaluated and simulated PCB traces for signal integrity

2013-2016
- **Stryker Communications** Engineering Co-Op,Independent Engineering Contractor
  - Design, document, and develop prototypes to evaluate new KVM solutions
  - Coordinate the manufacture of prototypes with contract manufactures
  - Test and debug prototypes and current products

Selected Publications

Clocker, K., Sengupta, S., & Johnston, M. (2019). A fully-integrated, single-element cmos anemometer. *IEEE Sensors Letters*.

Clocker, K., Votzke, C., Mengüç, Y., & Johnston, M. L. (2019). Compact modeling of stretchable printed liquid metal electrical interconnects. In *2019 ieee international conference on flexible and printable sensors and systems (fleps)* (pp. 1–3). IEEE.

Technical Skills

EDA Tools
- Cadence Virtuoso, HSpice, Xilinx ISE, KiCad, MPLAB X, Visio

Programming
- Verilog, VHDL, C, C++, MatLab, Python, LaTeX, Git, LabView

Misc
- Micro Soldering, Oscilloscopes, High Speed PCB Layout