

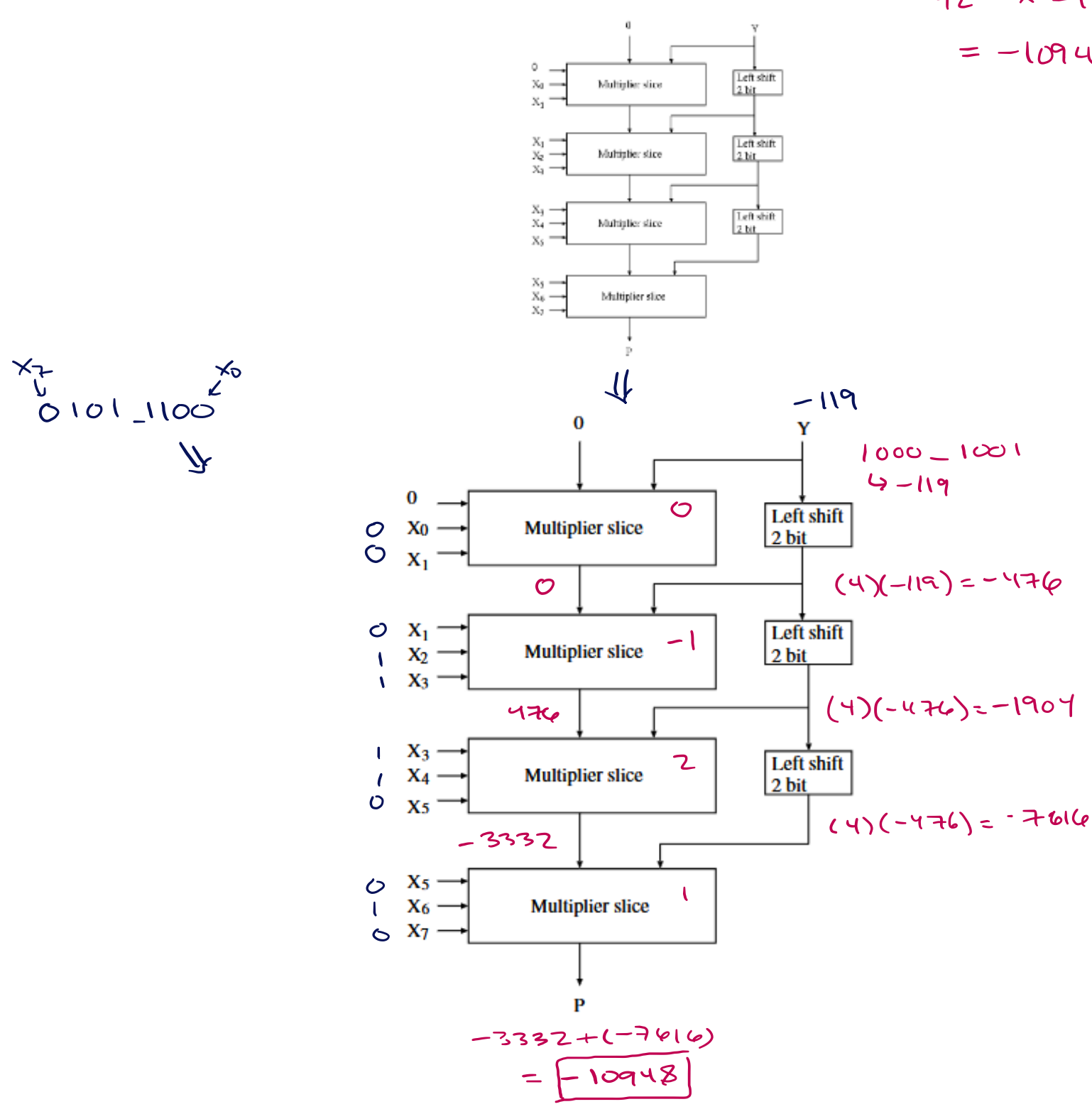
ECE428

HW5

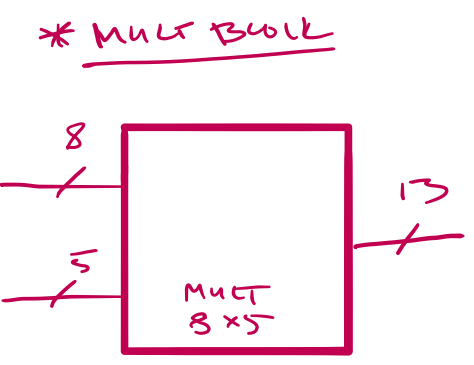
C. LOTTE
04/06/2025

1. Assume the Booth multiplier shown below is used to compute $0101\ 1100 \times 1000\ 1001$. Show the output of each multiplier slice. (50)

$$\begin{array}{r} 1000\ 1001 = 0111\ 0110 \\ \underline{0111\ 0111} \\ 117 \end{array}$$
$$\begin{array}{r} 92 \times -119 \\ = -10948 \end{array}$$



2. Use two 8-bit \times 5-bit signed multipliers (multiplying 2's complementary numbers) and a 15-bit adder to construct an 8-bit \times 8-bit signed multiplier. Draw the block diagram. Use it to compute $0101\ 1100 \times 1000\ 1001$. Show the output of each block (sub multipliers and the adder). (50)



$$A[7:0] = 0101\ 1100 = 92$$
$$B[7:0] = 1000\ 1001 = -119$$

$$A[7:0] \cdot B[7:0] = (A[7:4] \cdot 2^4 + \{0, A[3:0]\}) \cdot B[7:0]$$
$$= (A_7 A_6 A_5 A_4 A_3 \cdot 2^3 + 00 A_2 A_1 A_0) B[7:0]$$
$$= A_7 A_6 A_5 A_4 A_3 \cdot 2^3 \cdot B[7:0] + 00 A_2 A_1 A_0 \cdot B[7:0]$$
$$= \underbrace{A[7:3]}_{5\text{-bit}} \cdot \underbrace{2^3}_{L.S. 3} \cdot \underbrace{B[7:0]}_{8\text{-bit}} + \underbrace{00 A[2:0]}_{5\text{-bit}} \cdot \underbrace{B[7:0]}_{8\text{-bit}}$$

