

From: 0 sec To: 55 sec Marker — | Cursor 1 sec

Time 0 10 sec 20 sec 30 sec 40 sec 50 sec

mac\_tb

clk  
a[3:0]  
b[3:0]  
c[15:0]  
expected\_out[15:0]  
out[15:0]  
w\_dec  
x\_dec

Signal	0	6	7	1	3	2	15	4	6	4	0	2	1	10	2	11	2	6	4	10	
clk	XXX	6	7	1	3	2	15	4	6	4	0	2	1	10	2	11	2	6	4	10	
a[3:0]	XXX	1	-7	7	-4	-3	-2	-4	5	-6	4	2	-2	0	-4	3	-1	-4	5	-6	4
b[3:0]	XXX	0	6	-43	-36	-48	-54	-58	-118	-98	-134	-118	-122	-162	-156	-167	-175	-145	-169		
c[15:0]	0	6	-43	-36	-48	-54	-58	-118	-98	-134	-118	-122	-162	-156	-167	-175	-145	-169	-129		
expected_out[15:0]	0	6	-43	-36	-48	-54	-58	-118	-98	-134	-118	-122	-162	-156	-167	-175	-145	-169	-129		
out[15:0]	XXX	6	-43	-36	-48	-54	-58	-118	-98	-134	-118	-122	-162	-156	-167	-175	-145	-169	-129		
w_dec	XXX	1	-7	7	-4	-3	-2	-4	5	-6	4	2	-2	0	-4	3	-1	-4	5	-6	4
x_dec	XXX	6	7	1	3	2	15	4	6	4	0	2	1	10	2	11	2	6	4	10	

Type Signals

- reg a[3:0]
- reg b[3:0]
- parm bw
- reg c[15:0]
- reg clk
- reg expected\_out[15:0]
- integer i
- wire out[15:0]
- parm psum\_bw
- integer w\_dec
- integer w\_file
- integer w\_scan\_file
- integer x\_dec
- integer x\_file
- integer x\_scan\_file

Append Insert Replace

The screenshot shows the Vivado IDE's Timing Diagram window. The top toolbar includes icons for saving, undo, redo, and other standard editing functions. The main window displays a timing diagram for a 25-second period, with a time axis from 0 to 25 seconds. The diagram shows the timing of various signals, including mac\_predicted, mac\_wrapper\_instance, w\_bin, x\_bin, clk, a0[3:0], a1[3:0], a2[3:0], a3[3:0], b0[3:0], b1[3:0], b2[3:0], b3[3:0], c[15:0], expected\_out[15:0], out[15:0], w\_dec0, w\_dec1, w\_dec2, w\_dec3, x\_dec0, x\_dec1, x\_dec2, x\_dec3, bw, psum\_bw, i, w\_scan\_file, x\_scan\_file, x\_file, and w\_file. The diagram shows the relationship between these signals, with some signals having specific values indicated at various time points. For example, the 'a' signals (a0[3:0] to a3[3:0]) show a sequence of values from 0 to 15. The 'b' signals (b0[3:0] to b3[3:0]) show a sequence of values from 0 to 15. The 'c' signal (c[15:0]) shows a sequence of values from 0 to 15. The 'expected\_out' and 'out' signals show a sequence of values from 0 to 15. The 'w' signals (w\_dec0 to w\_dec3) show a sequence of values from 0 to 15. The 'x' signals (x\_dec0 to x\_dec3) show a sequence of values from 0 to 15. The 'bw' signal shows a sequence of values from 0 to 15. The 'psum\_bw' signal shows a sequence of values from 0 to 15. The 'i' signal shows a sequence of values from 0 to 15. The 'w\_scan\_file' signal shows a sequence of values from 0 to 15. The 'x\_scan\_file' signal shows a sequence of values from 0 to 15. The 'x\_file' signal shows a sequence of values from 0 to 15. The 'w\_file' signal shows a sequence of values from 0 to 15.