

An-Tai (Clyde) Li

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Education

Carnegie Mellon University (CMU) Master of Science in Software Engineering	Expected August 2013
National Taiwan University (NTU) Bachelor of Science in Electrical Engineering	June 2010

Additional Course Work

Human-Computer Interaction (HCI), Coursera	November 2012
■ Completed the online Human-Computer Interaction course hosted by Stanford University.	

Skills

System Oriented: C++, C#, C (Prior Experience), Java, Python
Web Oriented: JavaScript and jQuery(Proficient), HTML(5), CSS(3) and SASS, Ruby on Rails

Work Experience

Carnegie Mellon University Research Assistant	August 2012 - Present
■ Surveyed and implemented HTML5 web-application for sensor data visualization.	
■ Implemented HTML5 Hyperwall client and participated the design of database schema. The project is highlighted in the interoperation demo in CMU DMI Workshop 2012.	
Intel-NTU Connected Context Computing Center Research Assistant	January - June 2012
■ Designed method and system for managing SQL-NoSQL Hybrid Database. (Taiwan Patent Pending) .	
■ Surveyed and implemented a proxy server to enable Secure Index support in database.	

Awards / Honors

AngelHack - Silicon Valley	June 2013
■ Team won Intel and HP API prizes among over a hundred participated teams.	
Tizen Hack Competition	May 2013
■ Team won Runner Up (Second) prize among 20 participated teams.	
Hertz Mobility & Connected Vehicle Hackathon 2013	April 2013
■ Team won Nokia prize (\$1500 and 3 month nestGSV incubation).	
CMU-SV HTML5 Single Page App Hackathon	April 2013
■ Team won Both first-prize and the most popular app among over 50 registered participants.	
Invited to Google Taiwan's Mentorship, Google Taiwan	June 2010
■ Selected as 1 of 15 invited guests from Google Taiwan 2010 summer internship applicants.	

Publications

Books

- Chi-Lung Lee, and **An-Tai Li**. "Programming with Visual C# 2010 in 16 Lessons". Taipei, Taiwan: GOTOP Information Inc., 2011.
- Chi-Lung Lee, and **An-Tai Li**. "Making Things Easy with Google". Taipei, Taiwan: GrandTech Information Co., 2008.

Additional Research Experience

Minimal Register Usage Instruction Scheduling, NTU Fast Crypto Lab	August 2010
■ Developed a method to find optimal instruction schedule that use minimal number of registers. Able to handle approximately double instructions lines compared to prior research.	
CPU with Crypto Instructions, NTU Fast Crypto Lab	December 2009
■ Designed and implemented a CPU with Crypto instructions in hardware description level.	