An-Tai (Clyde) Li

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Education

Carnegie Mellon University (CMU) Expected August 2013
Master of Science in Software Engineering
National Taiwan University (NTU) June 2010
Bachelor of Science in Electrical Engineering

Additional Course Work

Human-Computer Interaction (HCI), *Coursera* November 2012
- Completed the online Human-Computer Interaction course hosted by Stanford University.

Skills

System Oriented: C++, C#, C (Prior Experience), Java, Python
Web Oriented: JavaScript and jQuery, HTML(5), CSS(3) and SASS, Ruby on Rails

Work Experience

Carnegie Mellon University August 2012 - Present
Research Assistant
- Surveyed and implemented HTML5 web-application for sensor data visualization.
- Implemented HTML5 Hyperwall client and participated the design of database schema. The project is highlighted in the interoperation demo in CMU DMI Workshop 2012.

Intel-NTU Connected Context Computing Center January 2012 - June 2012
Research Assistant
- Designed method and system for managing SQL-NoSQL Hybrid Database. **(Taiwan R.O.C. Patent Pending)**.
- Surveyed and implemented a proxy server to enable Secure Index support in database.

Honors

Hertz Mobility & Connected Vehicle Hackathon 2013 April 2013
- Team won Nokia prize ($1500 and 3 month nestGSV incubation).

CMU-SV HTML5 Single Page App Hackathon April 2013
- Team won Both first-prize and the most popular app among over 50 registered participants.

Invited to Google Taiwan's Mentorship, *Google Taiwan* June 2010
- Selected as 1 of 15 invited guests from Google Taiwan 2010 summer internship applicants.

Publications

Books
- Chi-Lung Lee, and **An-Tai Li**. "**Programming with Visual C# 2010 in 16 Lessons (Visual C# 2010 程式設計16堂課)**". Taipei, Taiwan: GOTOP Information Inc.(碁峰資訊), 2011.
- Chi-Lung Lee, and **An-Tai Li**. "**Making Things Easy with Google (用Google輕鬆過生活)**". Taipei, Taiwan: GrandTech Information Co.(上奇資訊), 2008.

Additional Research Experience

Minimal Register Usage Instruction Scheduling, *NTU Fast Crypto Lab* August 2010
- Developed a method to find optimal instruction schedule that use minimal number of registers. Able to handle approximately double instructions lines compared to prior research.

CPU with Crypto Instructions, *NTU Fast Crypto Lab* December 2009
- Designed and implemented a CPU with Crypto instructions in hardware description level.