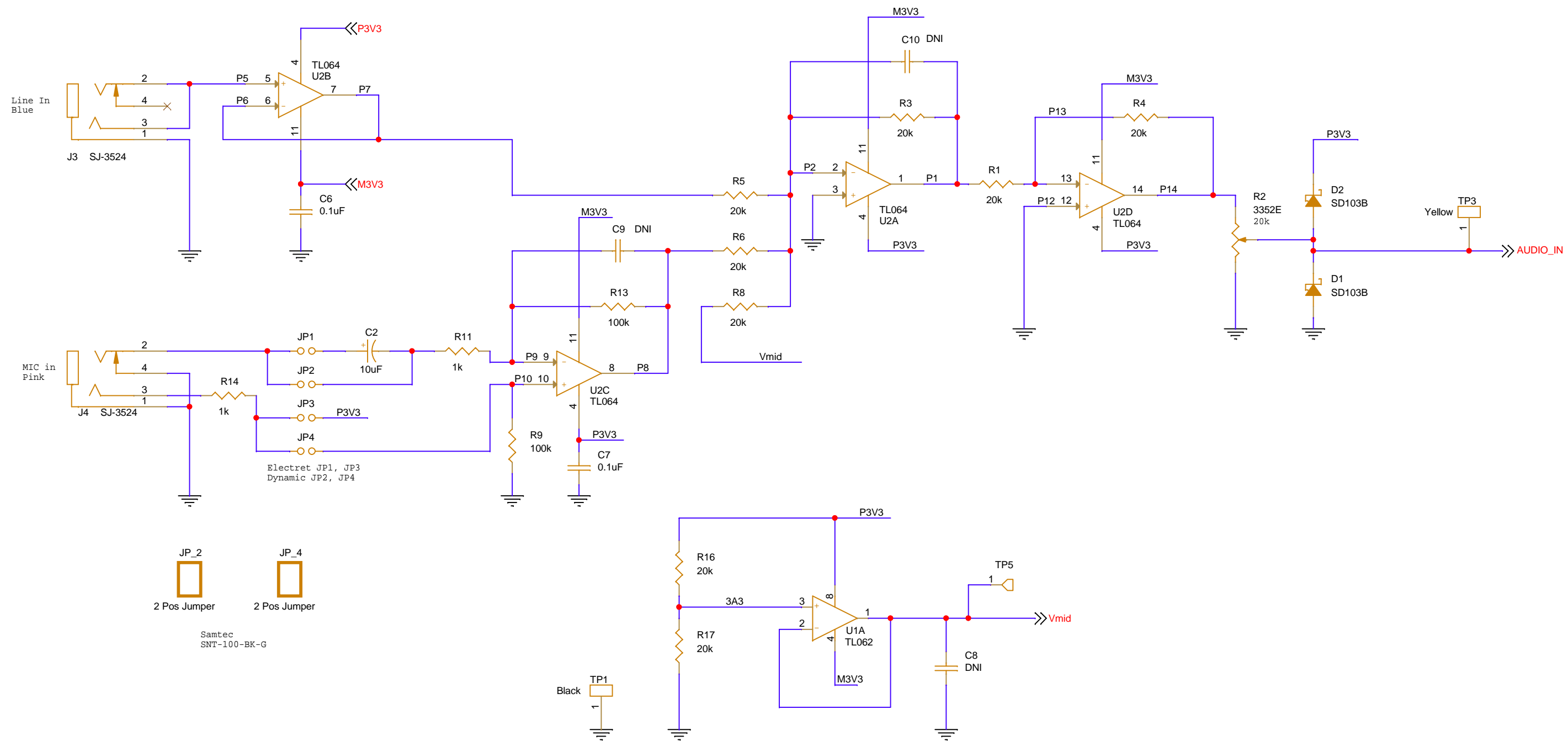
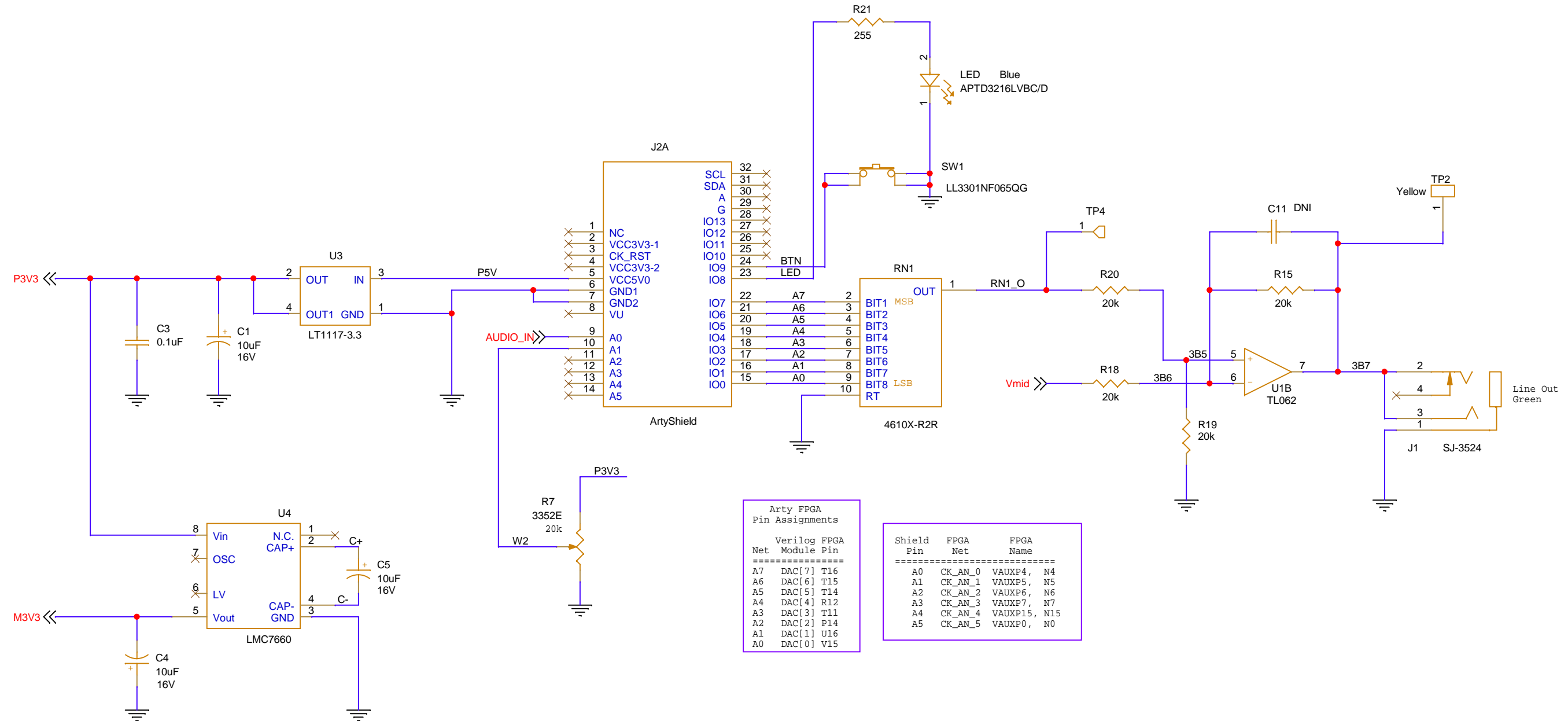


REV	SUMMARY OF CHANGES	DATE
1	INITIAL RELEASE	2019-11-24
2	Added test points.	2020-09-26



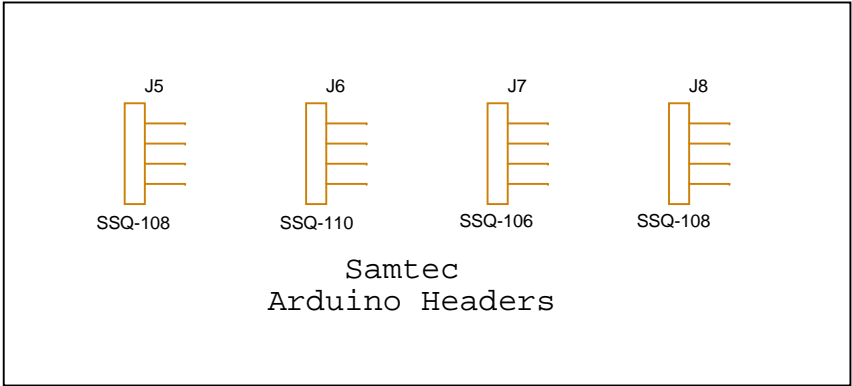
JP_2
2 Pos Jumper
JP_4
2 Pos Jumper
Samtec
SNT-100-BK-G

Microphone and Line Input		
Title Arty Adio Shield		
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Arty FPGA Pin Assignments		
Net	Verilog Module	FPGA Pin
=====		
A7	DAC[7]	T16
A6	DAC[6]	T15
A5	DAC[5]	T14
A4	DAC[4]	R12
A3	DAC[3]	T11
A2	DAC[2]	P14
A1	DAC[1]	U16
A0	DAC[0]	V15

Shield Pin	FPGA Net	FPGA Name
=====		
A0	CK_AN_0	VAUXP4, N4
A1	CK_AN_1	VAUXP5, N5
A2	CK_AN_2	VAUXP6, N6
A3	CK_AN_3	VAUXP7, N7
A4	CK_AN_4	VAUXP15, N15
A5	CK_AN_5	VAUXP0, N0



Power Supply and Line Output		
Title		
Arty Audio Shield		
Size B	Document Number	
	N/A	
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Sheet		2 of 2

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OFFENDING COMMAND: pdfmark

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