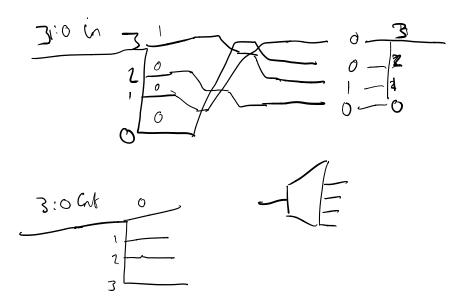
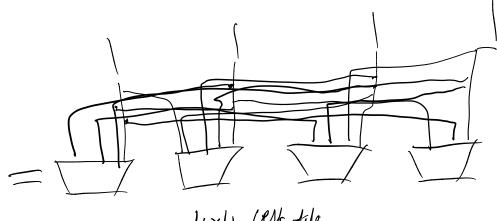
Rotate Left



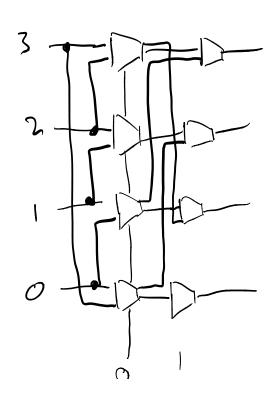
1000 2: 0010



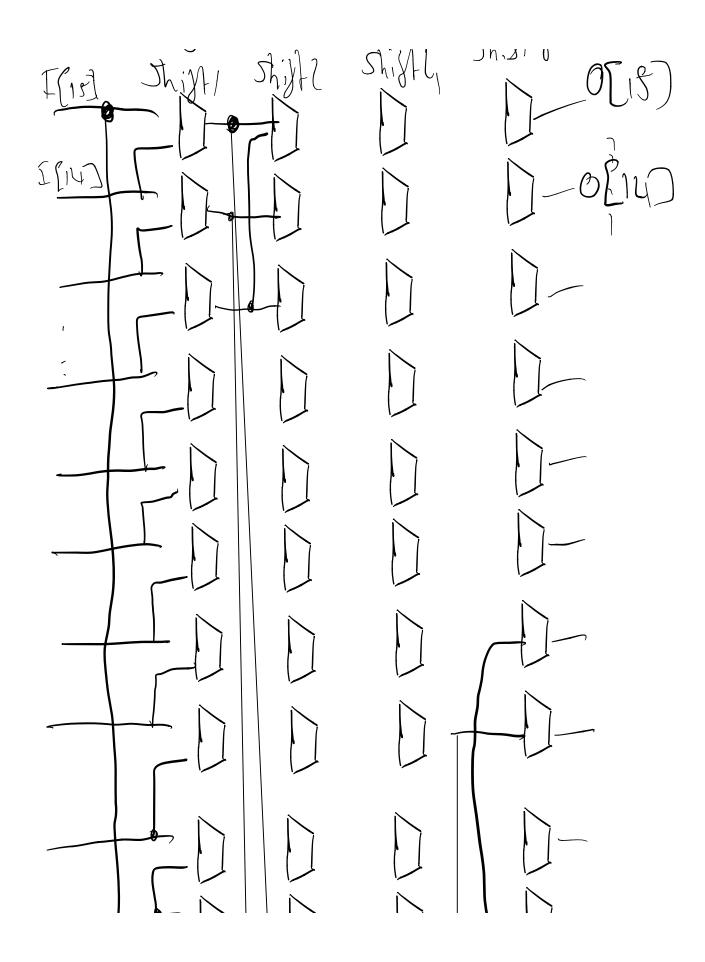
4×4 left dele

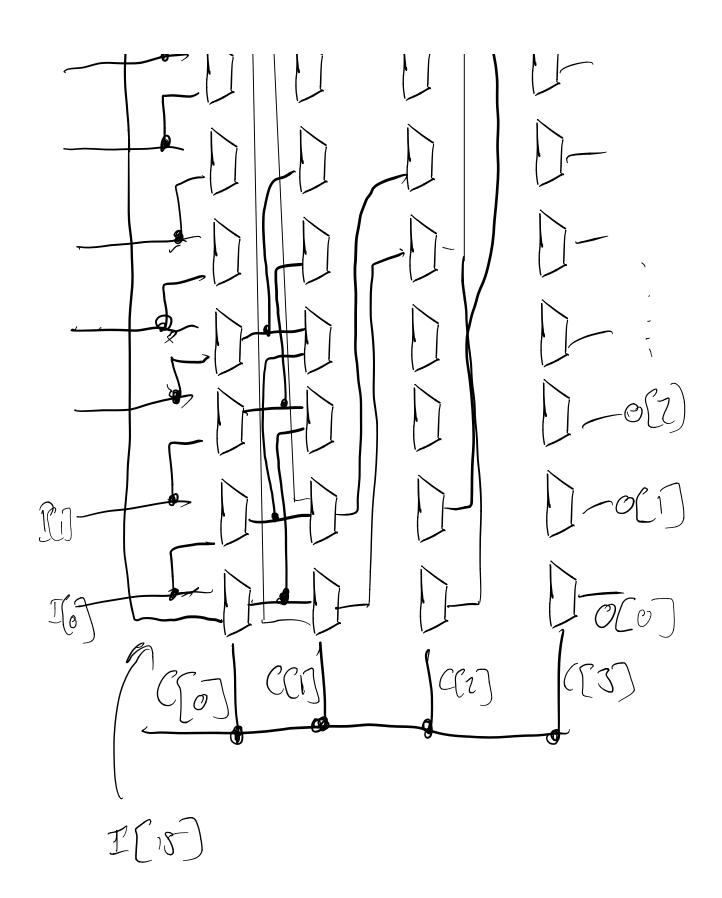
Module mux16_1 (in , shift, out)
in put [15:0] in
input [3:0] Shift
output
output
output
vire out
vire [3:0] shift
wire [3:0] in

alsign out=

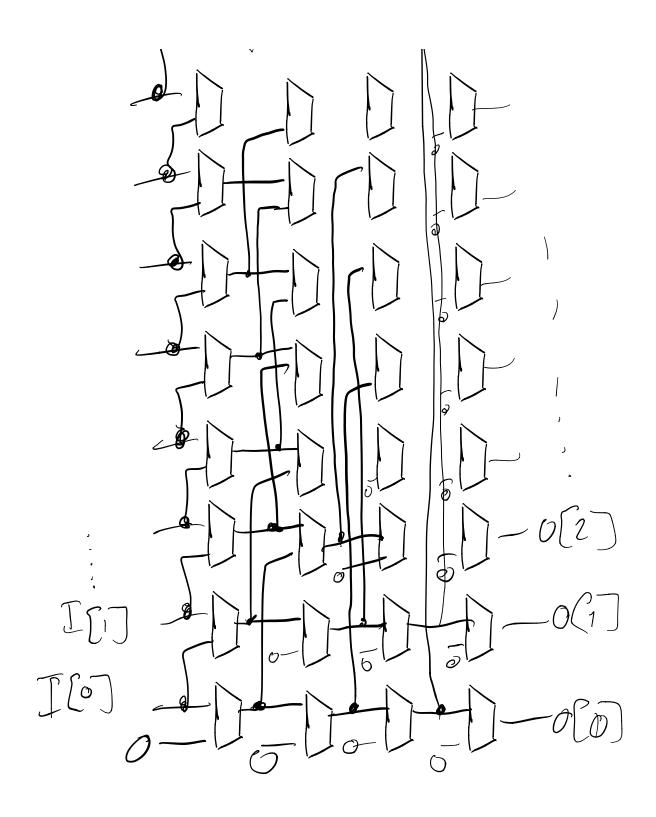


Rolls left





Shift Left



Shift Right Logical

<u></u> , _

