



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

ANANTAPUR - 515 002, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM. No. : JAC 091604

Serial No. : 900087001

Name : VADDE BAJARI

Course : ELECTRONICS & COMMUNICATION ENGINEERING

Admission No. : 092R1A0403

Year of Admission : 2009

Year of Completion : 2013

Name of the College : CRIET-RACHANAPALLI-ANANTAPUR

Class Awarded : First Class

S. No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S. No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS
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I YEAR

1 ENGINEERING CHEMISTRY	27	48	75	4	7 ENGINEERING DRAWING	27	32	59	6
2 MATHEMATICS-I	25	26	51	6	8 ENGINEERING & I.T. WORKSHOP	21	44	65	4
3 MATHEMATICAL METHODS	28	26	54	6	9 ENGINEERING PHYSICS AND ENGINEERING CHEMISTRY LAB	23	43	66	4
4 PROGRAMMING IN C AND DATA STRUCTURES	25	25	50	6	10C PROGRAMMING & DATA STRUCTURES LAB	23	38	61	4
5 ENGLISH	25	56	81	4	11ENGLISH LANGUAGE & COMMUNICATION SKILLS LAB	20	39	59	4
6 ENGINEERING PHYSICS	27	48	75	4					

I SEMESTER

II YEAR

II SEMESTER

* 1 SIGNALS AND SYSTEMS	18	25	43	4	1 PULSE & DIGITAL CIRCUITS	27	25	52	4
2 ELECTRICAL CIRCUITS	15	32	47	4	2 PRINCIPLES OF ELECTRICAL ENGINEERING	28	40	68	4
3 MATHEMATICS-III	20	30	50	4	3 ELECTROMAGNETIC THEORY & TRANSMISSION LINES	22	33	55	4
4 ELECTRONIC DEVICES AND CIRCUITS	19	25	44	4	4 SWITCHING THEORY & LOGIC DESIGN	25	38	63	4
5 ENVIRONMENTAL SCIENCE	22	38	60	4	5 MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	30	55	85	4
6 PROBABILITY THEORY & STOCHASTIC PROCESSES	27	25	52	4	6 ELECTRONIC CIRCUIT ANALYSIS	25	30	55	4
7 BASIC SIMULATION LAB	20	43	63	2	7 ELECTRICAL ENGINEERING LAB	23	44	67	2
8 ELECTRONIC DEVICES & CIRCUITS LAB	22	43	65	2	8 ELECTRONIC CIRCUIT ANALYSIS LAB	23	47	70	2

I SEMESTER

III YEAR

II SEMESTER

1 COMPUTER ORGANIZATION	29	41	70	4	1 MICROPROCESSORS & MICROCONTROLLERS	23	31	54	4
2 DIGITAL IC APPLICATIONS	30	52	82	4	2 DIGITAL SIGNAL PROCESSING	21	34	55	4
3 ANALOG COMMUNICATIONS	30	25	55	4	3 ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	28	46	74	4
4 CONTROL SYSTEMS	29	30	59	4	4 VLSI DESIGN	29	35	64	4
5 LINEAR IC APPLICATIONS	17	41	58	4	5 DIGITAL COMMUNICATIONS	30	29	59	4
6 ANTENNAS & WAVE PROPAGATION	23	32	55	4	6 MICROWAVE ENGINEERING	26	27	53	4
7 LINEAR & DIGITAL IC APPLICATIONS LAB	24	46	70	2	7 ANALOG & DIGITAL COMMUNICATIONS LAB	20	40	60	2
8 PULSE & DIGITAL CIRCUITS LAB	23	35	58	2	8 ADVANCED ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	23	41	64	2

I SEMESTER

IV YEAR

II SEMESTER

1 T.V. ENGINEERING	28	30	58	4	1 INFORMATION SECURITY	28	25	53	4
2 MANAGEMENT SCIENCE	28	42	70	4	2 CELLULAR & MOBILE COMMUNICATIONS	25	26	51	4
3 COMPUTER NETWORKS	29	25	54	4	3 DATA COMMUNICATIONS	25	46	71	4
4 EMBEDDED REAL TIME OPERATING SYSTEMS	28	31	59	4	4 SEMINAR	40	0	40	2
5 OPTICAL COMMUNICATIONS	27	28	55	4	5 PROJECT WORK	50	102	152	10
6 RADAR SYSTEMS	28	36	64	4	* 6 DIGITAL IMAGE PROCESSING	30	0	30	0
7 MICROWAVE & OPTICAL COMMUNICATIONS LAB	24	48	72	2					
8 MICROPROCESSORS & DSP LAB	22	46	68	2					

Number of Credits registered for : 220

Total Credits Acquired : 216

Aggregate Marks Secured for best : 3449

Percentage of Marks : 66.33%

Month of Declaration of Result : August 2013

(See overleaf for Instructions)

Max. Marks 5200

(*Courses registered but not counted for calculation of aggregate)

E. Kishore Kumar

CONTROLLER OF EXAMINATIONS