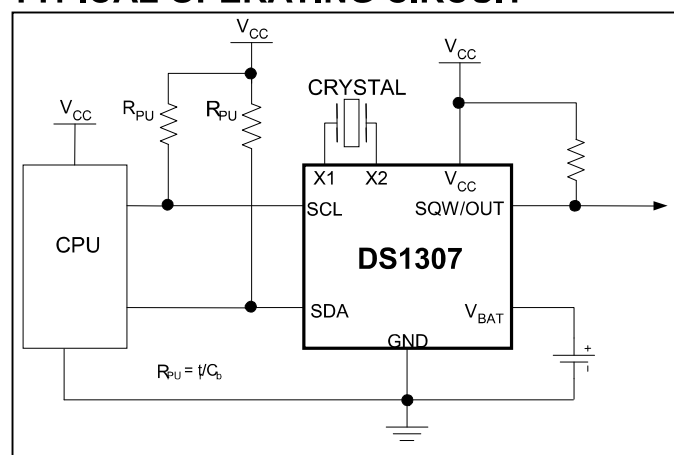


### GENERAL DESCRIPTION

The DS1307 serial real-time clock (RTC) is a low-power, full binary-coded decimal (BCD) clock/calendar plus 56 bytes of NV SRAM. Address and data are transferred serially through an I<sup>2</sup>C, bidirectional bus. The clock/calendar provides seconds, minutes, hours, day, date, month, and year information. The end of the month date is automatically adjusted for months with fewer than 31 days, including corrections for leap year. The clock operates in either the 24-hour or 12-hour format with AM/PM indicator. The DS1307 has a built-in power-sense circuit that detects power failures and automatically switches to the backup supply. Timekeeping operation continues while the part operates from the backup supply.

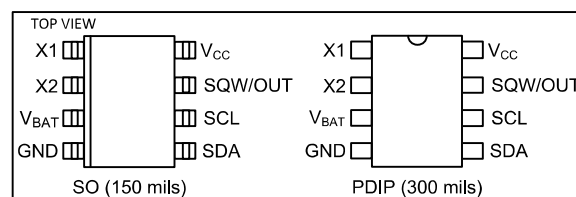
### TYPICAL OPERATING CIRCUIT



### BENEFITS AND FEATURES

- Completely Manages All Timekeeping Functions
  - Real-Time Clock Counts Seconds, Minutes, Hours, Date of the Month, Month, Day of the Week, and Year with Leap-Year Compensation Valid Up to 2100
  - 56-Byte, Battery-Backed, General-Purpose RAM with Unlimited Writes
  - Programmable Square-Wave Output Signal
- Simple Serial Port Interfaces to Most Microcontrollers
  - I<sup>2</sup>C Serial Interface
- Low Power Operation Extends Battery Backup Run Time
  - Consumes Less than 500nA in Battery-Backup Mode with Oscillator Running
  - Automatic Power-Fail Detect and Switch Circuitry
- 8-Pin DIP and 8-Pin SO Minimizes Required Space
- Optional Industrial Temperature Range: -40°C to +85°C Supports Operation in a Wide Range of Applications
- Underwriters Laboratories® (UL) Recognized

### PIN CONFIGURATIONS



### ORDERING INFORMATION

PART	TEMP RANGE	VOLTAGE (V)	PIN-PACKAGE	TOP MARK*
DS1307+	0°C to +70°C	5.0	8 PDIP (300 mils)	DS1307
DS1307N+	-40°C to +85°C	5.0	8 PDIP (300 mils)	DS1307N
DS1307Z+	0°C to +70°C	5.0	8 SO (150 mils)	DS1307
DS1307ZN+	-40°C to +85°C	5.0	8 SO (150 mils)	DS1307N
DS1307Z+T&R	0°C to +70°C	5.0	8 SO (150 mils) Tape and Reel	DS1307
DS1307ZN+T&R	-40°C to +85°C	5.0	8 SO (150 mils) Tape and Reel	DS1307N

+Denotes a lead-free/RoHS-compliant package.

\*A "+" anywhere on the top mark indicates a lead-free package. An "N" anywhere on the top mark indicates an industrial temperature range device. Underwriters Laboratories, Inc. is a registered certification mark of Underwriters Laboratories, Inc.

**ABSOLUTE MAXIMUM RATINGS**

Voltage Range on Any Pin Relative to Ground .....-0.5V to +7.0V  
 Operating Temperature Range (Noncondensing)  
     Commercial..... 0°C to +70°C  
     Industrial ..... -40°C to +85°C  
 Storage Temperature Range ..... -55°C to +125°C  
 Soldering Temperature (DIP, leads).....+260°C for 10 seconds  
 Soldering Temperature (surface mount).....Refer to the JPC/JEDEC J-STD-020 Specification.

*Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to the absolute maximum rating conditions for extended periods may affect device reliability.*

**RECOMMENDED DC OPERATING CONDITIONS**

(T<sub>A</sub> = 0°C to +70°C, T<sub>A</sub> = -40°C to +85°C.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage	V <sub>CC</sub>		4.5	5.0	5.5	V
Logic 1 Input	V <sub>IH</sub>		2.2		V <sub>CC</sub> + 0.3	V
Logic 0 Input	V <sub>IL</sub>		-0.3		+0.8	V
V <sub>BAT</sub> Battery Voltage	V <sub>BAT</sub>		2.0	3	3.5	V

**DC ELECTRICAL CHARACTERISTICS**

(V<sub>CC</sub> = 4.5V to 5.5V; T<sub>A</sub> = 0°C to +70°C, T<sub>A</sub> = -40°C to +85°C.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Leakage (SCL)	I <sub>LI</sub>		-1		1	μA
I/O Leakage (SDA, SQW/OUT)	I <sub>LO</sub>		-1		1	μA
Logic 0 Output (I <sub>OL</sub> = 5mA)	V <sub>OL</sub>				0.4	V
Active Supply Current (f <sub>SCL</sub> = 100kHz)	I <sub>CCA</sub>				1.5	mA
Standby Current	I <sub>CCS</sub>	(Note 3)			200	μA
V <sub>BAT</sub> Leakage Current	I <sub>BATLKG</sub>			5	50	nA
Power-Fail Voltage (V <sub>BAT</sub> = 3.0V)	V <sub>PF</sub>		1.216 x V <sub>BAT</sub>	1.25 x V <sub>BAT</sub>	1.284 x V <sub>BAT</sub>	V

**DC ELECTRICAL CHARACTERISTICS**

(V<sub>CC</sub> = 0V, V<sub>BAT</sub> = 3.0V; T<sub>A</sub> = 0°C to +70°C, T<sub>A</sub> = -40°C to +85°C.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
V <sub>BAT</sub> Current (OSC ON); SQW/OUT OFF	I <sub>BAT1</sub>			300	500	nA
V <sub>BAT</sub> Current (OSC ON); SQW/OUT ON (32kHz)	I <sub>BAT2</sub>			480	800	nA
V <sub>BAT</sub> Data-Retention Current (Oscillator Off)	I <sub>BATDR</sub>			10	100	nA

**WARNING:** Negative undershoots below -0.3V while the part is in battery-backed mode may cause loss of data.

**AC ELECTRICAL CHARACTERISTICS**(V<sub>CC</sub> = 4.5V to 5.5V; T<sub>A</sub> = 0°C to +70°C, T<sub>A</sub> = -40°C to +85°C.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
SCL Clock Frequency	f <sub>SCL</sub>		0		100	kHz
Bus Free Time Between a STOP and START Condition	t <sub>BUF</sub>		4.7			μs
Hold Time (Repeated) START Condition	t <sub>HD:STA</sub>	(Note 4)	4.0			μs
LOW Period of SCL Clock	t <sub>LOW</sub>		4.7			μs
HIGH Period of SCL Clock	t <sub>HIGH</sub>		4.0			μs
Setup Time for a Repeated START Condition	t <sub>SU:STA</sub>		4.7			μs
Data Hold Time	t <sub>HD:DAT</sub>		0			μs
Data Setup Time	t <sub>SU:DAT</sub>	(Notes 5, 6)	250			ns
Rise Time of Both SDA and SCL Signals	t <sub>R</sub>				1000	ns
Fall Time of Both SDA and SCL Signals	t <sub>F</sub>				300	ns
Setup Time for STOP Condition	t <sub>SU:STO</sub>		4.7			μs

**CAPACITANCE**(T<sub>A</sub> = +25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Pin Capacitance (SDA, SCL)	C <sub>I/O</sub>				10	pF
Capacitance Load for Each Bus Line	C <sub>B</sub>	(Note 7)			400	pF

**Note 1:** All voltages are referenced to ground.**Note 2:** Limits at -40°C are guaranteed by design and are not production tested.**Note 3:** I<sub>CCS</sub> specified with V<sub>CC</sub> = 5.0V and SDA, SCL = 5.0V.**Note 4:** After this period, the first clock pulse is generated.**Note 5:** A device must internally provide a hold time of at least 300ns for the SDA signal (referred to the V<sub>IH(MIN)</sub> of the SCL signal) to bridge the undefined region of the falling edge of SCL.**Note 6:** The maximum t<sub>HD:DAT</sub> only has to be met if the device does not stretch the LOW period (t<sub>LOW</sub>) of the SCL signal.**Note 7:** C<sub>B</sub>—total capacitance of one bus line in pF.

## TIMING DIAGRAM

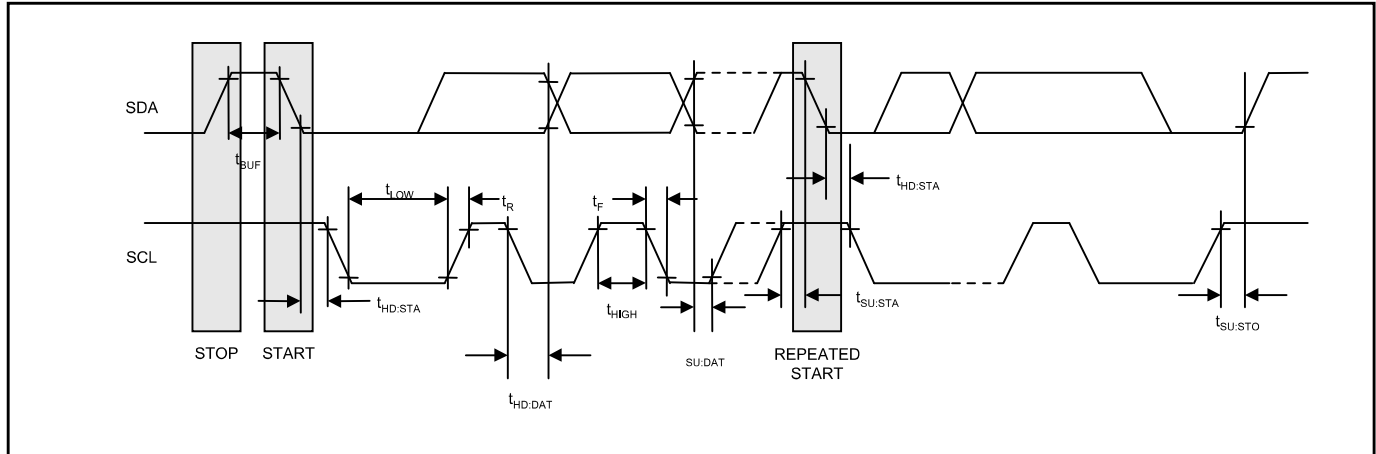
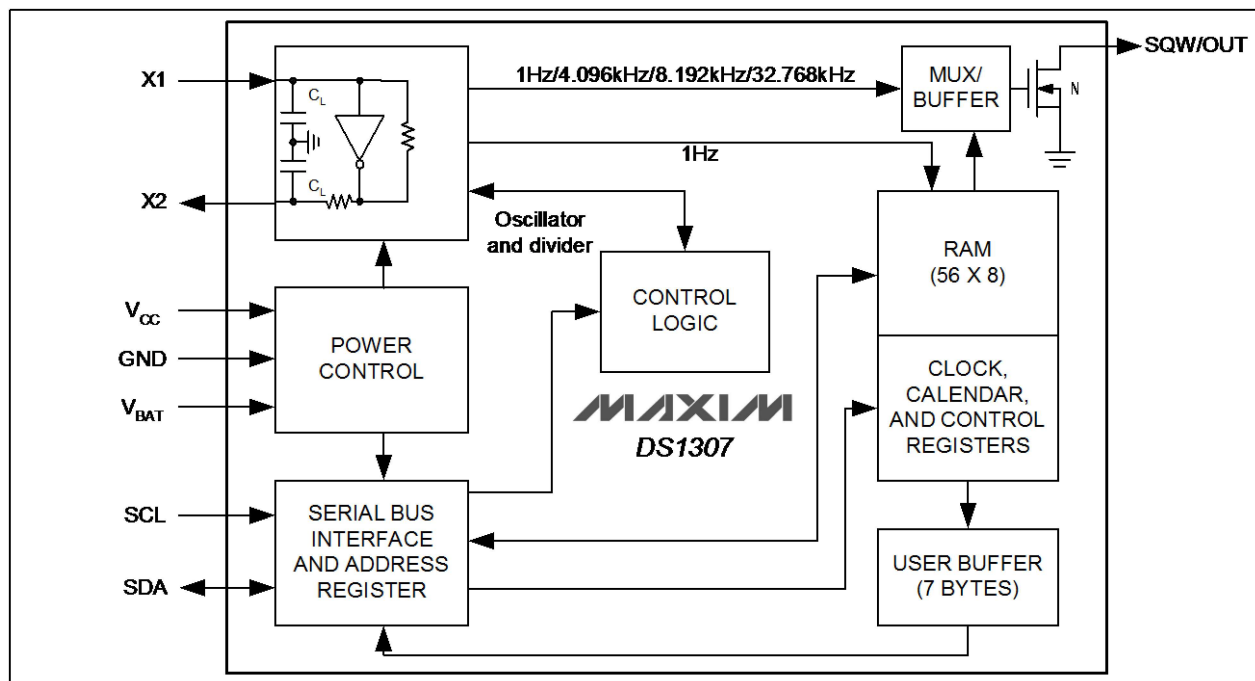
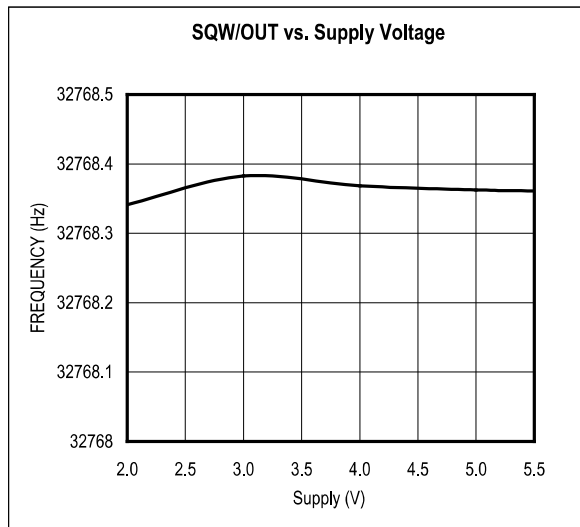
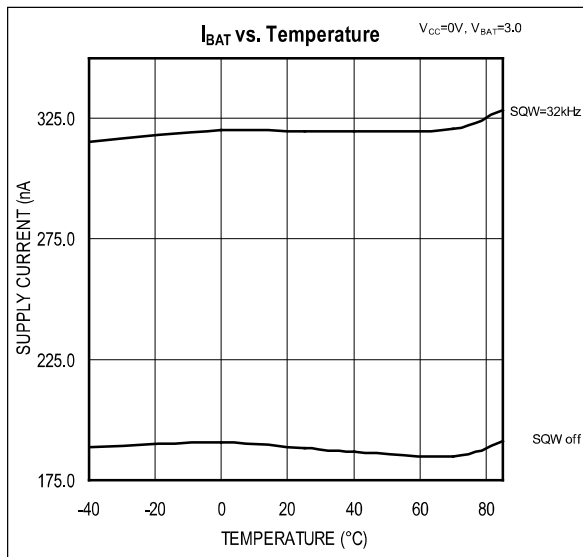
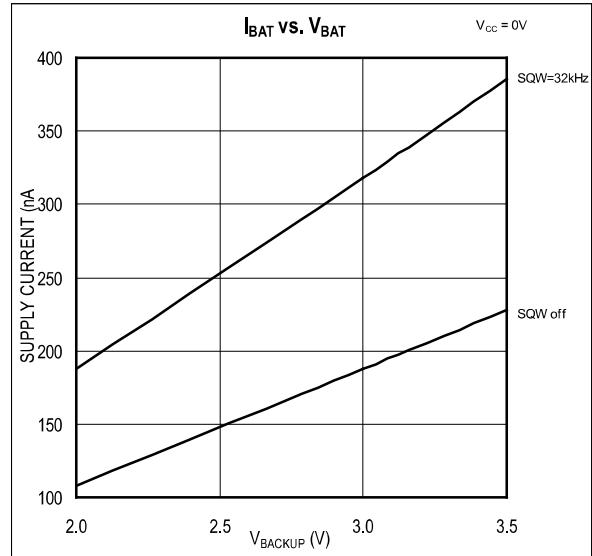
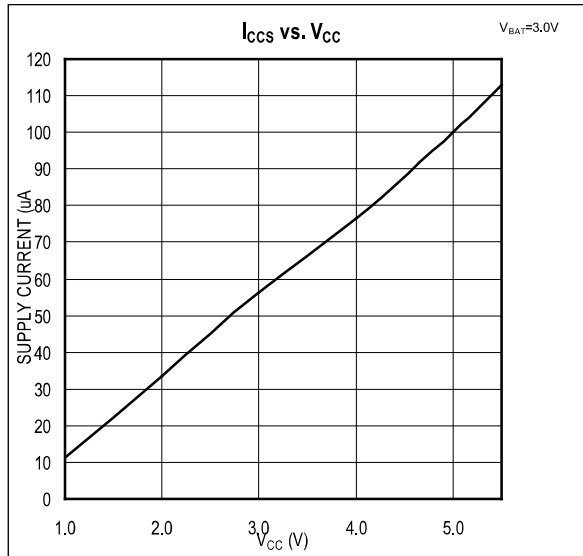


Figure 1. Block Diagram



**TYPICAL OPERATING CHARACTERISTICS**(V<sub>CC</sub> = 5.0V, T<sub>A</sub> = +25°C, unless otherwise noted.)

## PIN DESCRIPTION

PIN	NAME	FUNCTION
1	X1	Connections for Standard 32.768kHz Quartz Crystal. The internal oscillator circuitry is designed for operation with a crystal having a specified load capacitance ( $C_L$ ) of 12.5pF. X1 is the input to the oscillator and can optionally be connected to an external 32.768kHz oscillator. The output of the internal oscillator, X2, is floated if an external oscillator is connected to X1.
2	X2	
3	V <sub>BAT</sub>	Backup Supply Input for Any Standard 3V Lithium Cell or Other Energy Source. Battery voltage must be held between the minimum and maximum limits for proper operation. Diodes in series between the battery and the V <sub>BAT</sub> pin may prevent proper operation. If a backup supply is not required, V <sub>BAT</sub> must be grounded. The nominal power-fail trip point (V <sub>PF</sub> ) voltage at which access to the RTC and user RAM is denied is set by the internal circuitry as 1.25 x V <sub>BAT</sub> nominal. A lithium battery with 48mAh or greater will back up the DS1307 for more than 10 years in the absence of power at +25°C.  UL recognized to ensure against reverse charging current when used with a lithium battery. Go to: <a href="http://www.maxim-ic.com/ga/info/ul/">www.maxim-ic.com/ga/info/ul/</a> .
4	GND	Ground
5	SDA	Serial Data Input/Output. SDA is the data input/output for the I <sup>2</sup> C serial interface. The SDA pin is open drain and requires an external pullup resistor. The pullup voltage can be up to 5.5V regardless of the voltage on V <sub>CC</sub> .
6	SCL	Serial Clock Input. SCL is the clock input for the I <sup>2</sup> C interface and is used to synchronize data movement on the serial interface. The pullup voltage can be up to 5.5V regardless of the voltage on V <sub>CC</sub> .
7	SQW/OUT	Square Wave/Output Driver. When enabled, the SQWE bit set to 1, the SQW/OUT pin outputs one of four square-wave frequencies (1Hz, 4kHz, 8kHz, 32kHz). The SQW/OUT pin is open drain and requires an external pullup resistor. SQW/OUT operates with either V <sub>CC</sub> or V <sub>BAT</sub> applied. The pullup voltage can be up to 5.5V regardless of the voltage on V <sub>CC</sub> . If not used, this pin can be left floating.
8	V <sub>CC</sub>	Primary Power Supply. When voltage is applied within normal limits, the device is fully accessible and data can be written and read. When a backup supply is connected to the device and V <sub>CC</sub> is below V <sub>TP</sub> , read and writes are inhibited. However, the timekeeping function continues unaffected by the lower input voltage.

## DETAILED DESCRIPTION

The DS1307 is a low-power clock/calendar with 56 bytes of battery-backed SRAM. The clock/calendar provides seconds, minutes, hours, day, date, month, and year information. The date at the end of the month is automatically adjusted for months with fewer than 31 days, including corrections for leap year. The DS1307 operates as a slave device on the I<sup>2</sup>C bus. Access is obtained by implementing a START condition and providing a device identification code followed by a register address. Subsequent registers can be accessed sequentially until a STOP condition is executed. When V<sub>CC</sub> falls below 1.25 x V<sub>BAT</sub>, the device terminates an access in progress and resets the device address counter. Inputs to the device will not be recognized at this time to prevent erroneous data from being written to the device from an out-of-tolerance system. When V<sub>CC</sub> falls below V<sub>BAT</sub>, the device switches into a low-current battery-backup mode. Upon power-up, the device switches from battery to V<sub>CC</sub> when V<sub>CC</sub> is greater than V<sub>BAT</sub> +0.2V and recognizes inputs when V<sub>CC</sub> is greater than 1.25 x V<sub>BAT</sub>. The block diagram in Figure 1 shows the main elements of the serial RTC.