1. Description

1.1. Project

Project Name	example2
Board Name	custom
Generated with:	STM32CubeMX 5.4.0
Date	01/11/2020

1.2. MCU

MCU Series	STM32G0
MCU Line	STM32G0x1
MCU name	STM32G031J6Mx
MCU Package	SO8N
MCU Pin number	20

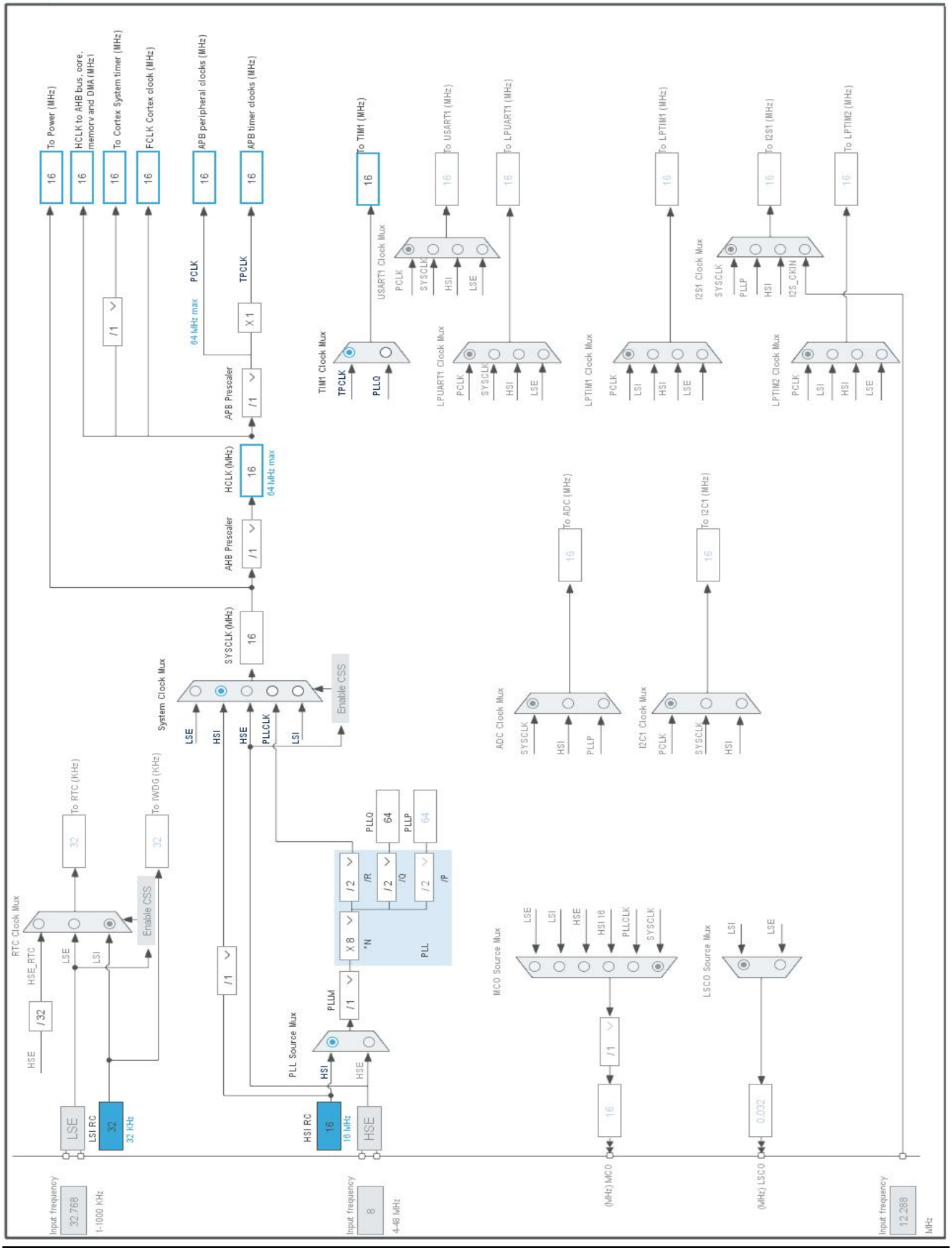
2. Pinout Configuration

3. Pins Configuration

Pin Number SO8N	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	PC14-OSC32_IN (PC14) *	I/O	GPIO_Output	
2	VDD/VDDA	MonoIO		
3	VSS/VSSA	MonoIO		
4	PF2 - NRST *	I/O	GPIO_Output	
5	PB0 *	I/O	GPIO_Output	
6	PA12 [PA10] *	I/O	GPIO_Output	
7	PA13	I/O	SYS_SWDIO	
8	PA14-BOOT0	I/O	SYS_SWCLK	

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value
Project Name	example2
Project Folder	E:\MyStuff\dev\hardware\Breakouts\STM32G031J6M6\firmware\example2
Toolchain / IDE	SW4STM32
Firmware Package Name and Version	STM32Cube FW_G0 V1.3.0

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32G0
Line	STM32G0x1
MCU	STM32G031J6Mx
Datasheet	DS12992_Rev0

6.2. Parameter Selection

Temperature	25
Vdd	3.0

7. IPs and Middleware Configuration

7.1. GPIO

7.2. SYS

mode: Debug

Timebase Source: SysTick

7.3. TIM1

Channel5: Output Compare No Output

7.3.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	0
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	0
Internal Clock Division (CKD)	**Division by 4 ***
Repetition Counter (RCR - 16 bits value)	**61 ***
auto-reload preload	Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection TRGO	Reset (UG bit from TIMx_EGR)
Trigger Event Selection TRGO2	Reset (UG bit from TIMx_EGR)

Break And Dead Time management - BRK Configuration:

BRK State	Disable
BRK Polarity	High
BRK Filter (4 bits value)	0
BRK Sources Configuration	
- Digital Input	Disable

Break And Dead Time management - BRK2 Configuration:

BRK2 State	Disable
BRK2 Polarity	High
BRK2 Filter (4 bits value)	0
BRK2 Sources Configuration	
- Digital Input	Disable

Break And Dead Time management - Output Configuration:

Automatic Output State	Disable
Off State Selection for Idle Mode (OSSI)	Disable
Lock Configuration	Off

Clear Input:

Clear Input Source	Disable

Output Compare No Output Channel 5:

Mode	Frozen (used for Timing base)
Pulse (16 bits value)	**60000 ***
Output compare preload	Disable
CH Polarity	High
CH Idle State	Reset

*** User modified value**

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	
	PA14-BOOT0	SYS_SWCLK	n/a	n/a	n/a	
GPIO	PC14-OSC32_IN (PC14)	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PF2 - NRST	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA12 [PA10]	GPIO_Output	Output Push Pull	No pull-up and no pull-down	**Medium ***	

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
TIM1 break, update, trigger and commutation interrupts	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
TIM1 capture compare interrupt	unused		

*** User modified value**

9. Software Pack Report