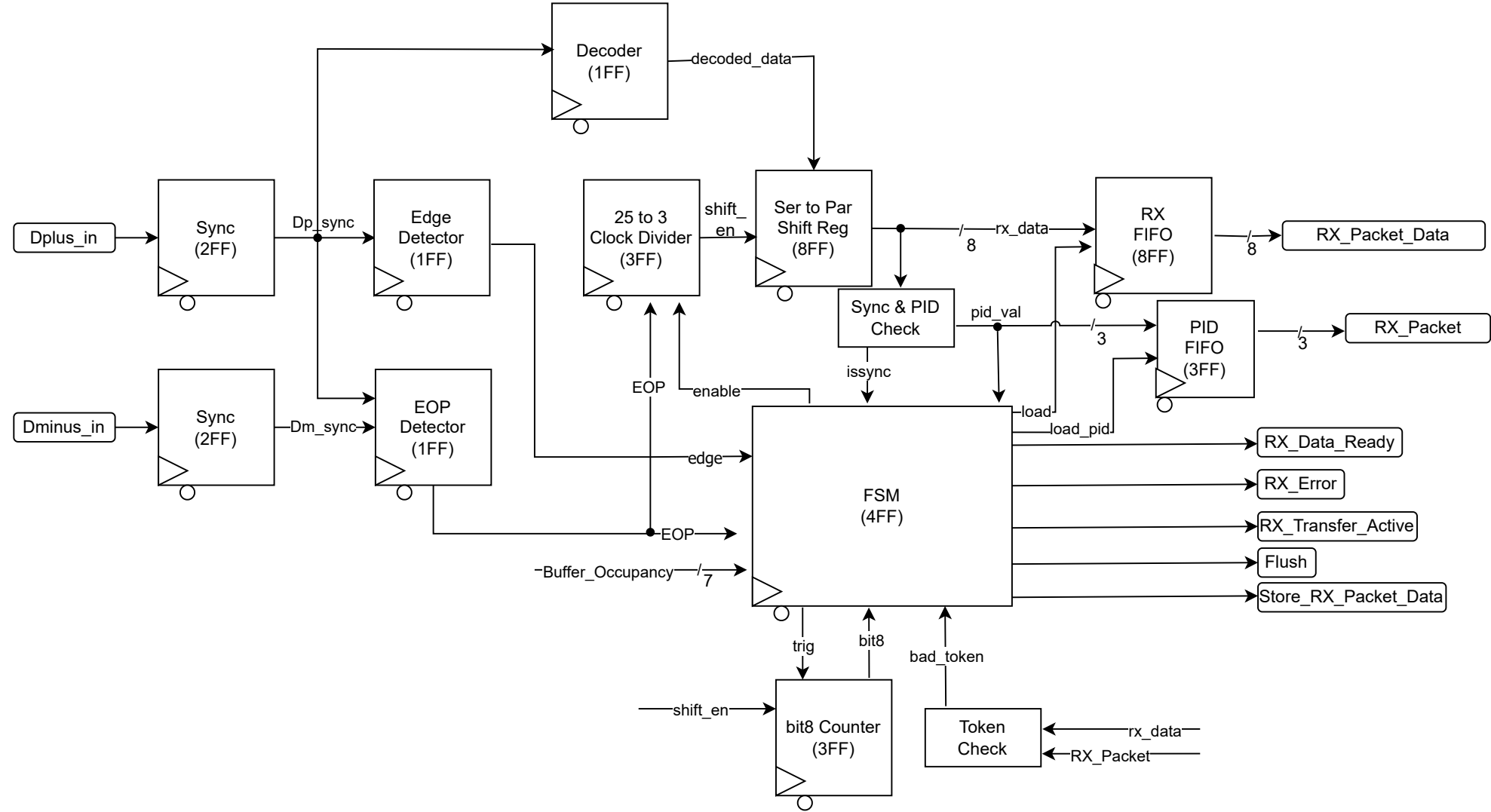


Top level USB RX

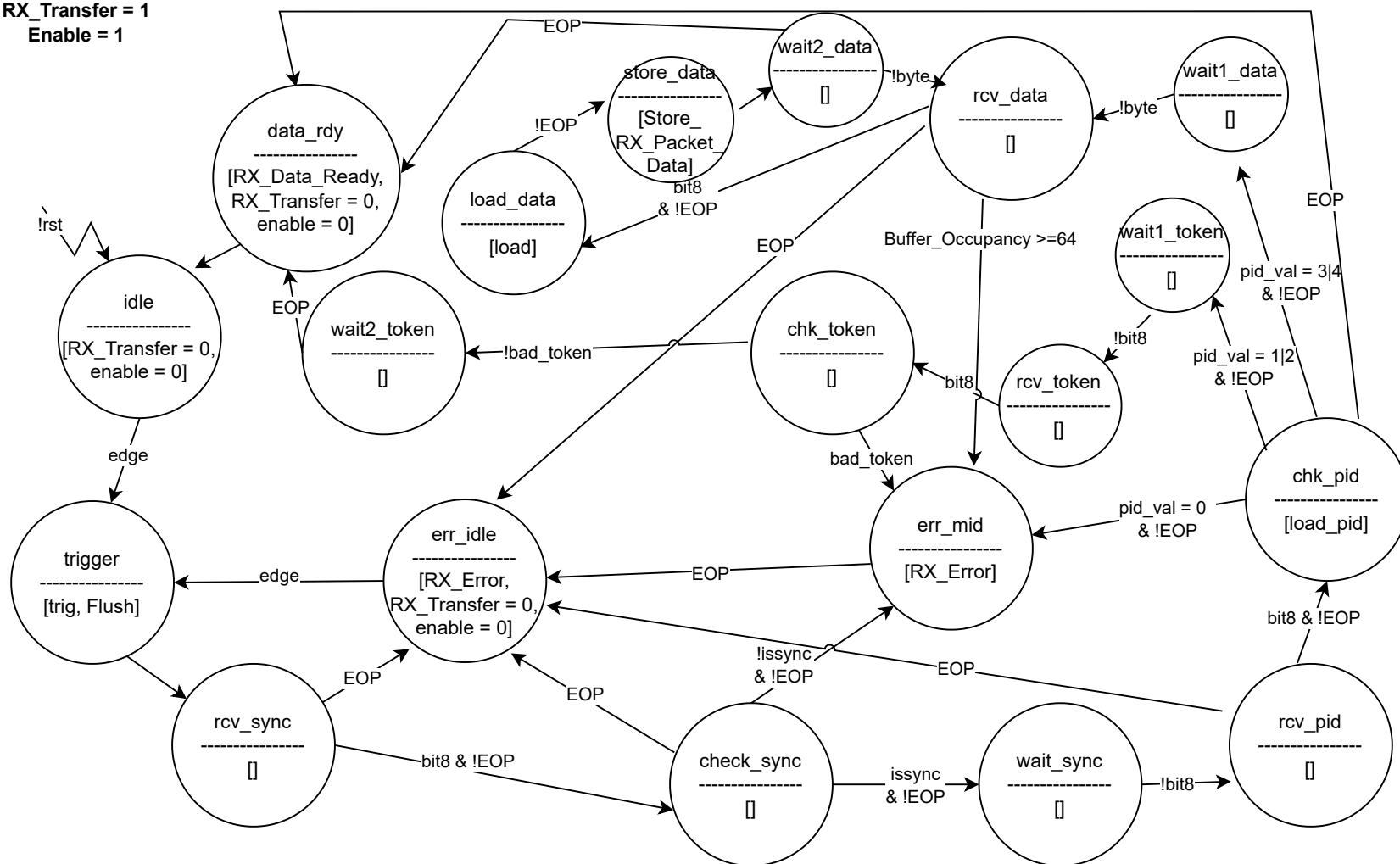
Device Address = 7'b0000001

36 Flip-Flops Total

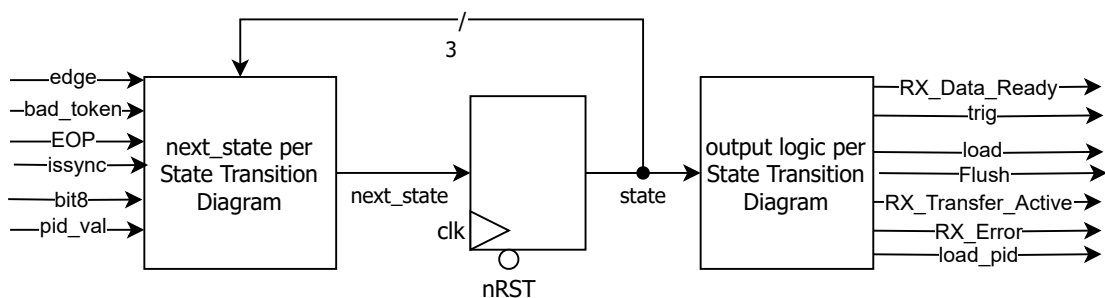


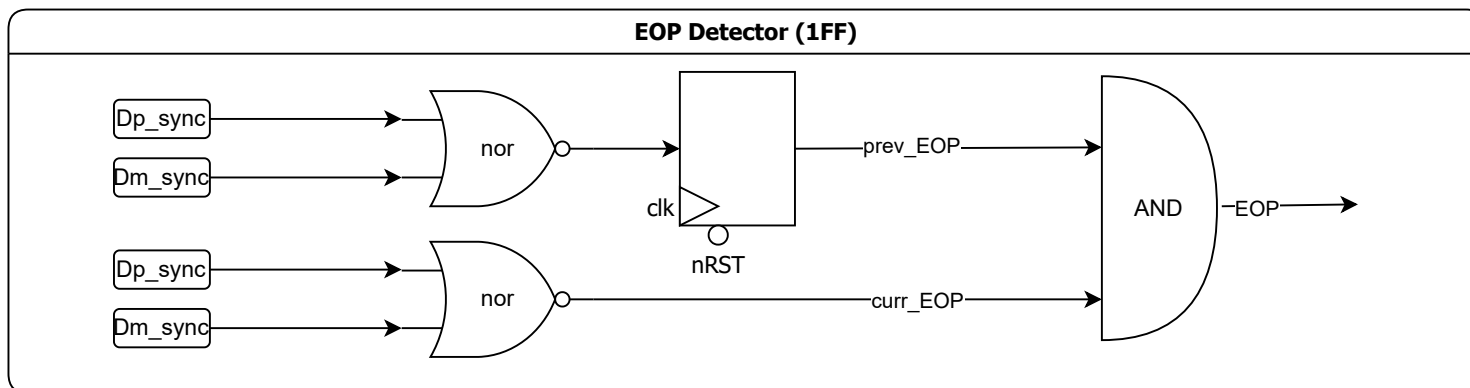
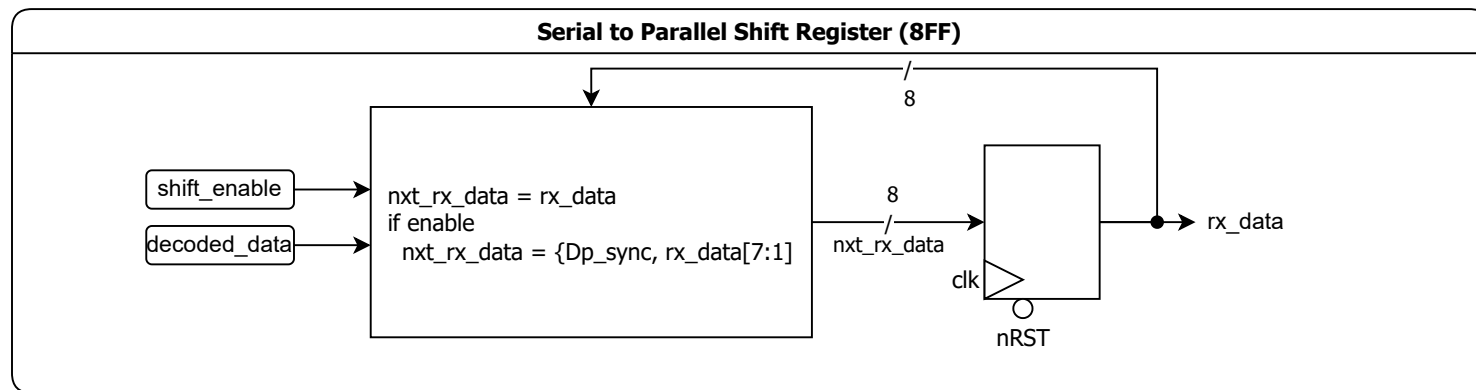
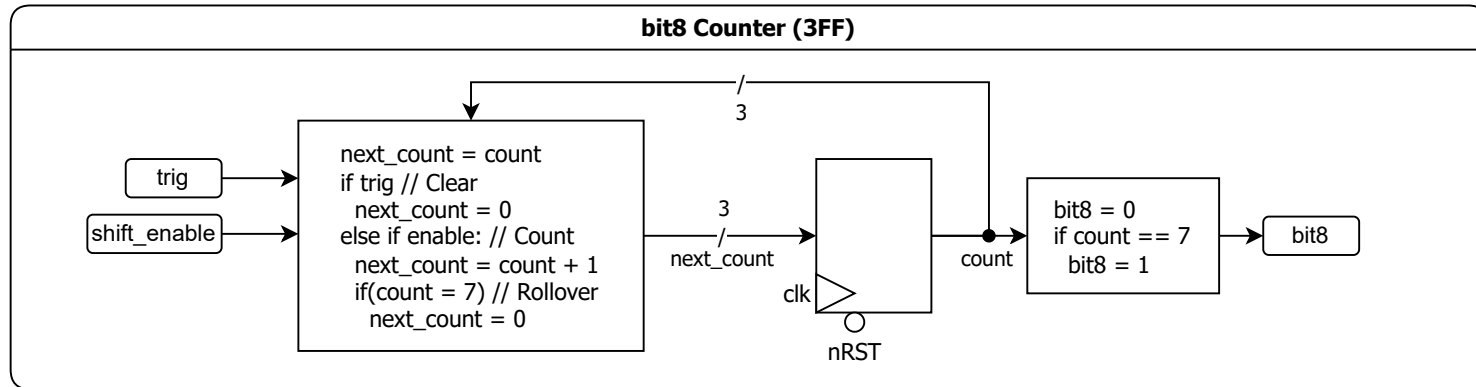
FSM State Transition Diagram

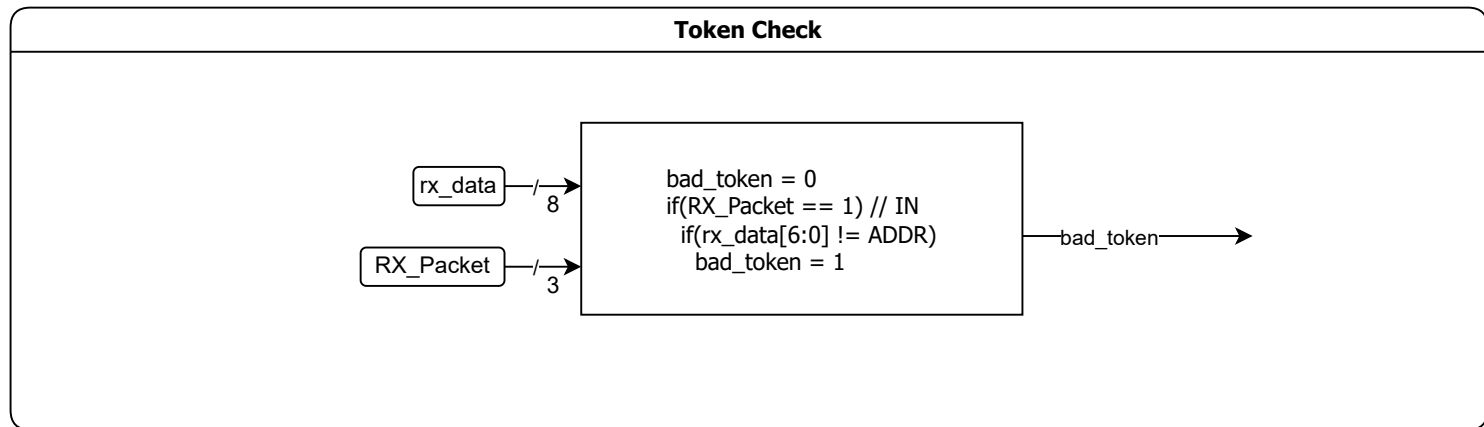
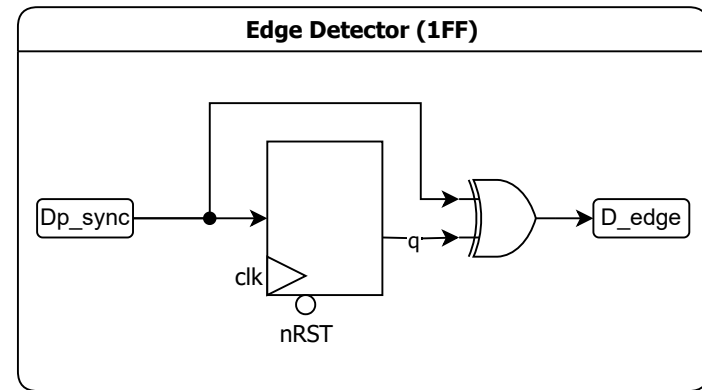
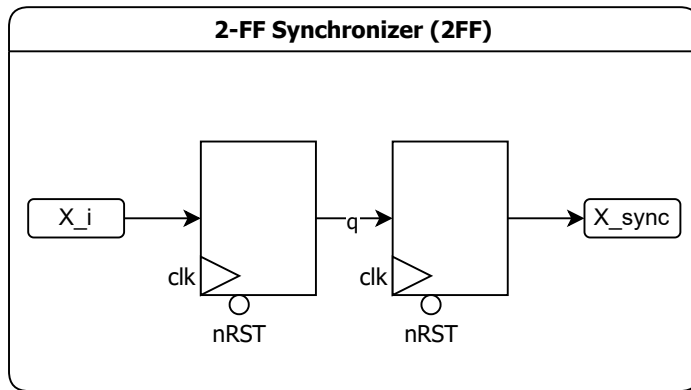
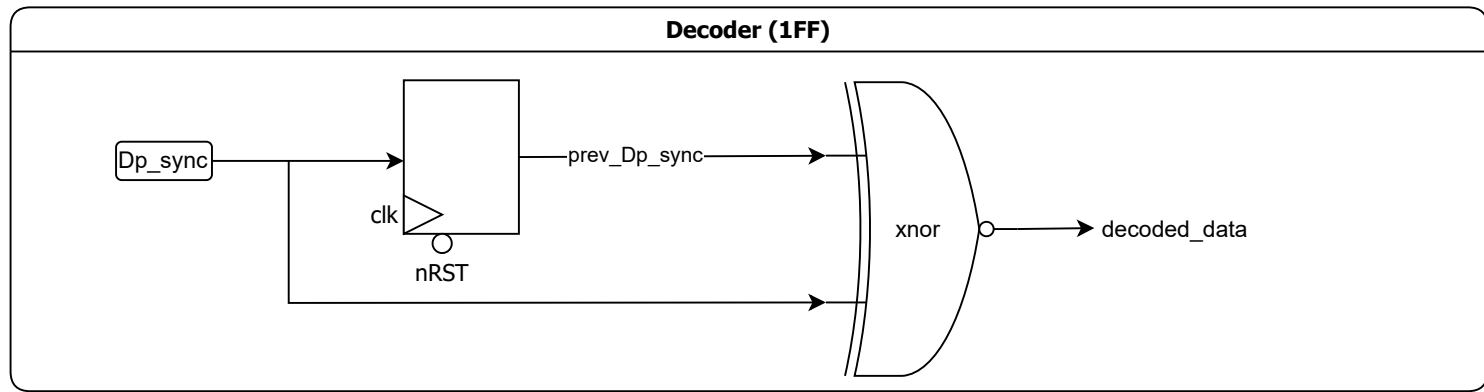
Unless Specified
RX_Transfer = 1
Enable = 1

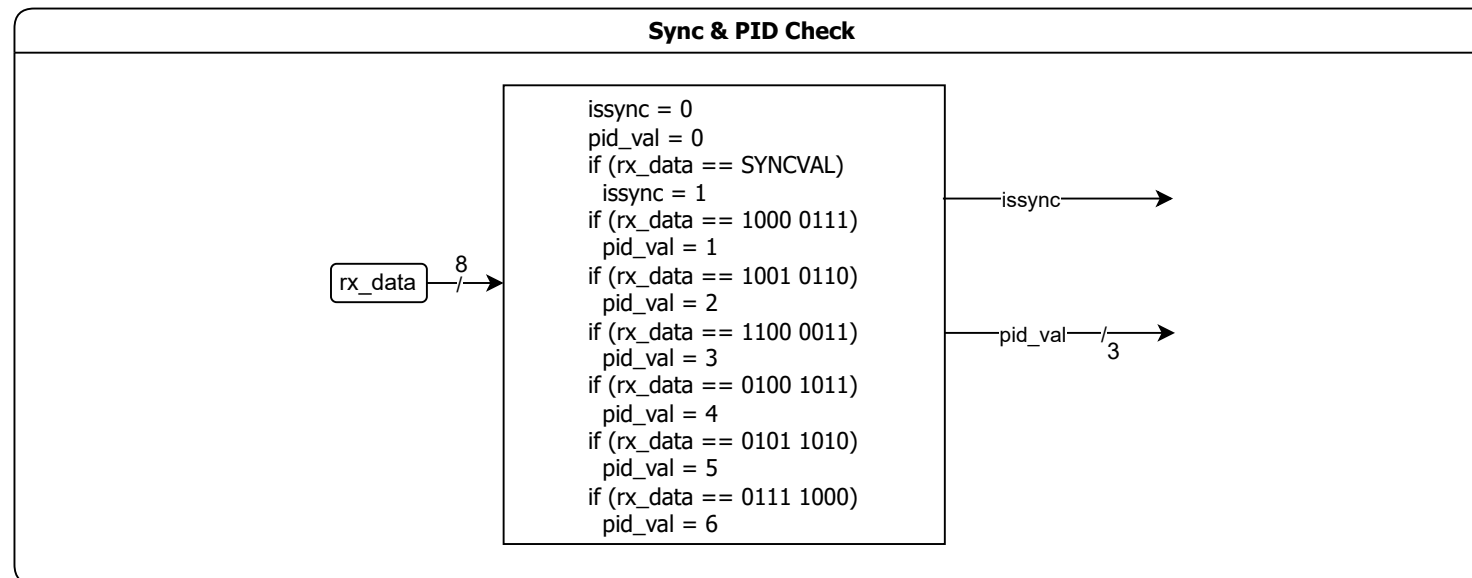
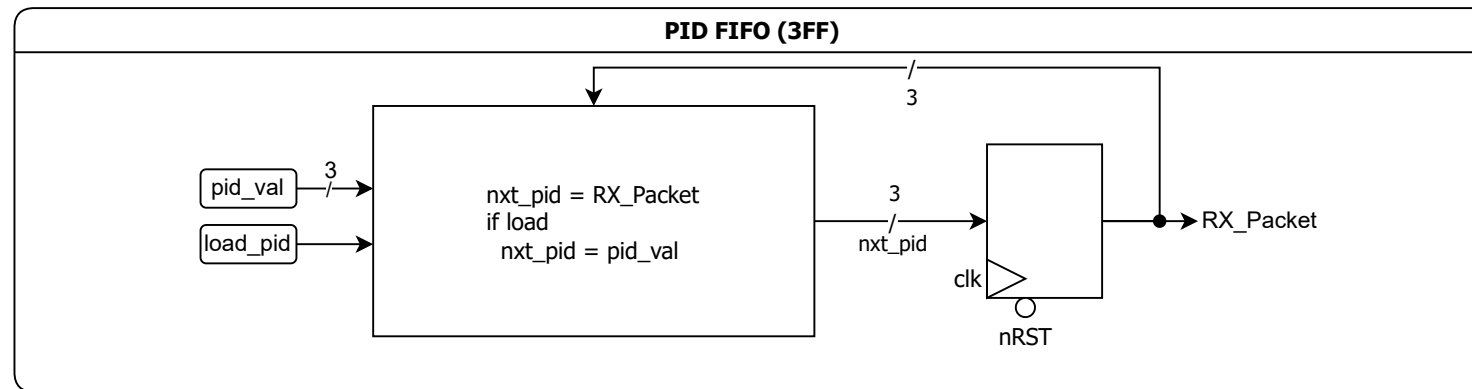
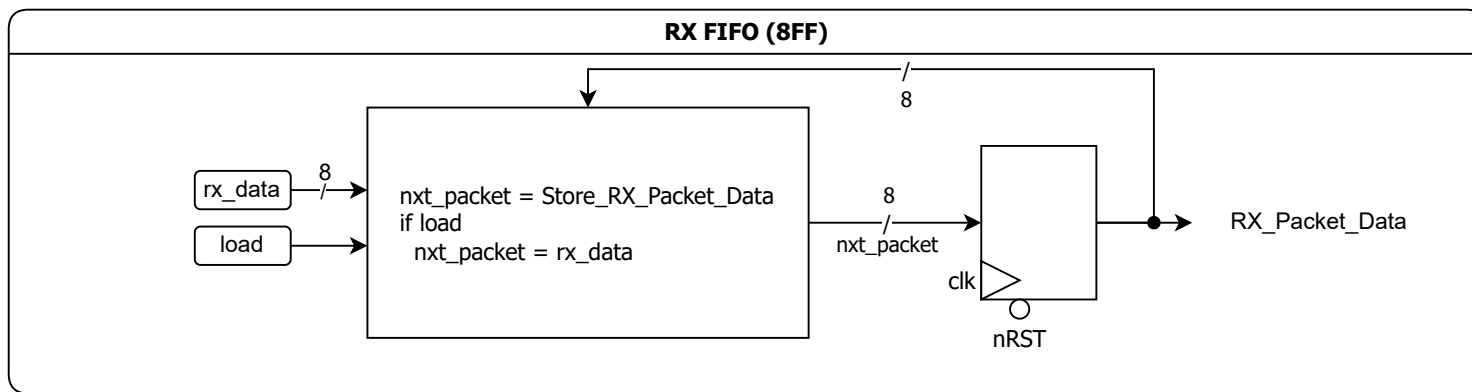


FSM (4FF)

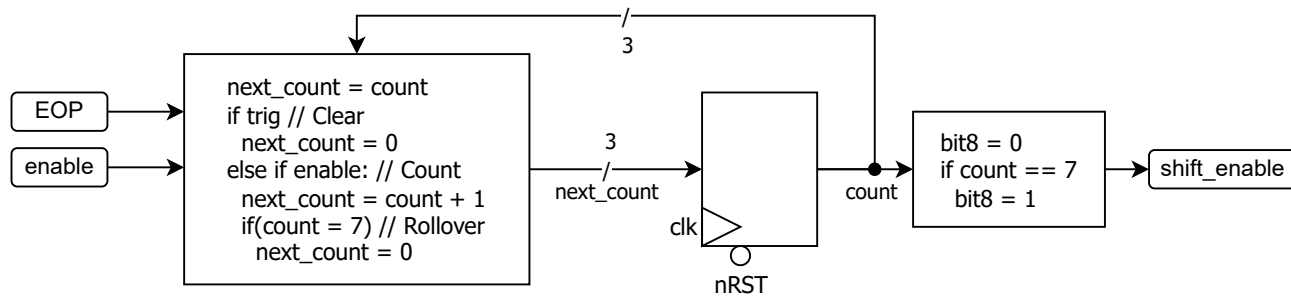








8-to-1 Clock divider (3FF)



25-to-3 Clkdiv (5FF)

