I2c

If you have (common) devices with 7 bit addresses up to 112 Devices (128 addresses - reserved addresses (0x00-0x07 and 0x78-0x7F are reserved))(certain limitations apply) If you have (less common) devices that support 10 bit addressing up to 1024 devices (you can mix 7bit and 10bit devices and reach up to 1136 devices that way)

Interrupts
https://0xax.gitbooks.io/linux-insides/content/interrupts/interrupts-1.html

GIT-imp

https://services.github.com/on-demand/downloads/github-git-cheat-sheet.pdf

https://git-scm.com/book/en/v2

GDB
http://users.ece.utexas.edu/~adnan/gdb-refcard.pdf

Spin lock

https://stackoverflow.com/questions/29274660/spin-lock-and-mutex-lock-order

Early Suspend and Late resume – spinlock, mutex
https://community.nxp.com/thread/261901

volatile
https://barrgroup.com/Embedded-Systems/How-To/C-Volatile-Keyword

volatile IMP
https://www.quora.com/What-is-volatile-Memory-in-C-and-how-is-it-used

http://www.mytechinterviews.com/singly-linked-list-delete-node

https://www.sitesbay.com/data-structure/c-queue

http://www.vmars.tuwien.ac.at/documents/extern/2360/Documenting_first_steps.pdf

https://www.nxp.com/docs/en/user-guide/LAUTERBACHTRACE32UG.pdf

strings

http://www.programmingsimplified.com/c-program-concatenate-strings

stack and Queue

http://btechsmartclass.com/DS/U2_T9.html

recursive linked list
https://gist.github.com/vo/2758919

work queue

https://0xax.gitbooks.io/linux-insides/content/interrupts/interrupts-9.html

https://kerneltweaks.wordpress.com/tag/workqueue/