Intel interview Questions :

C Questions :

1. preprocessor directives .

2. why we using #define to define a constant . rather than by using keyword..i.e like int a =10;

3. To toggle a particular bit and set a bit and clear bit.

4. to check number is multiplies of 4 or not.without using % operator.

5. To check meachine is little endian or big endian..?

6. reverse string without any arthimatic operation and tmp variable , loops also...?

7. To insert element in particular position in array.

8. diference b/w structure and union...and real time appilications..with respect to memory.

9. malloc operations(failed conditions).

10. bitfilelds

11.selfreferential structure...what is use of selfreferential structure...?

12. static functions and static variables...inclideing characterstics...?

13. In case of volatile, how the compiler takes the data every time from 14.memory, not from the cache

D.S :

1. insert a node at partcular position in single linked list...

Linux :

1.difference b/w fork and vfork.

2.copy on write(COW).

3.interrupts handling in o.s...how interupt handled by os..?

4.dif b/w semaphore and mutex...real time applications...?

5.spin locks ..brifely

6.memory barries.

7.mmap system call working.

8. process states.

Device Drivers :

1. any driver in linux

2. how driver interact with H/w....explaination

3.driver architecture..?

Digital Electronics:

1.what is active low signal.

2.discussion on logic gates(AND ,OR ,NOT,XOR,XNOR);

3.discussion on MUX.

4.power gateing and Edge gating...?

5.what is PLL and PLC...?

6.what is pipeling

7.tell me any processor ..?

8. memory hirarcy

9.what is cache memory..why using cache memory

10.cache ratio

11. how cache memory interact with processor..?

12.ARM ARCHITECTURE...?

Note: please make sure concentrate on ARM architectre.

1. process states.
2. explain mmap.
3.Interrupts types.
4.bit clear questions .
5. insert function in linked list.

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Below are the questions INTEL.

Telephonic interview:

- What are the components in the Linux power management?
- What is early suspend and late resume?
- CPU governors
- cpu frequency ranges
- Linux power management vs Android power maangement
- Additional components in android respect to linux kernel
- How the device and driver recognize in the device tree
- USB gadget drivers
- How many i2c devices connect
- I2C organization in linux
- Mutex and semaphores
- Types of spin locks
- Concurrent work queues
- Memory barriers
- which spin lock is uesd between process and bottom half and hard and soft interruuts
- How to debug the kernel crash if the PC is got corrupted
- How to debug the kernel crash if the PC only available and no call stack available
- print HELLO WORLD 5 times without using the loop and goto
- Check whether the number is multiply by 4 or not without using any *,/,% operators
- write a c program to find the Loop in the linked list, what if the loop is at the starting node
- Write a logic for reverse of single linked list

F2F discussion:

Most questions asked in my projects with detailed explanation. (Less questions are generic), Rest of the questions are as follows:

- Linux power management

- DVFS, how the scaling is happening here and what bases
- operating points
- CPU idle
- C states
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- Volatile
- In case of volatile, how the compiler takes the data every time from memory, not from the cache
- C Memory layout
- Static variables and static functions
- In which segment does static int a[10]={0}; will store
- Inserting the node in the single linked list
- Insert the nodes in the ascending order
- Return value of malloc success and fail cases

- ARM architecture

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