



(<http://forum.arduino.cc/index.php?action=profile;u=159718>)



Newbie

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4095.94

4095.94

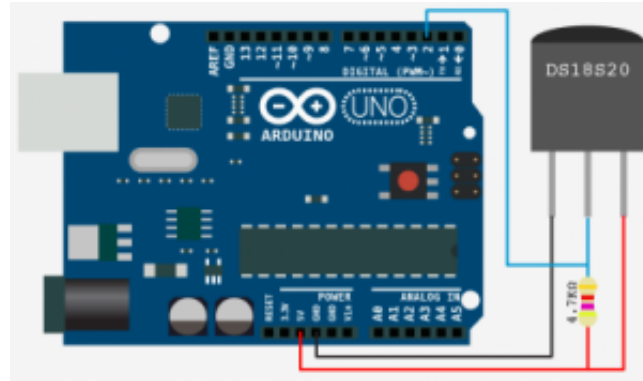
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4095.94

If I hold the temp sensor with my finger the numbers don't change a bit, which they obviously should.

I have it wired like the following on my Due but with the data line going to Due line A0 to the middle of the DS18B20 and a 4.7k resistor going from the middle pin to the 5v line.

Sorry bud, I really want this library to work because it's one of the only hold backs keeping me from using the Due instead of the Uno/Leonardo which I have verified good working code on.



**alvesjc**  
(<http://forum.arduino.cc/index.php?action=profile;u=18472>)



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Jr. Member

Posts: 84

(<http://www.aquaedsource.com>)

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Arduino rocks

Aquaedsource

(<http://www.aquaedsource.com>)

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**Re: OneWire in Due** (<http://forum.arduino.cc/index.php?topic=141030.msg1062434#msg1062434>)

Jan 08, 2013, 12:17 pm (<http://forum.arduino.cc/index.php?topic=141030.msg1062434#msg1062434>)

#2

Hi Kcore.

Thank you for testing.

It's working already for me and one colleague, what IDE are you using? We are bought using testing IDE compiled from git.

One thing important I forgot to mention, and is related to IDE version. This lib is set to use ODSR register, meaning this, that you MUST have OWER register set to 1.

This has been fixed in the IDE currently in git, can you compile it and repeat the test please?

There is also other way, in onewire.h, you have the following code for SAM3x.

Code: [Select]

```
#elif defined(__SAM3X8E__)
#define PIN_TO_BASEREG(pin)             (&(digitalPinToPort(pin)->PIO_PER)) //to get Base
#define PIN_TO_BITMASK(pin)             (digitalPinToBitMask(pin))
#define IO_REG_TYPE uint32_t
#define IO_REG_ASM
#define DIRECT_READ(base, mask)         (((*(base+15)) & (mask)) ? 1 : 0)      /*(Pio Offset: 0x0014) Input Disable Register */
#define DIRECT_MODE_INPUT(base, mask)   ((* (base+5)) = (mask))
/*(Pio Offset: 0x0014) Output Disable Register */
```

Replace it with the following lines:

Code: [Select]

```
#define DIRECT_WRITE_LOW(base, mask)    ((* (base+14)) &= ~(mask))             /*(Pio Offset: 0x0014) Output Disable Register */
#define DIRECT_WRITE_HIGH(base, mask)   ((* (base+14)) |= (mask))              /*(Pio Offset: 0x0014) Output Disable Register */
//define DIRECT_WRITE_LOW(base, mask)   ((* (base+13)) = (mask))
/*(Pio Offset: 0x0034) Clear Output Data Register */
//define DIRECT_WRITE_HIGH(base, mask)   ((* (base+12)) = (mask))
/*(Pio Offset: 0x0030) Set Output Data Register */
```

with: