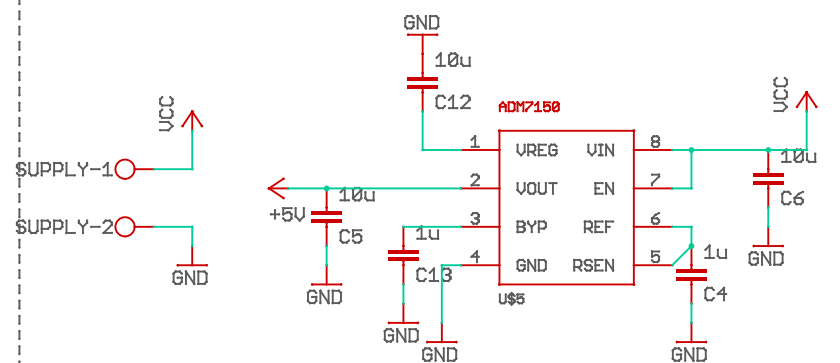
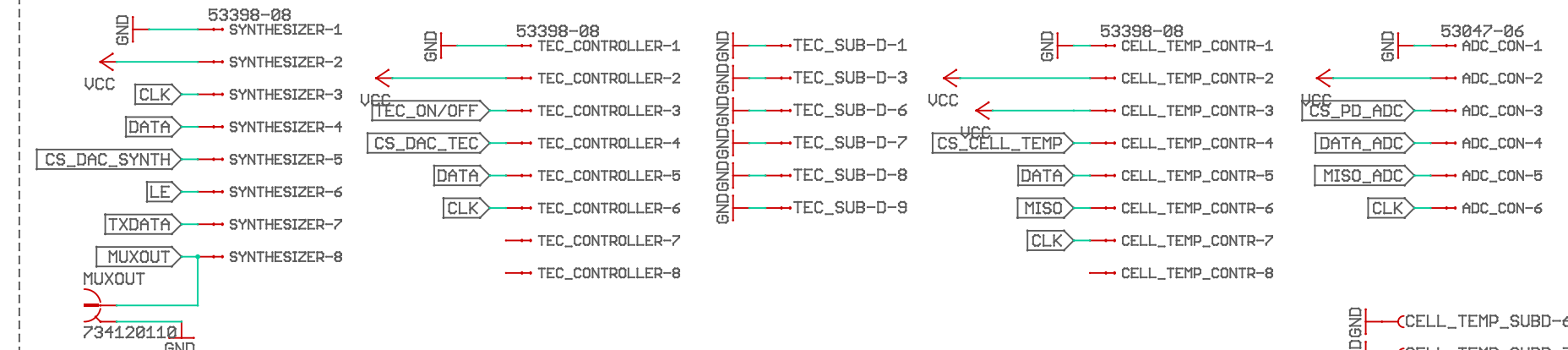


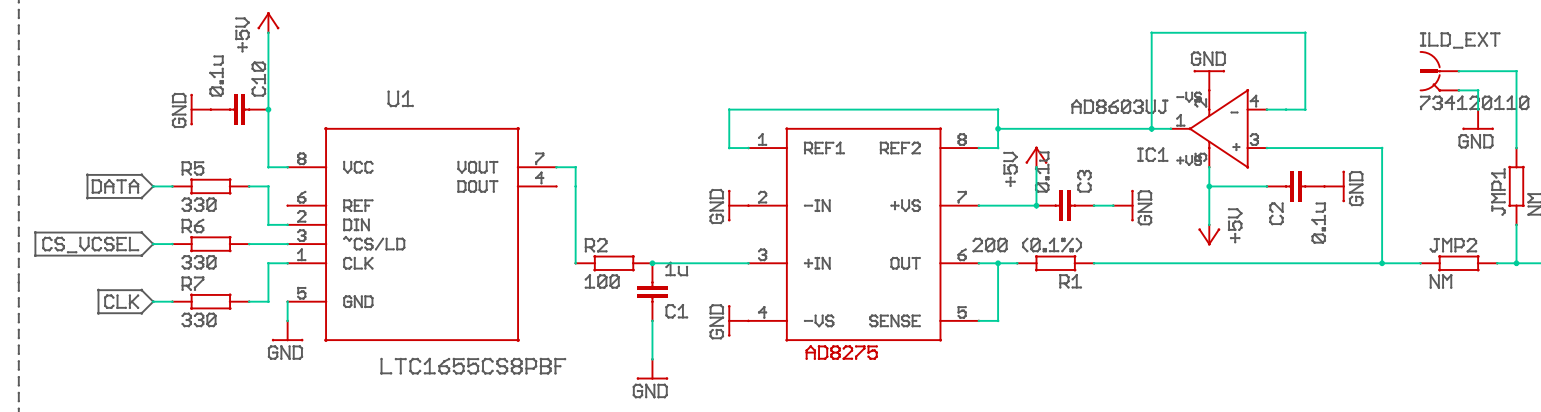
Voltage regulation



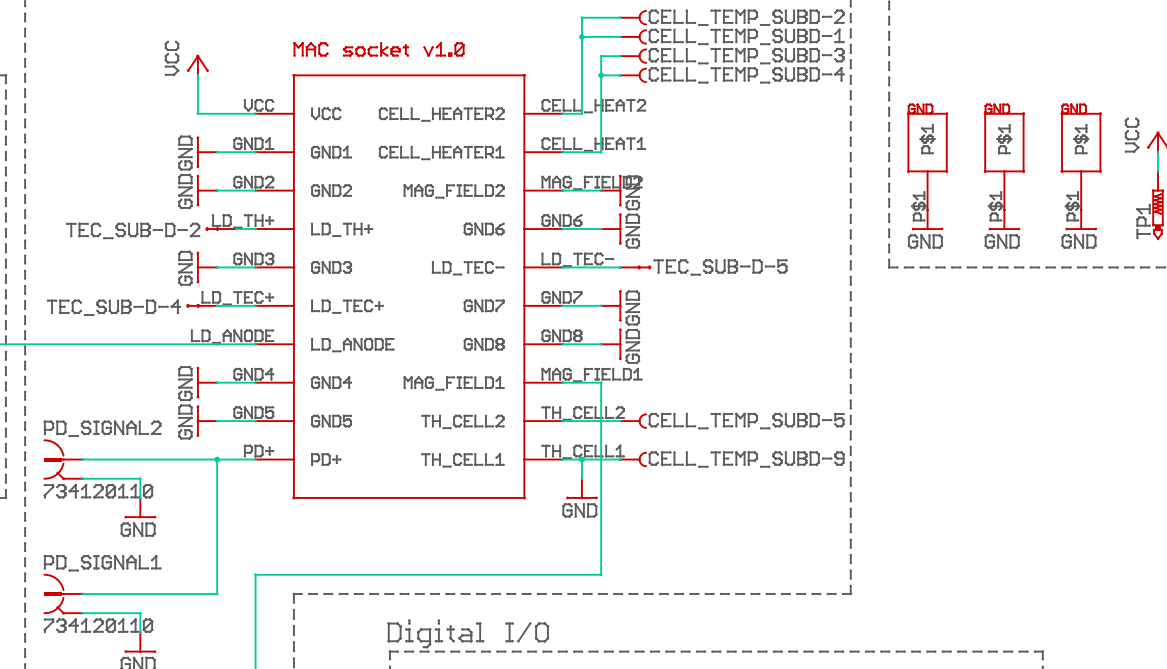
Subassembly I/O



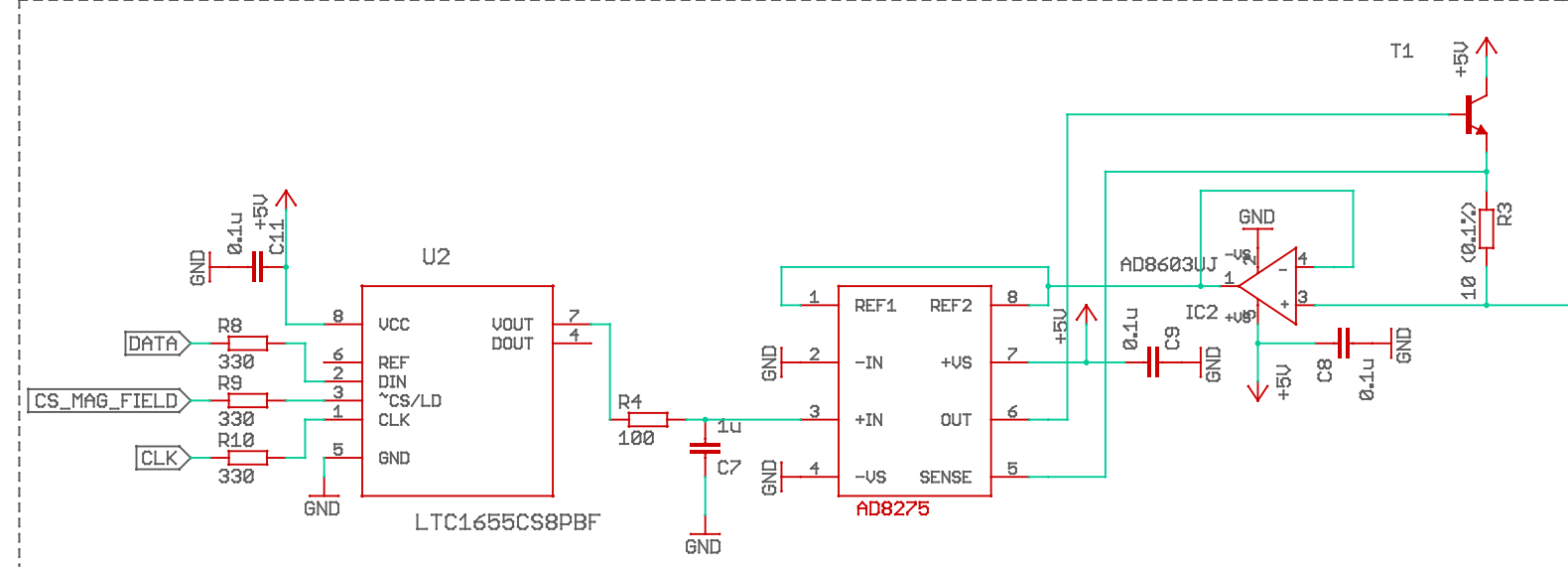
UCSEL Current Source



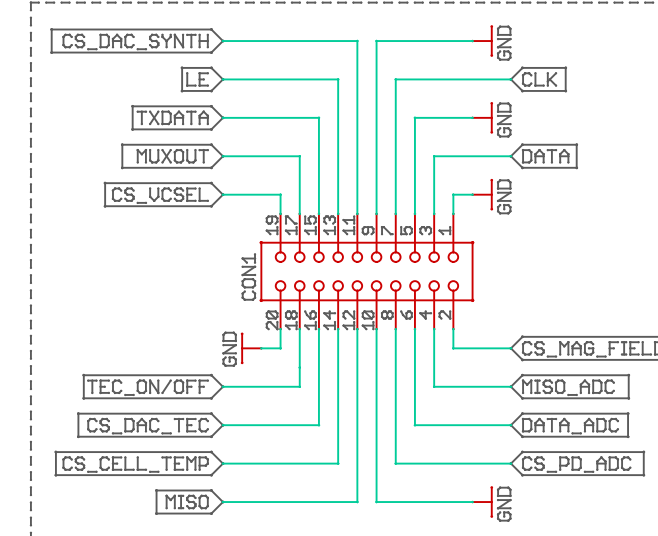
MAC Socket



Magnetic Field Current Source



Digital I/O



J.Rutkowski

Atomic clock test board v1.1

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