

Definition uGT readout record

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June 29, 2023

Contents

1	Def	finition uGT readout record														
	1.1	Overview of structure	2													
	1.2	Readout record dump	4													
	1.3	Data block	6													

1 Definition uGT readout record

This document is a description of the structure of a uGT readout record.

In chapter "Event Builder Overview" of document [1] one can find a description about building a uGT readout record on AMC13.

The following description and explanations are based on documents [1] and [2].

1.1 Overview of structure

In the following table the structure of the uGT readout record is shown:

- the first 8 lines contain AMC13 header information,
- followed by 3 AMC#1 header lines.
- Then the first block of input data (of AMC#1), containing a block header and 30 data words (32 bits), is shown.
- A certain number of input data blocks and output data blocks (each with the same block size) and a AMC#1 trailer line finish AMC#1 readout record.
- Data of AMC#2 to AMC#6 with output data blocks only
- and 2 AMC13 trailer lines fill up the readout record.

63 60)	56	52 4	18	44	40	36	32	2	28	24	20	16	12	2 8	3 4		0	
0x5 Ev_t		t	LV1_id					BXId					ource_i	.d	FOV	нх	\$ \$		
uFOV	FOV Res nAMC Reserved								OrN 0x0									0	
0 L M S	E P V	С	AMC1_size						0 0	0 0 0 0 Blk_No AmcNo					BoardID				
0 L M S	L M S E P V C AMC6_size									0 0 0 0 Blk_No AmcNo					BoardID				
0x0	0x0 AmcNo AMC1 LV1_id									BXId					Data_lgth				
User											01	εN	BoardID						
uGT build											MP7 FW version								
input data											BlockID=0x00 Block size [0				reserved [1]				
input data											input data								
								•											
BlockID=0x02 Block size reserved										input data									
						additio	nal 1	1 inpu	it da	ta b	locks A	AMC1							
output data											BlockID=0x21 Block size				reserved				
output data											output data								
								•											
BlockID=0x23 Block size reserved										output data									
additional 8 output data blocks AMC1																			
0x0	AmcN AMC		LV1_id							BXId				Data_lgth					
User									OrN					BoardID					
uGT build											MP7 FW version								
						9	outp	ut dat	a blo	ocks	AMC2								
						bl	Locks	for A	мс3		AMC6								
CRC 32 (AMCs)											LV1_id 0 0 0 0 Data_lgth					:h			
CRC 32										0 0 0 0 Blk_No LV1_id BX					BXId				
0xA	Evt_lgth							CRC CFXXEV Stat TTS							TR	\$ \$			

- [0]: "Event type" is set in configuration key: "ugt infra mp7 base" with <param cmd="roLoadMenu" id="mode1:eventType" type="uint">0xc0</param>.
- [1]: Contains probably the "bank id" which is set in configuration key: "ugt infra mp7 base" with

<param cmd="roLoadMenu" id="mode1:capture0:bankId" type="uint">1</param>
for output data.

1.2 Readout record dump

Example of a readout record dump (with description):

```
5186f41767557c08 - AMC13 header: Event type, LV1 id, BX id, FED#
10604240512ff300 - AMC13 header: AMCs, Orbit nr (32 bits)
Of00014a00010000 - AMC13 header: Data length, AMC#1
0f00009000020000 - AMC13 header: Data length, AMC#2
0f00009000030000
0f00009000040000
0f00009000050000
0f00009000060000
0186f4176750014a - AMC#1 header: AMC#1, LV1 id, BX id, Data length
000000c0ff300000 - AMC#1 header: Ev. type [0], Orbit nr (16 bits)
0000115200030202 - AMC#1 header: uGT FW build, MP7 FW version
00000000001e0200 - AMC#1 input: Block ID, Block size, "bankId"=input
... muon input data
021e02000000000 - Block ID, Block size, "bankId"=input [1]
... muon input data AMC#1
00000000041e0200
... muon input data AMC#1
061e020000000000
... muon input data AMC#1
06000200081e0200
... e/gamma input data AMC#1
0a1e020006000200
... e/gamma input data AMC#1
000008000c1e0200
    jet input data AMC#1
0e1e020000000800
... jet input data AMC#1
02000200101e0200
... tau input data AMC#1
121e020002000200
... tau input data AMC#1
0002a02a141e0200
... esums data AMC#1
181e02000006f000
... ext cond input data AMC#1
00000000211e0100 - AMC#1 output: Block ID, Block size, "bankId"=output
    algo output data AMC#1
231e010000000000
    algo output data AMC#1
00000000251e0100
... algo output data AMC#1
271e01005ca70bcc
... algo output data AMC#1
```

```
00000000291e0100
... algo output data AMC#1
2b1e010000000000
... algo output data AMC#1
000000002d1e0100
... algo output data AMC#1
2f1e010000000000
... algo output data AMC#1
00000000311e0100
... algo output data AMC#1
9ac23b761700014a - AMC#1 trailer: CRC, LV1 id (8 bits), Data length
0286f41767500090 - AMC#2 header
000000c0ff300000 - AMC#2 header
0000115200030202 - AMC#2 header
00000000211e0100
    algo output data AMC#2
. . .
231e010000000000
... algo output data AMC#2
00000000251e0100
... algo output data AMC#2
271e01005ca70bcc
... algo output data AMC#2
00000000291e0100
... algo output data AMC#2
2b1e010000000000
... algo output data AMC#2
000000002d1e0100
... algo output data AMC#2
2f1e010000000000
... algo output data AMC#2
00000000311e0100
... algo output data AMC#2
cb79a76317000090 - AMC#2 trailer
0386f41767500090 - AMC#3 header
000000c0ff300000 - AMC#3 header
0000115200030202 - AMC#3 header
00000000211e0100
. . .
\dots AMC#3 - AMC#6
36cb696317000090 - AMC#6 trailer
f6b9461200017675 - AMC13 trailer: CRC, LV1 id (8 bits), BX id
a000042484680000 - AMC13 trailer: Ev. length, CRC (16 bits)
```

1.3 Data block

Description of a block with "block size" = 0x1e (6 frames [32 bits] @ +/-2 bx = 30) of a certain "Block ID" (0x00, 0x02, ..., 0x21, 0x23, ...):

```
0000000001e0200 - bx-2: frame 0
000000000000000 - bx-2: frame 2, frame 1
000000000000000 - bx-2: frame 4, frame 3
0000000000000000 - bx-1: frame 0, bx-2:
                                     frame 5
000000000000000 - bx-1: frame 2, frame 1
                       frame 4, frame 3
000000000000000 - bx-1:
000000000000000 - bx: frame 0, bx-1: frame 5
                     frame 2, frame 1
00000000000000 - bx:
000000000000000 - bx: frame 4, frame 3
000000000000000 - bx+1: frame 0, bx: frame 5
000000000000000 - bx+1: frame 2, frame 1
000000000000000 - bx+1: frame 4, frame 3
000000000000000 - bx+2: frame 0, bx+1: frame 5
000000000000000 - bx+2: frame 2, frame 1
000000000000000 - bx+2: frame 4, frame 3
```

Remark:

The order of +/-2 bx has to be verified!

References

- [1] AMC13 Event Builder: $http://ohm.bu.edu/\sim hazen/CMS/AMC13/UpdatedDAQPath_2014-07-10.pdf\ 1$
- [2] MP7 Readout & DAQ: https://github.com/cms-l1-globaltrigger/mp7_ugt_legacy/blob/master/doc/read_out_record/MP7Readout.pdf 1