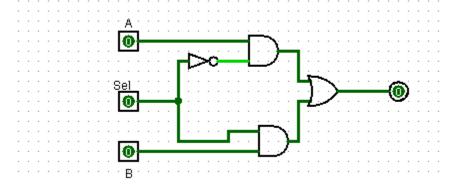


MUX



DMUX In Out Sel

XOR gate

