THESIS TITLE

Submitted in partial fulfillment of the requirements for

the degree of

Doctor of Philosophy

in

Electrical and Computer Engineering

MY NAME

B.S., Electrical & Computer Engineering, University of XYZ

Carnegie Mellon University
Pittsburgh, PA

MONTH YEAR

To Charles Babbage.

Acknowledgments

Go ACTL! Thanks to Dr. Shawn Blanton for being an amazing advisor! Thanks to my amazing ACTL colleagues, CMU collaborators and friends!

Thanks to my committee members: Prof A, Prof B, and Dr. C.

Thanks to my funding sources: Mr. Money Bags.

Abstract

My work was amazing! I did a lot of things that were really important for the field of electrical and computer engineering.

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Chapter 1

Introduction

Welcome everyone to my amazing thesis. We use to be testing vacuum tubes [1], now we're testing hundreds of billions of transistors [2]. Moore's Law [3, 4] sure is crazy!

1.1 Contributions

These are all the crazy things I contributed to the world of computer engineering.

1.2 Organization

This is how my thesis is organized.

Glossary

Example This is an example of a glossary entry.

Acronyms

IC Integrated Circuit.

Bibliography

[1] R. D. Eldred, "Test Routines Based on Symbolic Logical Statements," *Journal of the ACM*, vol. 6, no. 1, pp. 33–37, 1959. 1

[2] A. Tirumala and R. Wong, "NVIDIA Blackwell Platform: Advancing Generative AI and Accelerated Computing," in *IEEE Hot Chips Symposium*, 2024. 1

[3] G. Moore, "Cramming More Components onto Integrated Circuits," *Electronics*, pp. 114–117, 1965. 1

[4] G. E. Moore, "Progress in Digital Integrated Electronics," in *IEEE International Electron Devices Meeting*, pp. 11–13, 1975. 1