

## **Jan13 - Jan17 (Week 1)**

### **Team Updates:**

1. Mainboard V3 WIP
2. Batteryboard V3 WIP
3. Z+/- WIP
4. Power testing
5. Test plan for Mainboard and Batteryboard done

### **Individual Updates:**

Haoran Zheng

1. Reviewed the schematics and PCB to refresh my knowledge of previously assembled boards
2. Wrote test plans for Mainboard V3 and Batteryboard V3
3. Made up for the missing Milestone report

Haoen Li

1. Going over the most up-to-date Mainboard and Battery board schematics and PCB
2. Added potential testing aspects for the test plan

Zhaonan Shi

1. Back to Campus on Thursday, catching up with the progress
2. Went over the testing plans with the team

Varun Rajesh

1. Over break: C Star Tracker implementation: [varun-rajesh/minLOST](https://github.com/varun-rajesh/minLOST)
2. XY board updates (schematic / layout)
3. Solar Panel MPPT implementation testing, debugging
4. Argus-2 GitHub setup + Transferred old library files

