

CMPE 2020A Test 1

NAME:

GT ID NO:

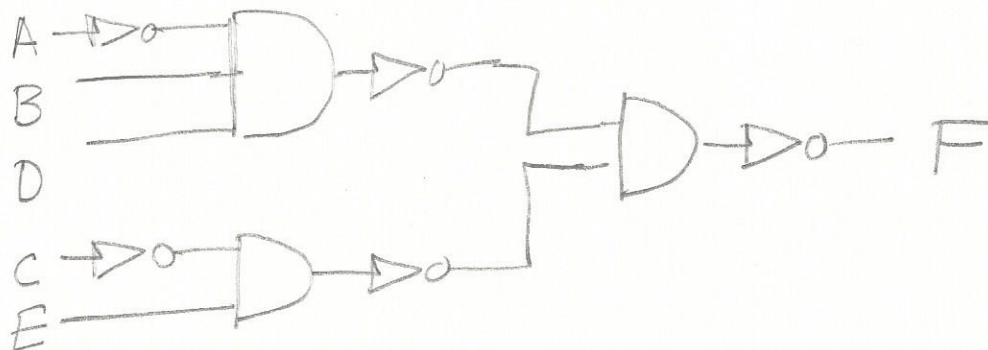
Open book, open notes, no calculators.

Problem 1 (10 points):

- (a) Rewrite the function $F = \overline{(A + (\overline{B + D})) + (C + \overline{E})}$ using only the AND function and inversion. Hint: Use deMorgan's formula.

$$\begin{aligned} F &= \overline{\overline{A} \cdot (\overline{B + D}) + \overline{C} \cdot \overline{E}} = \overline{\overline{A} \cdot BD + \overline{C} \cdot E} \\ &= \overline{(\overline{A}BD) \cdot (\overline{C}E)} \end{aligned}$$

- (b) Draw a circuit for F using only AND gates and inverters from the expression for F derived in part (a)



Problem 2 (10 points):

(a) Convert 347 (OCTAL) to HEXADECIMAL

HEX representation =

$$\begin{array}{ccc} 3 & 4 & 7 \\ \hline 0111 & 0011 & \\ \hline E & 7 & \end{array} = E7$$

(b) Convert 67.35 into binary. Compute the fractional part to 4 bits of accuracy.

Binary representation =

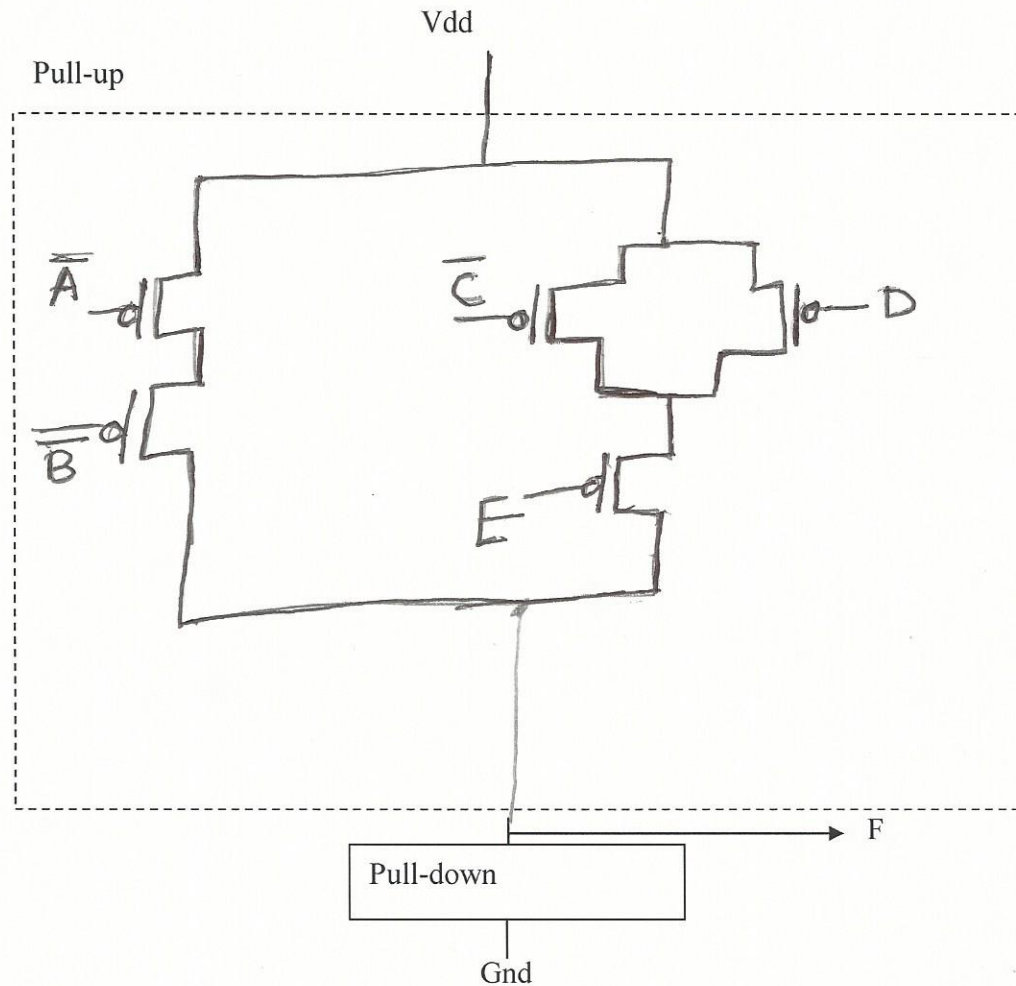
$$1000011.0101$$

$$\begin{array}{r} 2 \overline{)67} \\ 2 \overline{)33} \ 1 \\ 2 \overline{)16} \ 1 \\ 2 \overline{)8} \ 0 \\ 2 \overline{)4} \ 0 \\ 2 \overline{)2} \ 0 \\ \quad 1 \ 0 \end{array}$$

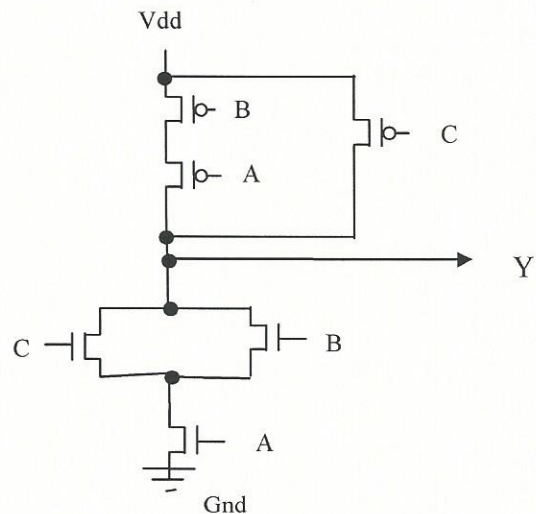
$$\begin{array}{r} .35 \\ \times 2 \\ \hline 0.7 \\ \times 2 \\ \hline 1.4 \\ \times 2 \\ \hline 0.8 \\ \times 2 \\ \hline 1.6 \end{array}$$

Problem 3 (14 points):

- (a) For the complex gate implementation of the function $F = (A \bullet B) + ((C + \overline{D}) \bullet \overline{E})$, draw the pull-up chain below. We assume that the pull-dn chain has already been designed correctly. You may assume that the complemented values of variables are available directly.



(b) For the following circuit,



(a) Does this circuit contain a "short" condition ? i.e. for any input combination does there exist a path from Vdd to Gnd ? If yes, give all input combinations that result in shorts.

Contains shorts ?

☒ Yes | ☐ No (circle one)

Input combinations (ABC) : 110

ABC	PULL-UP $\overline{A}\overline{B} + \overline{C}$	PULL-DOWN $A \cdot (B + C)$	
000	1	0	
001	1	0	
010	1	0	
011	0	0	FLAT
100	1	0	
101	0	1	
110	1	1	SHORT
111	0	1	

(b) Does this circuit contain an "open/float" condition ? i.e. for some input combination there does not exist any path from Vdd to Y AND there does not exist any path from Y to Gnd ? If yes, give all input combinations that result in floats.

Contains floats ?

☒ Yes | ☐ No (circle one)

Input combinations (ABC):

011

Problem 4 (6 points):

Simplify the expression $F = \bar{Z} + XY + (\bar{X} + \bar{Y})Z$ to the smallest number of literals. Show what Boolean identity you used in each step of the reduction.

$$F = \bar{Z} + X \cdot Y + (\bar{X} + \bar{Y}) \cdot Z$$

$$= \bar{Z} + X \cdot Y + (\bar{X} + \bar{Y}) \quad (A + \bar{A}B = A + B)$$

$$= \bar{Z} + \underbrace{X \cdot Y + \overline{X \cdot Y}}_{=1} \quad (A + \bar{A} = 1)$$

$$= 1 + \bar{Z} \quad (A + 1 = 1)$$

$$= 1$$