

XTRX rev4 GPIO functions



GPIO presented on miniPCle connector

Pin#	NAME	GPIO #	Diff P/N Pair	Predefined Function	Type	Note
3	1PPSI_GPIO1(1N)	GPIO1	GPIO1N	External 1PPS input	I	CMOS 3.3V
5	1PPSO_GPIO2(1P)	GPIO2	GPIO1P	GPS 1PPS output	O	CMOS 3.3V
20	TDD_GPIO3_P	GPIO3	GPIO3P	-	I/O	CMOS 3.3V
17	TDD_GPIO3_N	GPIO4	GPIO3N	TDD TX Enable output	O	CMOS 3.3V
42	LED_WWAN#_GPIO5	GPIO5	-	Output for LED WWAN#	O	CMOS 3.3V (Negative) require series resistor for LED
44	LED_WLAN#_GPIO6	GPIO6	-	Output for LED WLAN#	O	CMOS 3.3V (Negative) require series resistor for LED
46	LED_WPAN#_GPIO7	GPIO7	-	Output for LED WPAN#	O	CMOS 3.3V (Negative) require series resistor for LED
32	GPIO8	GPIO8	-	-	I/O	CMOS 3.3V through jumper connected by default

GPIO presented on FPC/FCC connector X12

Pin#	NAME	GPIO#	Diff P/N Pair	Predefined Function	Type	Note
2	GPIO9_P	GPIO9	GPIO9P	-	I/O	CMOS 3.3V
4	GPIO9_N	GPIO10	GPIO9N	-	I/O	CMOS 3.3V
6	GPIO11_P	GPIO11	GPIO10P	-	I/O	CMOS 3.3V
8	GPIO11_N	GPIO12	GPIO10N	-	I/O	CMOS 3.3V

ADC inputs presented on FPC/FCC connector X8

Pin#	NAME	GPIO#	Diff P/N Pair	Predefined Function	Type	Note
2	ADC1I_P	-	ADC1I_P	LMS7002M ADC1 input I+	In	Analog input signal, impedance 100 Ohm ballanced
3	ADC1I_N	-	ADC1I_N	LMS7002M ADC1 input I-	In	Analog input signal, impedance 100 Ohm ballanced
5	ADC1Q_P	-	ADC1Q_P	LMS7002M ADC1 input Q+	In	Analog input signal, impedance 100 Ohm ballanced
6	ADC1Q_N	-	ADC1Q_N	LMS7002M ADC1 input Q-	In	Analog input signal, impedance 100 Ohm ballanced
8	ADC2I_P	-	ADC2I_P	LMS7002M ADC2 input I+	In	Analog input signal, impedance 100 Ohm ballanced
9	ADC2I_N	-	ADC2I_N	LMS7002M ADC2 input I-	In	Analog input signal, impedance 100 Ohm ballanced
11	ADC2Q_P	-	ADC2Q_P	LMS7002M ADC2 input Q+	In	Analog input signal, impedance 100 Ohm ballanced
12	ADC2Q_N	-	ADC2Q_N	LMS7002M ADC2 input Q-	In	Analog input signal, impedance 100 Ohm ballanced

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