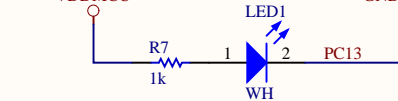
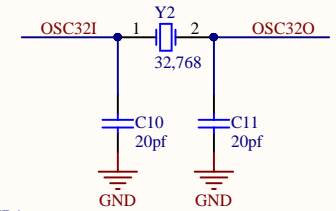
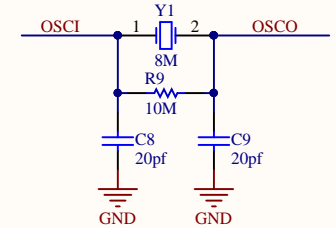
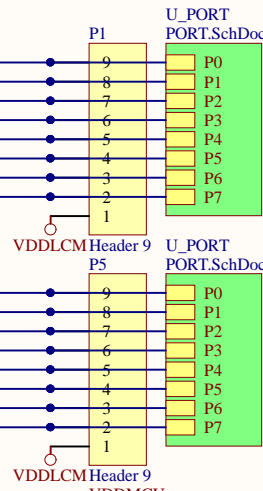
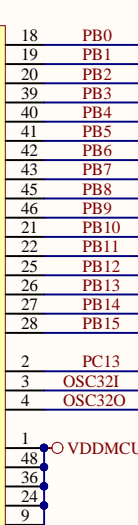
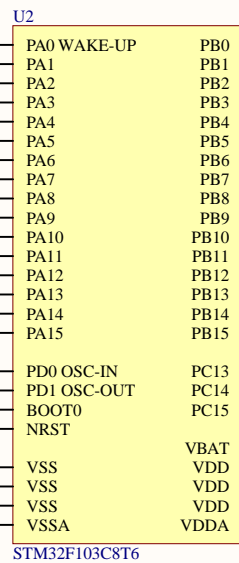
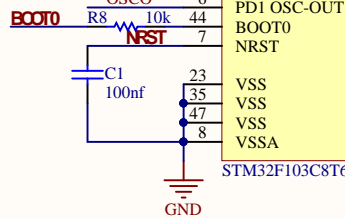
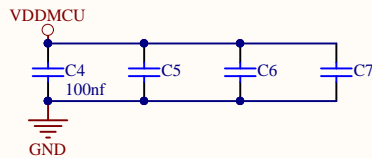
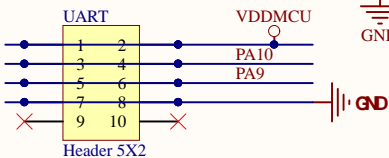
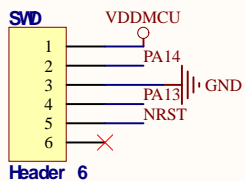
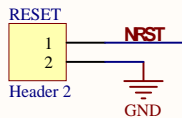
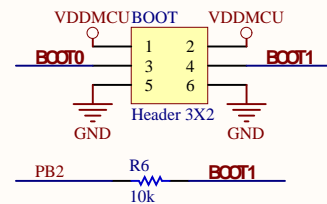
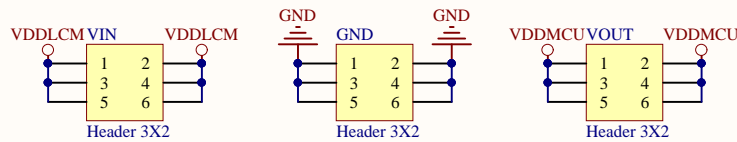
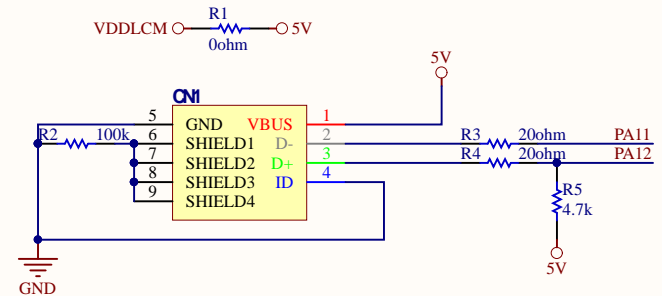
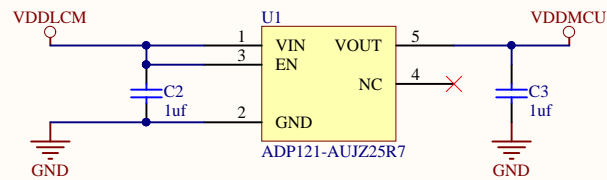


<https://www.youtube.com/watch?v=YZjnCOunlwU>

<https://www.youtube.com/watch?v=YE0juefdoyE>

STM32 VDD RANGE 2.4~3.6
ADP121 VIN 2.3~5.5
ADP121 OUT 1.2/1.5/1.8/2.0/2.5/3.0/3.3



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