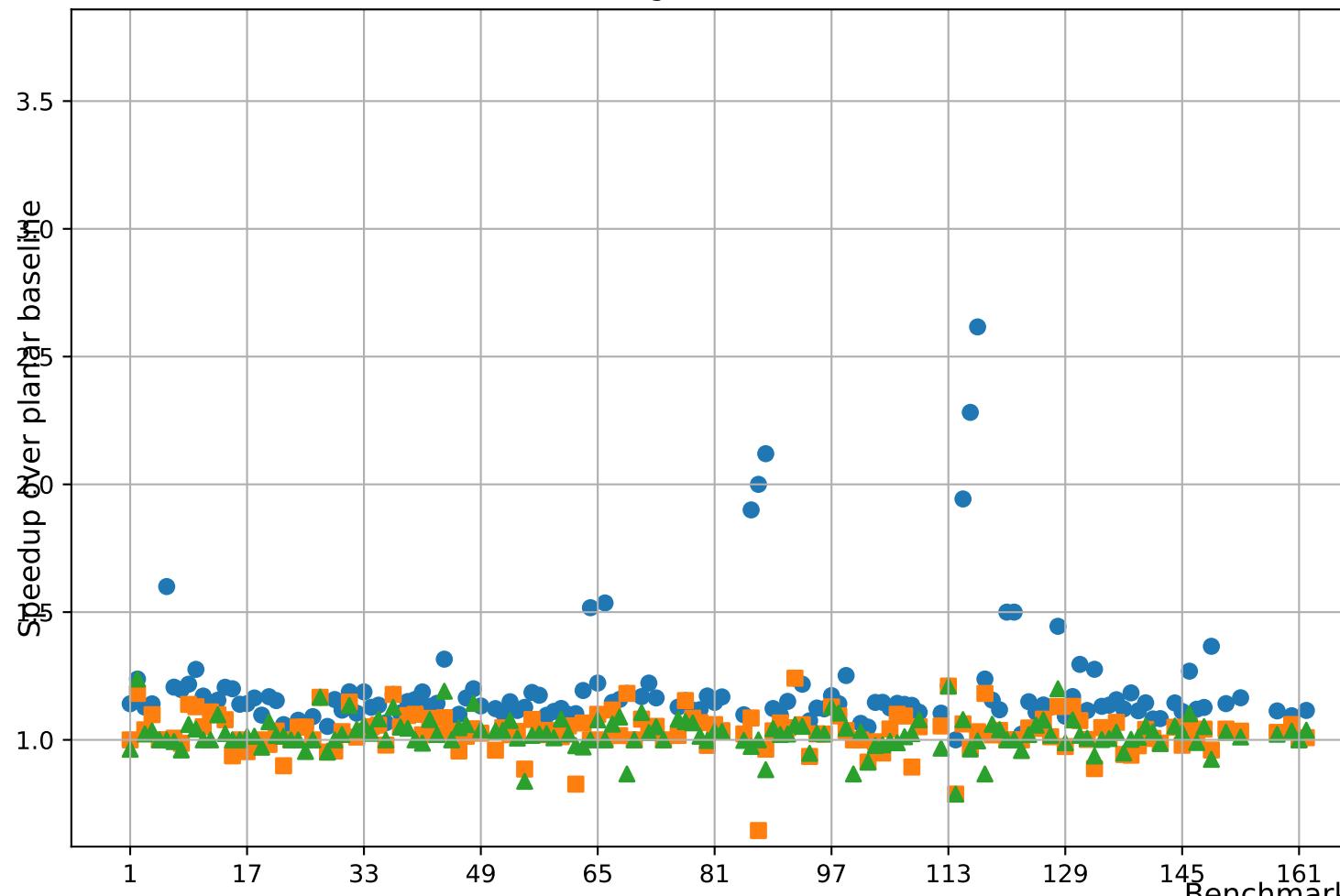


● 1 data qubit sharing single magic state

■ 2 data qubits sharing single magic state

▲ 4 data qubits sharing single magic state

Routing on lower level



Routing on upper level

