U1

Pin	Name		Name	Pin	
1	ANT		GND	16	
2	GND		NSS	15	CS
3	3.3V		MOSI	14	MOSI
4	RESET		MISO	13	MISO
5	DIO0		SCK	12	SCK
6	DIO1		DIO5	11	
7	DIO2		DIO4	10	
8	DIO3		GND	9	

Vcc

REST

DIO0
DIO1
DIO2

C1
0.1uF
Vcc

SX1278

Vcc

2 OUT
1
10uf
C2

IN 3
GND 1
U3
HT72xx
HT7233

4V

C5
0.1uF

Vcc

R1 10k

U2
18B20

Vcc

U4 ATmega8-au

Pin	Name		Name	Pin
30	PD0 (RxD) 0	RX	ADC7	22
31	PD1 (TxD) 1	TX	ADC6	19
32	PD2 (INT0) 2	DIO0		
1	PD3 (INT1) 3	DIO1		
2	PD4 (XCK/T0) 4		A0 (ADC0) PC0	23	DIO2
9	PD5 (T1) 5		A1 (ADC1) PC1	24
10	PD6 (AIN0) 6		A2 (ADC2) PC2	25
11	PD7 (AIN1) 7		A3 (ADC3) PC3	26
			A4 (ADC4/SDA) PC4	27	SDA
			A5 (ADC5/SCL) PC5	28	SCL
			(RESET) PC6	29
12	PB0 (ICP1) 8	REST		
13	PB1 (OC1A) 9	CS		
14	PB2 (SS/OC1B) 10	MOSI	AREF	20
15	PB3 (MOSI/OC2) 11	MISO	AVCC	18
16	PB4 (MISO) 12	SCK		
17	PB5 (SCK) 13			
7	PB6 (XTAL1/OSC1) 20			
8	PB7 (XTAL2/TOSC2) 21			

Vcc
C3
0.1uF

Vcc

SI2301CDS
4V
Q1   S
G
D

R2 1M
B1
R4 1M

D1 SS14

R5 1M

Vcc
R3 10k

S1

TX

RX

0.1uF
C4
J1

| 5 |
| 4 |
| 3 |
| 2 |
| 1 |