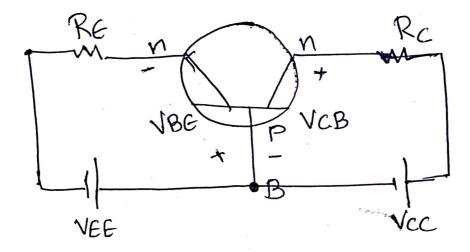
* BJT Configurations! -

The Transistor can be connected in a Circuit in, following 3 configurations.

- 1) Common base configuration.
- 2 Common Gmitter Configuration.
- 3 Common wllecter configuration.

* X common base configuration:-

The common base npn transistor Configuration as shown in fig.



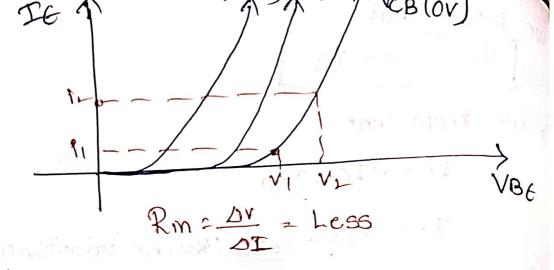
- The Collector and base and output is taken from the Collector and base.
- input and output circuits and hence The name Common base Configuration.

> we know that

IC = DIG + ICBO (Reverse soduredion current in CB when emitter is

* input characterstics: - (IE, VCB Constant

- -> To determine the input characterstics, The · Collector- base voltage is kept constant at zero Volts and The emitter current IE is increased form zero in suitable equal stips by increasing WEG.
- -> when VCB is equi to zero and emitter-base Junction is forward biased of shown in The Characteristics, The Junction behaves as a tormand biased diade so that emitter ament IG increases rapidly with small increases in emiter-base voltage. VBE.

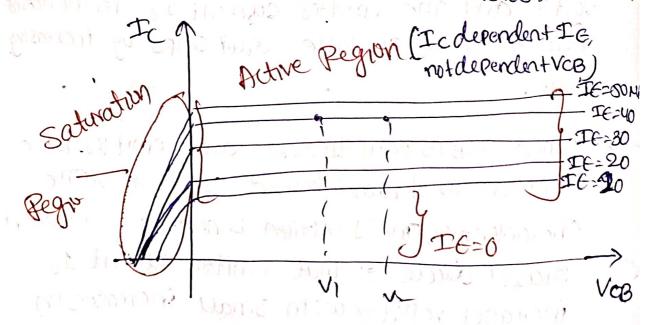


me width of the base region will decrease the control of the base region will decrease this effect results in an increase of It.

Therefore, The curves shift towards the letter as VCB is increased

output characterstics: - (Ic, VCB) (Ic Constant

The relation between Ic and Vob Keeping IE Constant is called output characteristics of common base transistor.

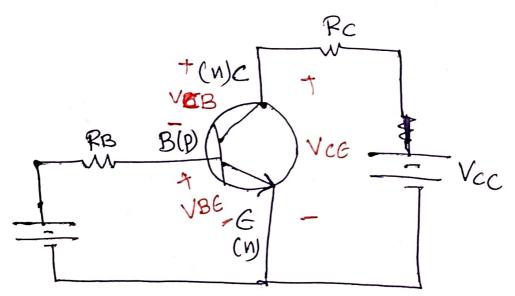


y we know that ITC= O(IE + ICBO we onow mat IC= Q(IC+IB)+ICBO IE=IC+IB IC [I-d] = QIB + ICBO Tc = α TB + α TcB0 (α) α ine infect voltage 14100 CE Configuration is the Drie countries vertige: 301 and The culture Veltage 1- collictes - control vellege meile 1 - Tot is IB air - 1 - Tutput (113787) is Ic.

* common Emitter configuration:

- in This Configuration input is applied between by and emitter and output is laken from collector are emitter.
 - Here, emitter of the transistor is common to both input and output circuits and hence The name common emitter configuration.
 - -> Common emitted Configuration npn transia.

 As shown in fig.
- The input voltage in the CE Configuration is the base -emitter voltage (VBC) and The output Voltage is The Collector-emitter voltage. Theilf Current is IB and The output Current is Ic.



* current Relations in CE configuration > we know that , In CB Configuration IC = OTE + ICBO : TC- TCBO = Q IE IC - ICB = IG - D / we know that IE = Ic + IB - (2) sub of 1 in 1 Ic = IcBO = Ict IB $\frac{T_C - T_C}{\alpha} = T_B + \frac{T_{CBO}}{\alpha}$ $Tc\left[\frac{1}{\alpha}-1\right] = \frac{T_{B}+T_{CBO}}{\alpha}$ TC = IB O TCBO => we know that B= + (all+sl) k-st => me Bac is the ratio of out put current Ic and input current IB in Common emitted Configuration. It is common emitter amplification factor

LACY. = B = IC

MX X-1. = B = IC

MX X-1. = B = TC

MX X-1 we know that IG = IC+IB = IB = IE-Ic TE-IC TO STE B = IC/IG (X= IC)

$$\frac{1}{1-\alpha}$$

we know that $\alpha = \frac{TC}{TE}$ and TE = TC + TB.

$$A = \frac{IC}{IC + IB}$$

Dividing me numerator and demoninator oby

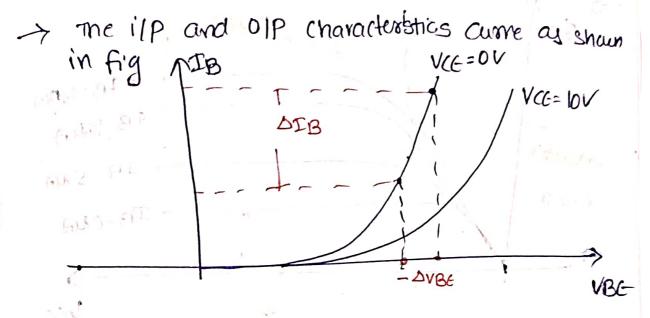
$$C = \frac{IC|IB}{IC} = \frac{B}{I+B}$$

* we know that

 $\mathcal{I}_{\mathcal{B}}$

* input characteristics:-

and input current IB at constant Collector-emitter Voitage VCE. The base current is taken along y-axis and base emitter Voitage VBE is taken along x-axis.



> The input Resistance is the ratio of charge in base emitter voltage(2VBE) to the resulting charge in base current (DIB) at Constant Collector emitter Voltage VCE.

The value of Pi in CE Configuration is greater than the value of Pi in CB configuration.

* out put Characteristics !-

1. This Chavackristics shows the selation between The Collector Current Ic and Collector Voltage VCG, & Various fixed Valued of IB. . In Min

A ctive Region IB: WHA TB=ONA spanis to other sund somattoff Regin

samp beinner run if 1981/5/60/101 stimm negy

in kest (union (273) at Consider allection entions

value of 19 in it antiquidation is greater than the value of 19 in as antiquethor.

STYVES VIB + VEG

4.7.3 Common Collector Configuration

- In this configuration, input is applied between base and collector and output is taken from emitter and collector.
- Here, collector of the transistor is common to both input and output circuits and hence the name common collector configuration. It is also known as emitter follower configuration.

• Common collector connections for both npn and pnp transistors are shown in

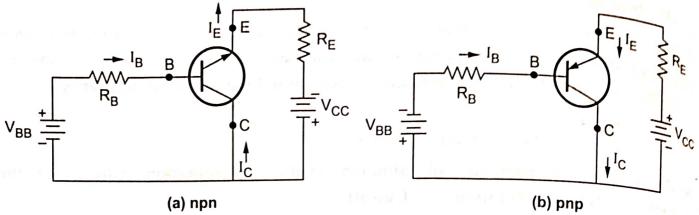


Fig. 4.7.12 Common collector configurations

4.7.3.1 Current Relations in CC Configuration

We know that

$$I_E = I_B + I_C = I_B + \alpha_{dc} I_E + I_{CBO}$$

$$\therefore I_{E}(1-\alpha_{dc}) = I_{B} + I_{CBO}$$

$$\therefore I_{E} = \frac{I_{B}}{1 - \alpha_{dc}} + \frac{I_{CBO}}{1 - \alpha_{dc}}$$

We know that

$$\beta_{dc} = \frac{\alpha_{dc}}{1 - \alpha_{dc}}$$

$$\therefore 1 + \beta_{dc} = 1 + \frac{\alpha_{dc}}{1 - \alpha_{dc}} = \frac{1 - \alpha_{dc} + \alpha_{dc}}{1 - \alpha_{dc}} = \frac{1}{1 - \alpha_{dc}}$$

$$I_E = I_B(1+\beta_{dc}) + I_{CBO}(1+\beta_{dc})$$

Neglecting I_{CBO} we have

$$I_E = I_B(1 + \beta_{dc})$$

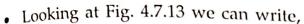
.. Current gain in CC configuration is given by

$$\gamma = \frac{I_E}{I_B} = (1 + \beta_{dc}) = 1 + \frac{\alpha_{dc}}{1 - \alpha_{dc}} = \frac{1}{1 - \alpha_{dc}}$$

4.7.3.2 Common Collector Input Characteristics

- The input characteristics of CC configuration is a graph of input current $^{l_{B}}$ (base current) versus input voltage V_{CB} (collector base voltage) at constant V_{CE} .
- The base current is taken along Y-axis and collector base voltage V_{CB} is taken along X-axis.

- Fig. 4.7.13 shows the input characteristics of a typical transistor in common-collector configuration.
- common collector input The characteristics are quite different either common base from or common emitter input characteristics. This difference is due to the fact that the input voltage VCB is largely determined by the level of collector to emitter voltage V_{CE}.



$$V_{CE} = V_{CB} - V_{BE}$$

$$V_{CB} = V_{CE} + V_{RE}$$

or

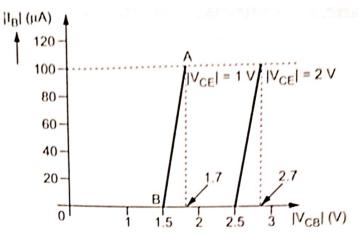


Fig. 4.7.13 Input characteristics of transistor in CC configuration

• In CC configuration input junction is BC and it is reversed biased so input resistance in CC configuration is very high.

47333 Common Collector Output Characteristics

- It is the curve between emitter current I_E and collector to emitter voltage V_{CE} at constant base current I_B .
- The emitter current is taken along Y-axis and collector to emitter voltage along X-axis.
- Fig. 4.7.14 shows the output characteristics of a typical transistor in common collector configuration.

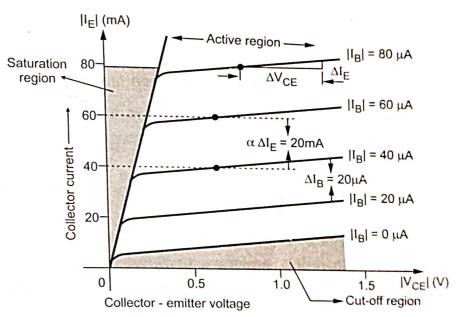


Fig. 4.7.14 Output characteristics of the transistor in CC configuration

Since, I_C is approximately equal to I_E, the common collector output characteristics
are practically similar to those of the common emitter output characteristics.