

## Motivation

We investigated the optimization of multi-channel filtering signals to overcome current limitations in real-time signal processing of neural recordings.

Open Ephys GUI, an open source platform for analyzing recorded neural signals in real time with which we work, requires heavy CPU usage when processing >32 signals simultaneously.

## Methodology

High-level overview of coding strategies implemented throughout optimization process

Selecting Optimal Difference Equation

Direct Form II (DF2) Transposed Implementation

Intrinsics

Compiler/architecture optimizations

Matrix Operations

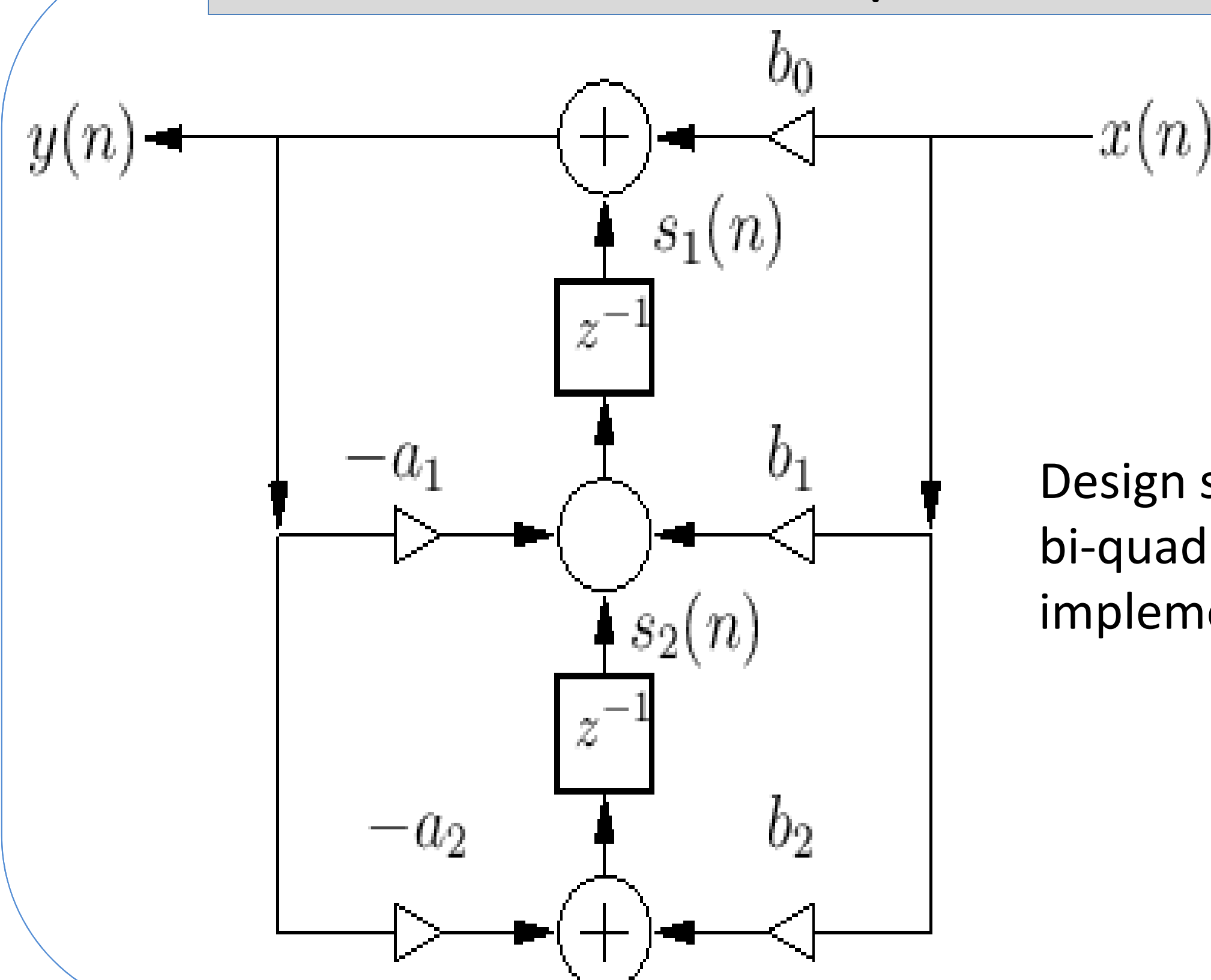
POSIX Threads

Partial Reordering

Full Reordering

Without Reordering

## Direct Form II Transposed Structure

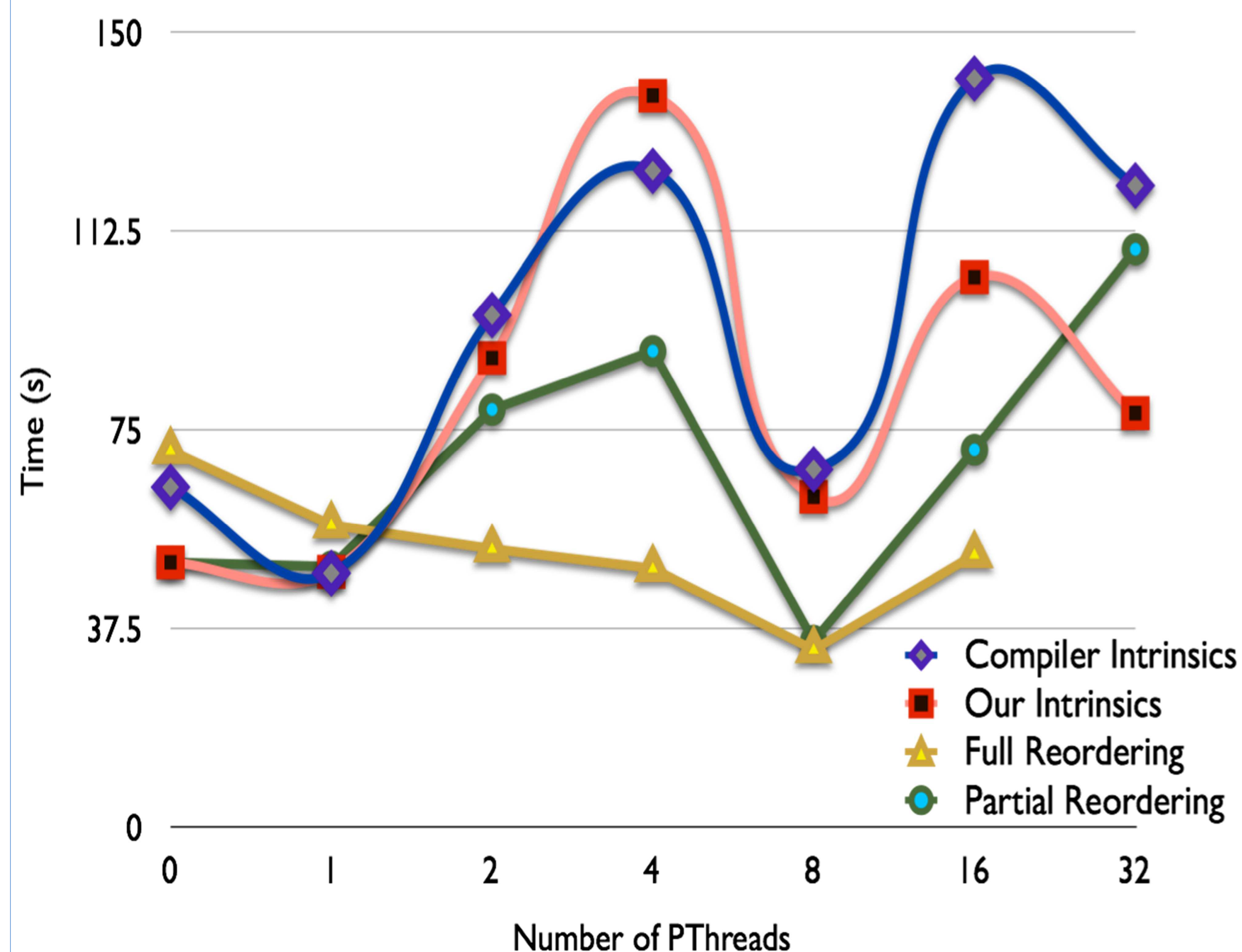


## Test Parameters

All data was collected on a laptop equipped with an AMD A6-3400M quad-core processor, which supports a clock rate of up to 2.3 GHz, under these conditions:

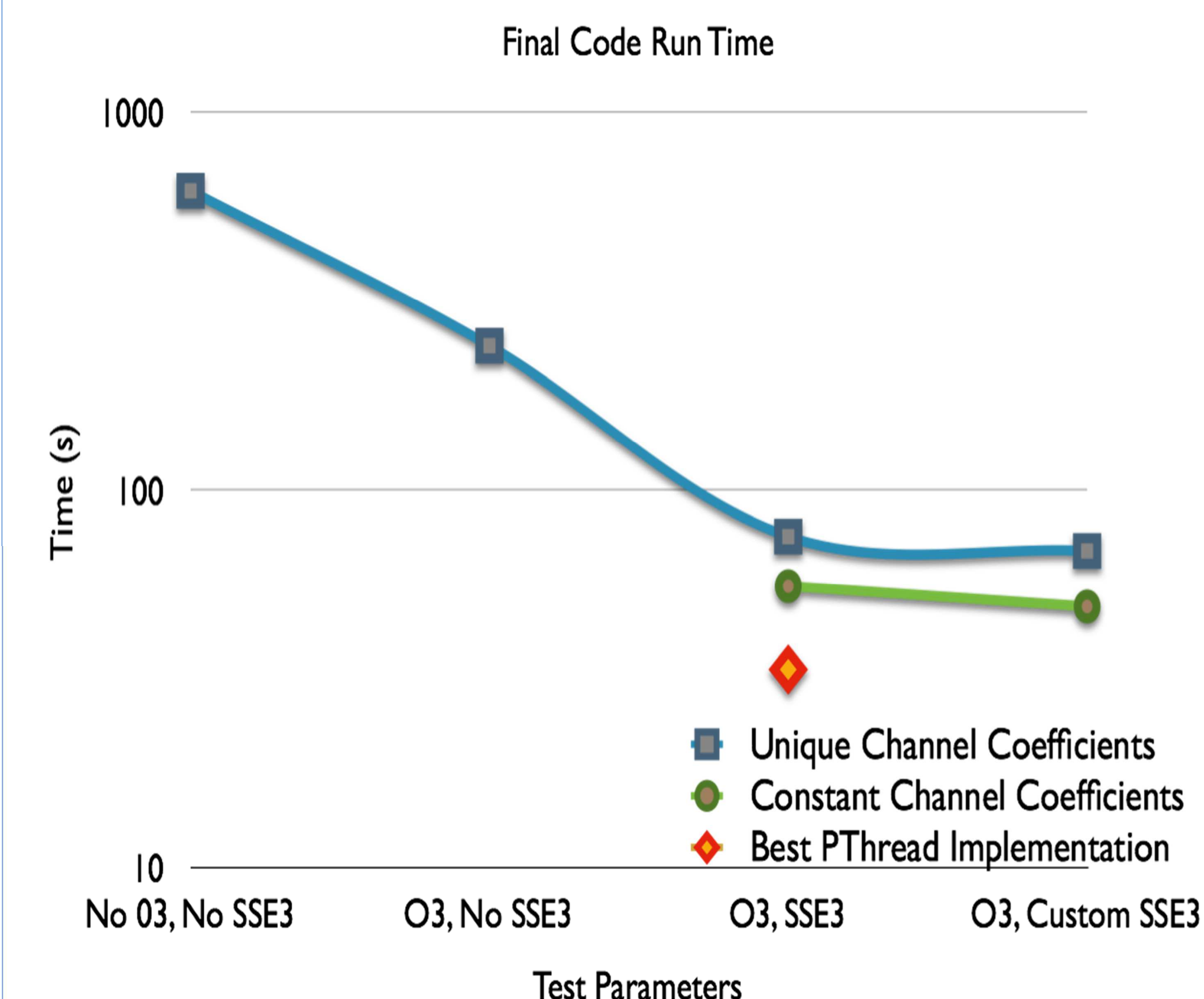
- Implemented filter: 4-pole Butterworth bandpass filter
- Number of input channels = 256
- Number of time samples = 600,000
- Number of data filter cycles = 100

## Code Run Time – PThread Implementation



**Figure 1. Total Code Operation Time – PThread Implementation.** 4 tests under various PThread control conditions demonstrate similar patterns according to the number of PThreads used during code execution.

- Compiler Intrinsics – Implementation with naïve data structures relying purely on compiler optimizations of SSE3 intrinsics
- Our Intrinsics – Implementation with naïve data structures using our custom SSE3 intrinsics code
- Partial Reordering – Implementation assigning an output vector to each thread utilizing compiler intrinsics
- Full Reordering – Intermediate variables are aligned by channel and separated by thread



**Figure 2. Total Code Operation Time.** Both tests show code execution times based on different methods of compiler optimization. Default features included slow arrangement of data and 4 separate w vectors for temporary variables. Test 1 had a & b coefficients in vectors; Test 2 fixed coefficients for all channels.

## Conclusions and Future Work

Our optimal filter implementation utilized the DF2t filter structure with 8 PThreads and innovative data structuring with SSE3 intrinsics instruction set. Results suggest that 8 PThreads provide the perfect combination between cache hits, CPU utilization, and no cache poisoning. Based on our code implementation results, there are three key areas for future directions for this project.

- Optimizing code for GPGPU OpenCL framework
- Analysis of multiple architectures for optimized filter performance
- Integrate code into open source Open Ephys GUI

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