MSP430F16x Processor VCC_3.3 VCC_A VCC_3.3 U16 DVcc AVcc R15 SW1 12 P1.0/TACLK 13 P1.1/TA0 14 P1.2/TA1 15 P1.3/TA2 16 P1.4/SMCLK 17 P1.5/TA0 18 P1.6/TA1 19 P1.7/TA2 2,6 ETHER_DATA_00 /RST/NMI 2.6 ETHER_DATA_00 2.6 ETHER_DATA_01 2.6 ETHER_DATA_02 2.6 ETHER_DATA_03 2.6 ETHER_DATA_04 2.6 ETHER_DATA_05 2.6 ETHER_DATA_06 2.6 ETHER_DATA_06 PB C6 0.1uF GND_D GND_D 20 P2.0/ACLK 21 P2.1/TAINCLK 22 P2.2/CAOUT/TA0 23 P2.3/CAO/TA1 3 BUTTON_1 3 BUTTON_2 4 FILTER_CLK_SWITCH 4 AD_FILTER_CLK_TMRA 24 P2.3/CA0/TA1 25 P2.5/Rosc 26 P2.6/ADC12CLK/DMAE0 P2.7/TA0 4 DAC_FILTER_CLK_TMRA 3 LED_RED 3 LED_GREEN 3 LED_YELLOW MSP_JTAG_RST# 4 MSP_JTAG_TCK 4 MSP_JTAG_TMS 4 MSP_JTAG_TDI 4 MSP_JTAG_TDO 4 _57 56 TCK TMS 28 29 30 93.1/SIMO0/SDA P3.2/SOMIO 4 USR_0 3 I2C_SDA 3 UART_INV# 3 I2C_SCL 4 UART_TX_CYG 4 UART_RX_CYG 3 UART_TX TDI TDO/TDI 30 P3.2/SOMI0 31 P3.3/UCLK0/SCL 32 P3.4/UTXD0 93.5/URXD0 93.6/UTXD1 P3.7/URXD1 C49 52 CLK_02_OUT XT2OUT 36 P4.0/TB0 37 P4.1/TB1 38 P4.2/TB2 40 P4.4/TB3 41 P4.5/TB5 42 P4.6/TB6 P4.7/TBCLK 2,6 ETHER_ADDRESS_00 2,6 ETHER_ADDRESS_02 2,6 ETHER_ADDRESS_02 2,6 ETHER_ADDRESS_03 4,6 AD_FILTER_CLK_TMRB 4,6 DAC_FILTER_CLK_TMRB 2,6 ETHER_IOW# 2,6 ETHER_IOW# Y3 T 8.0000 MHZ 18pF C50 XT2IN 18pF CLK_01_OUT GND_D Xout/Tclk Y2 32.768 KHZ 3 SPI_MEM_CS# 3 SPI_MOSI 3 SPI_MISO 3 SPI_CLK 3 SPI_MEM_RST# 4 USR_1 2 ETHER_SLEEP 2 ETHER_RESET 44 P5.0/STE1 94 P5.0/STE1 45 P5.1/SIMO1 46 P5.2/SOMI1 47 P5.3/UCLK1 95.3/UCLK1 95.5/SMCLK 95.5/SMCLK 95.7/TBoutH/SVSOUT CLK_01_IN XIN VRef+ 5 ANAO_IN 5 ANA1_IN 5 ANA2_IN 5 ANA3_IN 6 MIC_IN 4 SYS_CURRENT 5 DACO_OUT 5 DAC1_OUT 59 60 P6.1/A1 10_X VeRef+ 61 P6.2/A2 Vref-/VeRef- 11 X 2 P6.3/A3 P6.4/A4 4 P6.5/A5 P6.6/A6/DAC0 P6.7/A7/DAC1/SVSIN 63 DVss AVss MSP430F169 GND_D GND_A MSP430F16x Processor Document Number MSP430F16x_DLX Saturday, June 19, 2004











