## MSP430F16x Processor The jumpers on these two connectors allow the peripherals (RS-232, LEDs, 7-segment & pushbuttons) to be disconnected from the MSP430F16x. VCC\_3.3 VCC\_A VCC\_3.3 U5 DVcc AVcc R12 10K J8 SW1 P1.0/TACLK 12 P1.0/TACLK 13 P1.1/TA0 14 P1.2/TA1 15 P1.3/TA2 16 P1.4/SMCLK P1.5/TA0 P1.6/TA1 19 P1.7/TA2 /RST/NMI PB 12 14 16 18 20 22 24 GND\_D GND\_D P2.0/ACLK P2.0 21 P2.0/ACLK P2.1/TAINCLK P2.2/CAOUT/TA0 P2.1 P2.2 23 P2.2/CAOUT/TA0 24 P2.3/CA0/TA1 25 P2.5/Rosc 26 P2.6/ADC12CLK/DMAE0 P2.7/TA0 P2.3 P2.4 P2.5 P2.6 P2.7 CONN13x2 Port1(0:7) GND\_D TCK 57 TMS TDI TDO/TDI P3.0 P3.1 P3.2 P3.3 P3.0/STE0 29 P3.0/STE0 29 P3.1/SIMO0/SDA 30 P3.2/SOMI0 31 P3.3/UCLK0/SCL P3.4/UTXD0 P3.5/URXD0 P3.5/URXD0 J13 3 UARTO\_TX\_CYG 3 UARTO\_RX\_CYG 34 P3.6/UTXD1 P3.7/URXD1 C16 18 pF 2 UART1\_TX 2 UART1\_RX 36 P4.0/TB0 P4.1/TB1 P4.1/TB1 P4.2/TB2 P4.3/TB3 40 P4.4/TB4 41 P4.5/TB5 P4.6/TB6 P4.7/TBCLK XT2OUT P4.0 P4.1 P4.2 P4.3 P4.4 P4.5 P4.6 P4.7 Y2 8.0000 MHz (DNI) CONN5x2 Port2(0:3) XT2IN GND\_D Xout/Tclk 44. P5.0/STE1 P5.1/SIMO1 P5.2/SOMI1 P5.2/SOMI1 P5.4/MCLK P5.4/MCLK P5.5/SMCLK P5.5/SMCLK P5.7/TBoutH/SVSOUT Y1 32.768 KHz (DNI) P5.0 P5.1 P5.2 P5.3 P5.4 P5.5 P5.6 P5.7 59 P6.0/A0 P6.1/A1 P6.2/A2 P6.3/A3 P6.4/A4 P6.5/A5 P6.6/A5/D P6.0 P6.1 P6.2 P6.3 P6.4 P6.5 P6.6 P6.7 →>> Vref-/VeRef- 2 Vref-/VeRef-P6 6/A6/DACO P6.7/A7/DAC1/SVSIN AVss DVss MSP430F169 GND\_D GND\_A 3 SYS\_CURRENT <<-MSP430F16x Processor Document Number MSP430F16x\_LITE x1 Wednesday, May 19, 2004





