

A

B

C

D

A

B

C

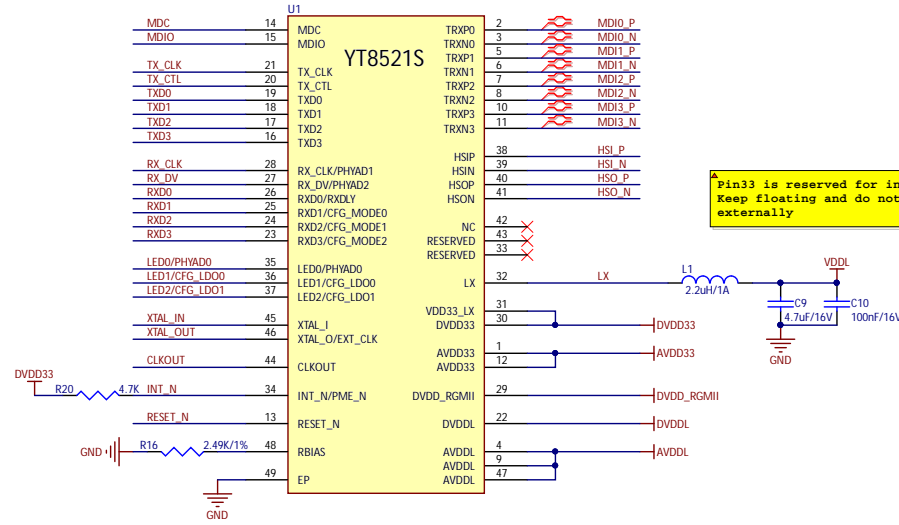
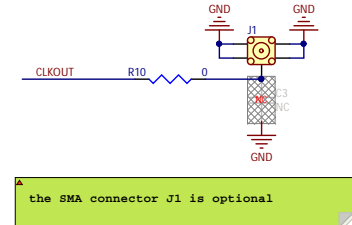
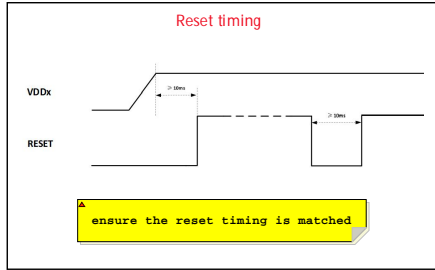
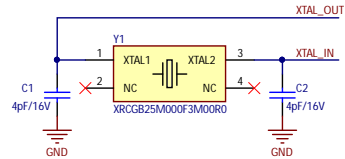
D

REVISION	DATE	COMMENTS
1.0	12/20/2019	initial version
1.1	02/17/2020	Add SFP and update pin33 comment
1.2	06/12/2020	Update YT8521S symbol Update comments

# YT8521S Reference Design V1.2

Sheet Title: <i>TITLE</i>	Drawn By: <i>OWB</i>
Project Title: <i>YT8521S Reference Design</i>	Revision: 1.2   Size: A3
Designed for:	Sheet 1 of 3
Number:	Mod. Date: 2020/6/12





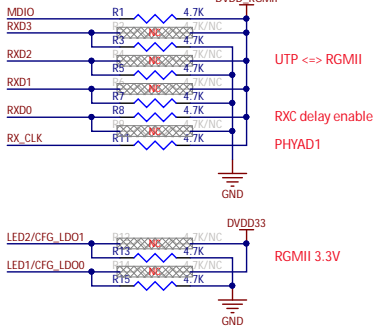
Pin33 is reserved for internal use. Keep floating and do not pull up externally

If RESET, INT, LED0, CFG\_LDO[1:0] need to be pulled up, they should be pulled up to DVDD33

If RXD[3:0], RX\_CLK, RX\_DV need to be pulled up, they should be pulled up to DVDD\_RGMII

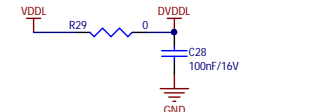
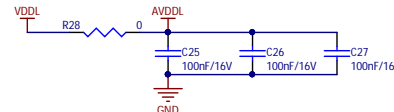
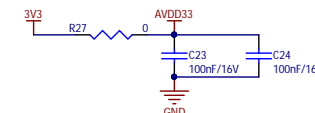
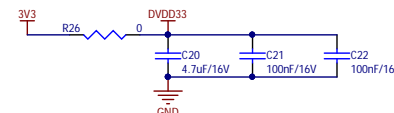
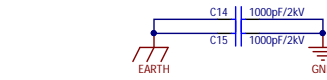
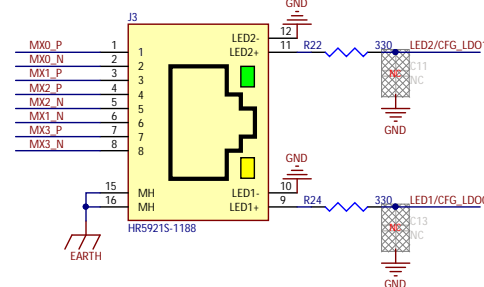
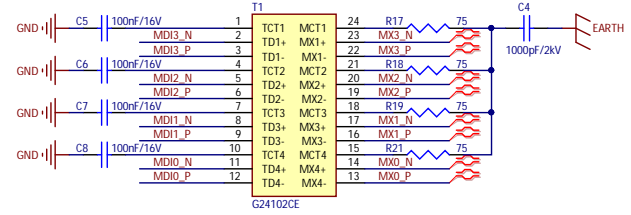
RXD[3:1]	Operation Mode
3'b000	UTP <=> RGMII
3'b001	FIBER <=> RGMII
3'b010	UTP/FIBER <=> RGMII
3'b011	UTP <=> SGMII
3'b100	SGMII (PHY) <=> RGMII
3'b101	SGMII (MAC) <=> RGMII
3'b110	UTP <=> FIBER (AUTO)
3'b111	UTP <=> FIBER (FORCE)

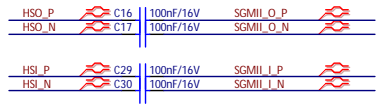
CFG_LDO[1:0]	RGMII Voltage Selection
2'b00	External 3.3V
2'b01	Internal 2.5V
2'b10	Internal 1.8V
2'b11	Not Available



if the LED pin is pulled high, it will be an active low output. And if the LED pin is pulled low, it will be an active high output.

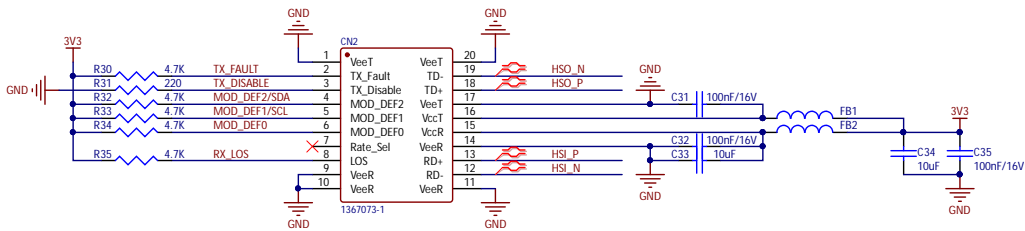
R25 is only for 3.3V RGMII application. R25 must be removed for 2.5V/1.8V RGMII application.





For SGMII interface, capacitors should be close to the source

For fiber mode, C16 C17 C29 and C30 are not needed. Connect HSO\_P/N HSI\_P/N to SFP directly



This part is only for reference.  
Please refer to SFP vendor's specification during your design.

