

Version	Date	Author	Change Note	Approved
V1.0	20160325	hxs	First edictor	
V1.0	20160420	hxs	1、修改 供电用SYR83R 2、删除VDD_CPUB、VDD_CPU连接点采集电路 3、R401修改成 4、修改R4010位置主 控页注意PCB布局靠控制器 5、修改VREF_DR1、VREF_DR0从VCC取电	

## RK3399 I2C MAP

## 3399E I2C MAP

Port	Pin name	Pull-up voltage	Bus name		ADDRESS
I2C0	GPIO1_B7/SPI3_RXD/I2C0_SDA GPIO1_C0/SPI3_TXD/I2C0_SCL	VCC3V0_PMU VCC3V0_PMU	I2C_SDA_PMIC I2C_SCL_PMIC	Rockchip RK808D	
				RK837	
				RK838	
				AT8563 _ RTC	
I2C1	GPIO4_A1/I2C1_SDA	VCC1V8_CODEC	I2C_SCL_AUDIO	ALC5651	
	GPIO4_A2/I2C1_SCL	VCC1V8_CODEC	I2C_SDA_AUDIO	MIPI CAM	
I2C2	GPIO2_A0/VOP_D0/CIF_D0/I2C2_SDA				
	GPIO2_A1/VOP_D1/CIF_D1/I2C2_SCL				
I2C3	GPIO4_C0/I2C3_SDA/UART2B_RX	VCC3V0_IO	I2C_SDA_HDMI	HDMI ID	
	GPIO4_C1/I2C3_SCL/UART2B_TX	VCC3V0_IO	I2C_SCL_HDMI	HDMI ID	
I2C4	GPIO1_B3/I2C4_SDA GPIO1_B4/I2C4_SCL	VCC3V0_IO		TC358775	
		VCC3V0_IO		MCU	
		VCC3V0_IO		AT24C16	
		VCC3V0_IO		AT88SC	
		VCC3V0_IO		FUSB302MPX	
I2C5					
I2C6	GPIO2_B1/SPI2_RXD/CIF_HREF/I2C6_SDA	VCC3V0_IO			
	GPIO2_B2/SPI2_TXD/CIF_CLKIN/I2C6_SCL	VCC3V0_IO			
I2C7	GPIO2_A7/VOP_D7/CIF_D7/I2C7_SDA	VCC3V0_IO	I2C_SDA_TP	TP	
	GPIO2_B0/VOP_CLK/CIF_VSYNC/I2C7_SCL	VCC3V0_IO	I2C_SCL_TP	TC358749XBG HDMI IN	



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3.3V GPIO LEVEL

MIPI CSI0	GPIO NAME	FUNCTION	NAME
	GPIO2_B4/DVP_PDN0_H	HDMIIN_PWREN	
	SPI1_CLK	HDMIIN_INT	GPIO1_B1
	GPIO2_A1/CIF_D1	HDMIIN_STBY	
	CPU_IR	HDMIIN_IR	
	SPI1_CSn0	HDMIIN_RST	GPIO1_B2
	I2C_SDA_HDMI	I2C_HDMI_IN	I2C3
	MIPI_CSI0	MIPI_CSI	

1.8V GPIO LEVEL

MIPI CAM	GPIO NAME	FUNCTION	NAME
	DVP_PDN1_H	MIPI_PWDN	GPIO1_D4
	GPIO2_B1/CIF_HREF	MIPI_RST	
	GPIO2_B3/CIF_CLKO	MIPI_MCLK_T2	
	I2C_SCL_AUDIO	I2C_CAM	I2C1
	MIPI_CSI1	MIPI_CSI1_CAM	MIPI_CSI1/DSI1

3.3V GPIO LEVEL

TP	GPIO NAME	FUNCTION	NAME
	I2C_SDA_HDMI	TP_I2C	I2C3
	TOUCH_INT_L	TP_INT	GPIO1_C4
	TOUCH_RST_L	TP_RESET	GPIO4_C6

3.3V GPIO LEVEL

MCU	GPIO NAME	FUNCTION	NAME
	I2C_MEMS	MCU_CPU_I2C	I2C4
	CHG_OK_H	WORK_DET	GPIO1_A1
	RTC_I2C	MCU_RTC_I2C	
	PMU_PWRON	POWER_KEY	PMU_PWRON
	COMP_INT_L	OFF-INT#	GPIO1_A0
	RTC_INT	MCU_INT	

3.3V GPIO LEVEL

LVDS	GPIO NAME	FUNCTION	NAME
	CABC_EN	REST_LVDS_IC	GPIO4_D5
	EFUSE_VQPS_EN_H	LVDS_PANEL_EN	GPIO4_D3
	LCD_EN_H	LCD_EN_H	GPIO1_B5
	LCD_BL_PWM	LCD_BL_PWM	PWM0
	MIPI_DSI0	MIPI_DSI0	
	RESX	LVDS_DET	GPIO2_D3

EDP

EDP	GPIO NAME	FUNCTION	NAME
	CABC_EN	REST_LVDS_IC	GPIO4_D5
	LCD_RST_H	EDP_PANEL_EN	GPIO4_D3
	LCD_EN_H	LCD_EN_H	GPIO1_B5
	LCD_BL_PWM	LCD_BL_PWM	PWM0
	MIPI_DSI0	MIPI_DSI0	
	DVP_PWR_H	EDP_DET	GPIO0_B0

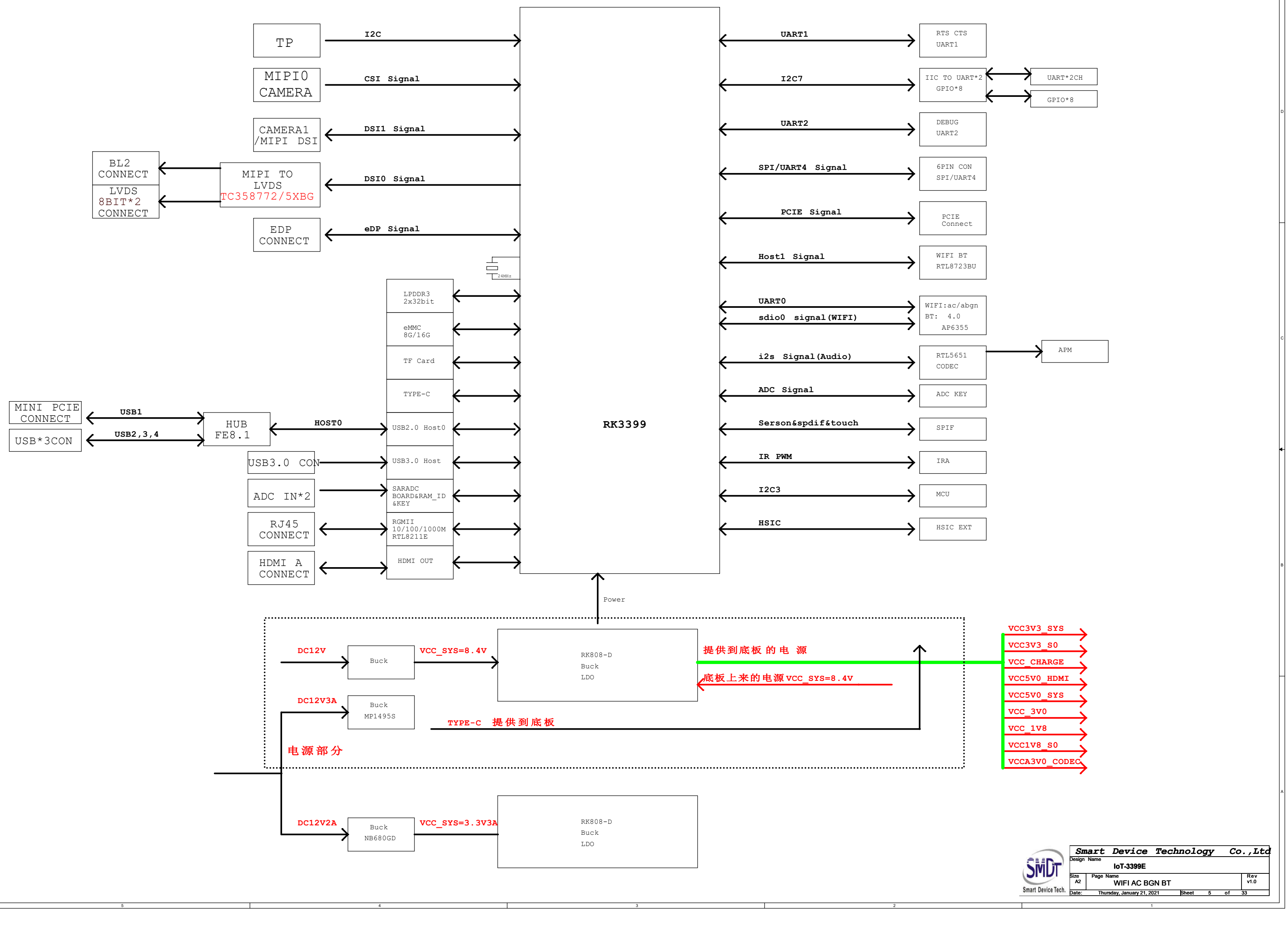
3.3V GPIO LEVEL

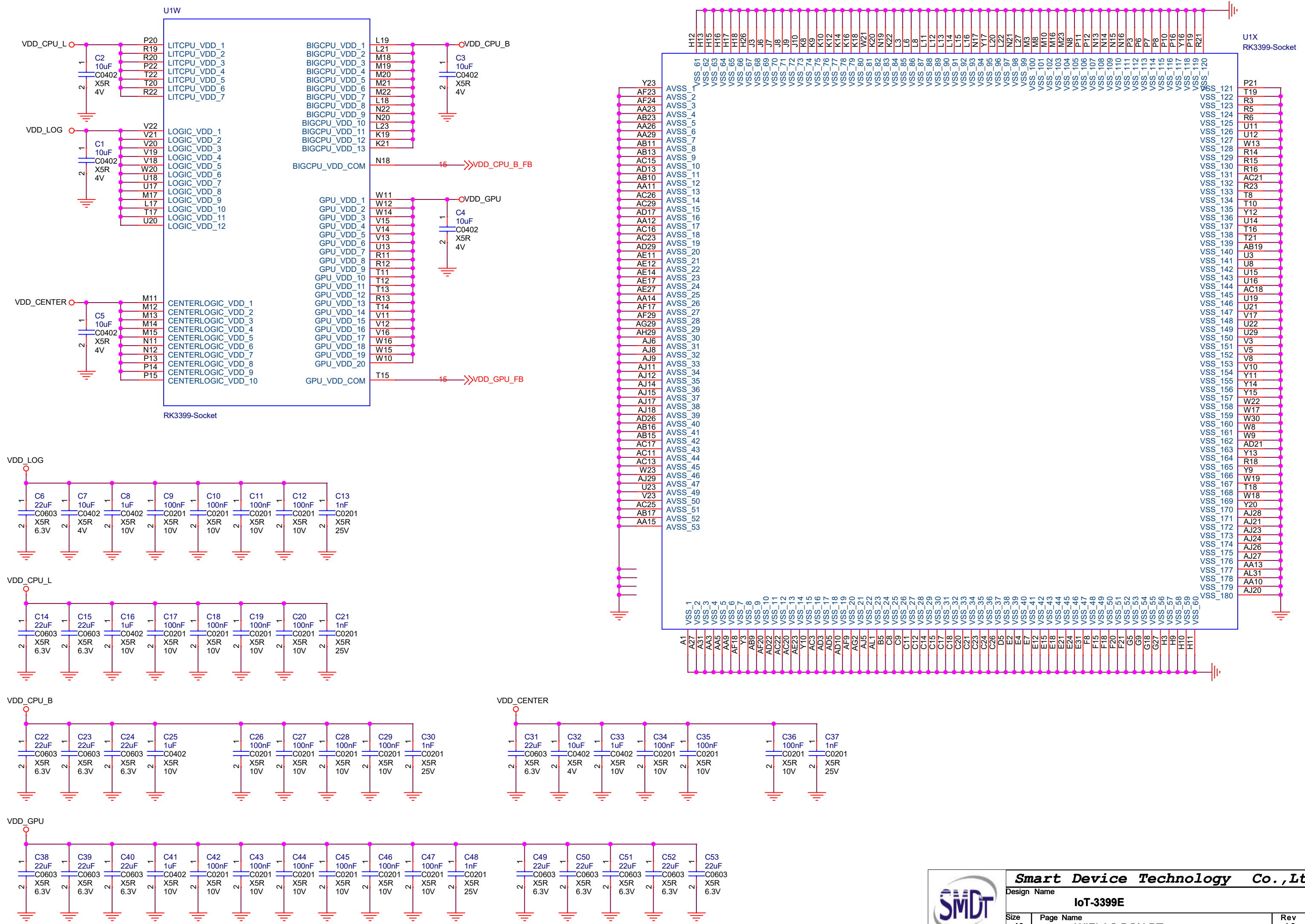
I2C TO UART	GPIO NAME	FUNCTION	NAME
	GPIO2_B0/CIF_VSYNC GPIO2_A7/CIF_D7	I2C TO UART	I2C7
	GPIO2_A6/CIF_D6	RST	GPIO2_A6
	GPIO2_A5/CIF_D5	INT	GPIO2_A5

3.3V GPIO LEVEL

LED	GPIO NAME	FUNCTION	NAME
	CHG_OK_H	POWER_LED	GPIO1_A1
	Monter	WORK_LED	GPIO1_C2

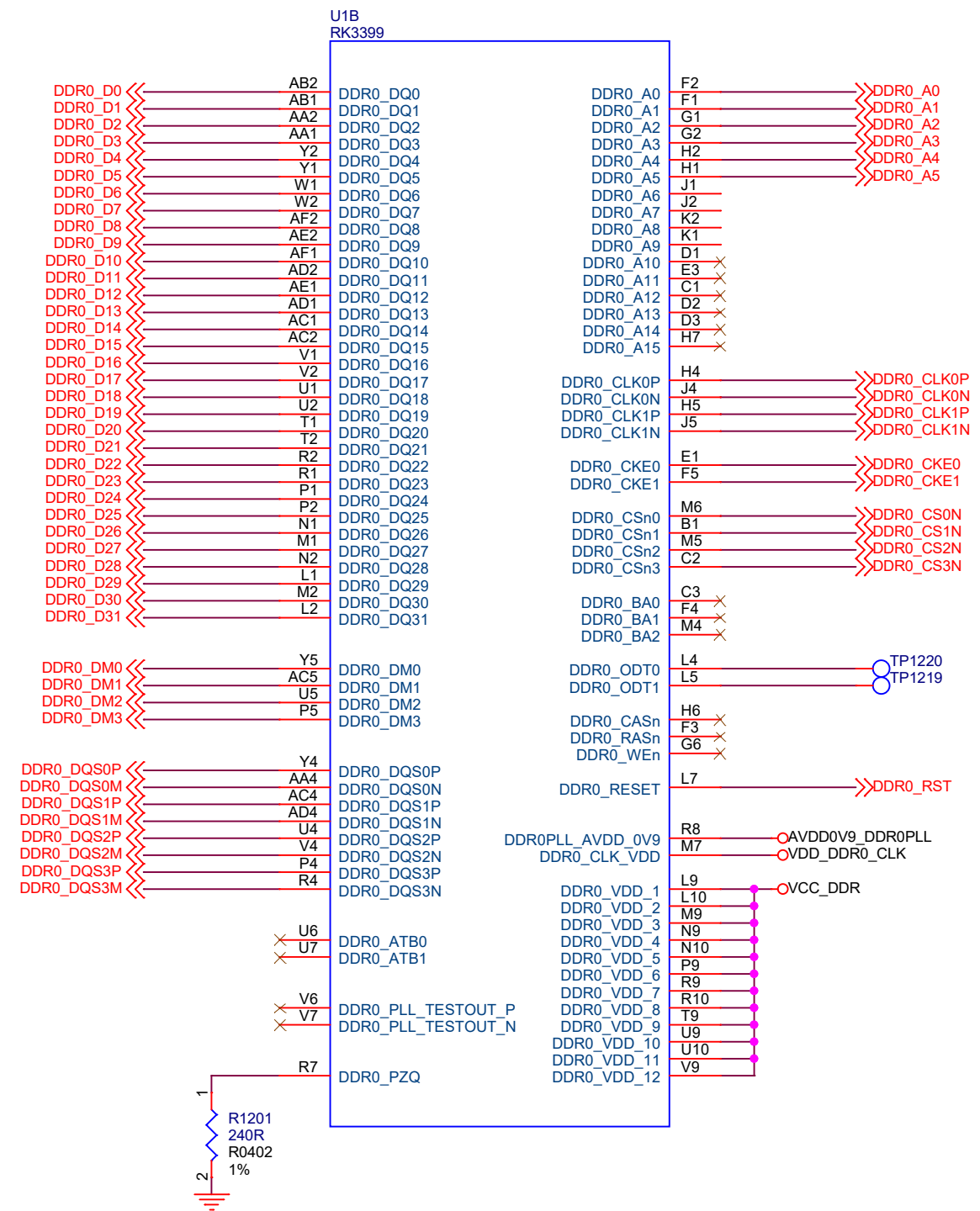
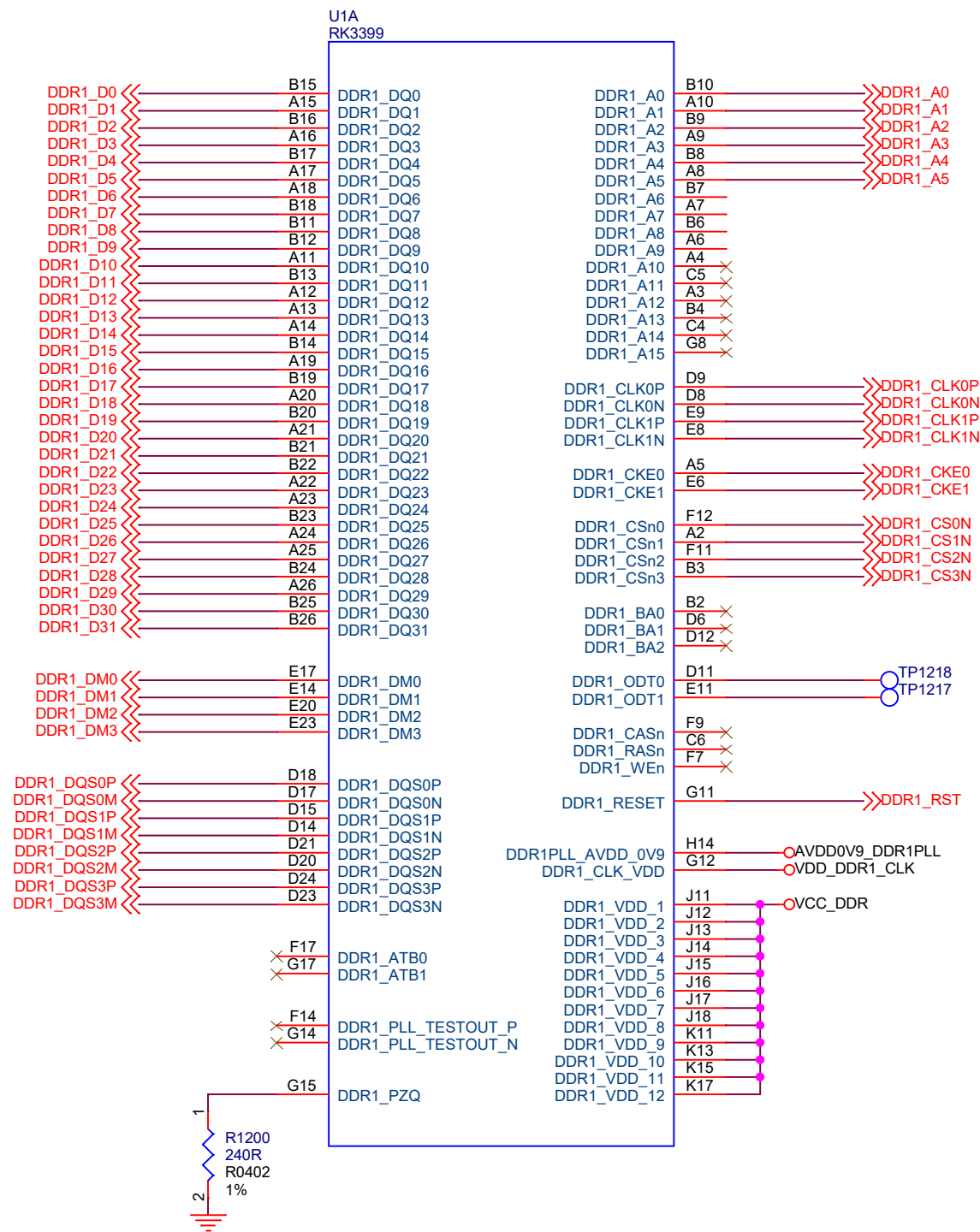




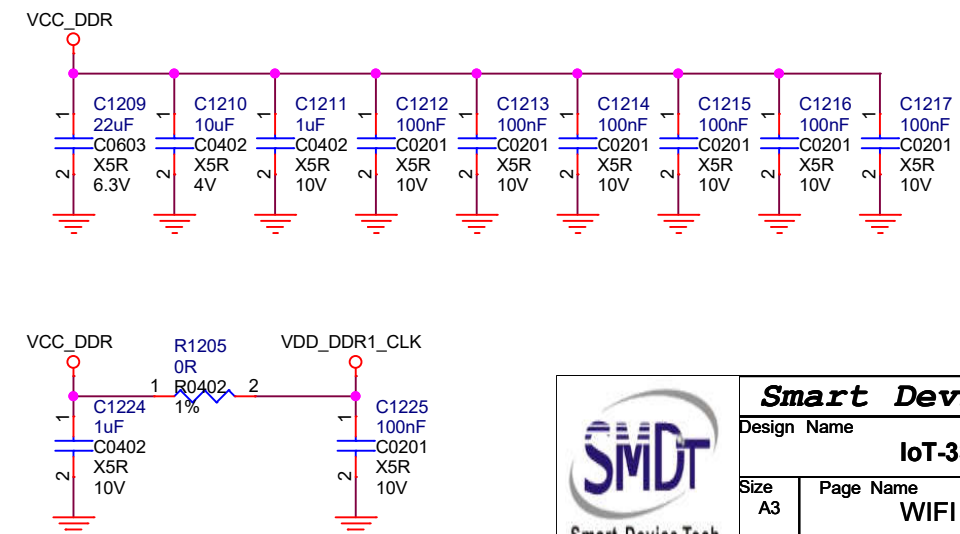
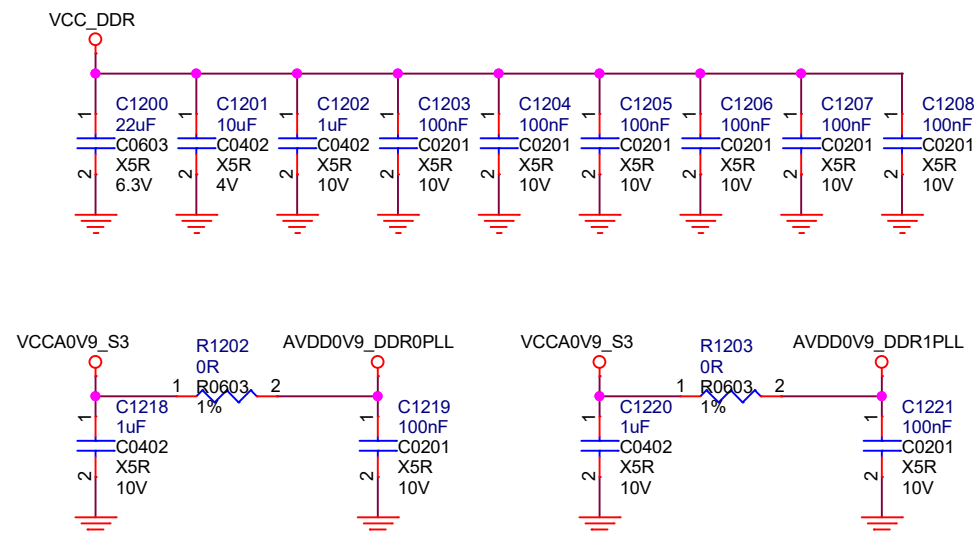








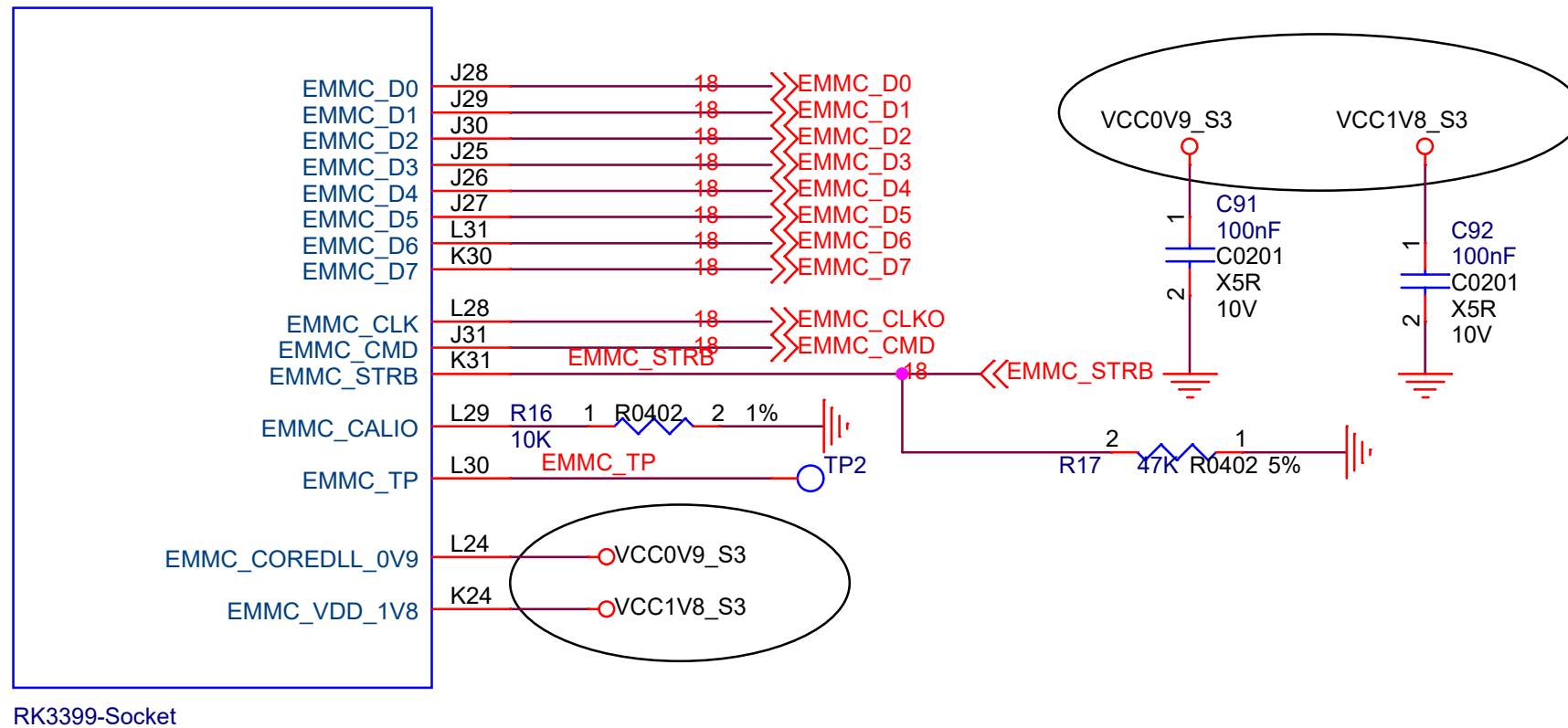
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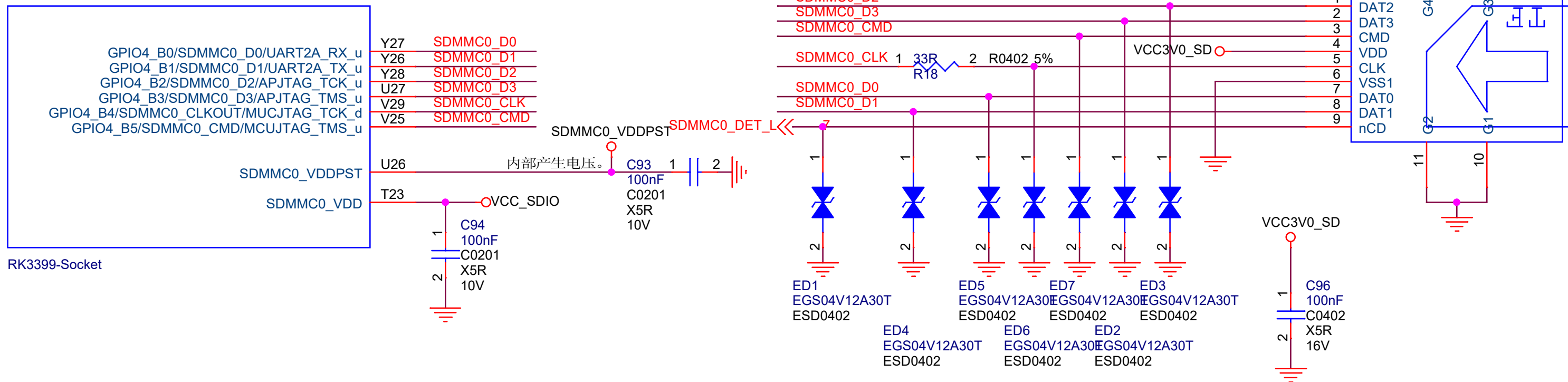
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U1H

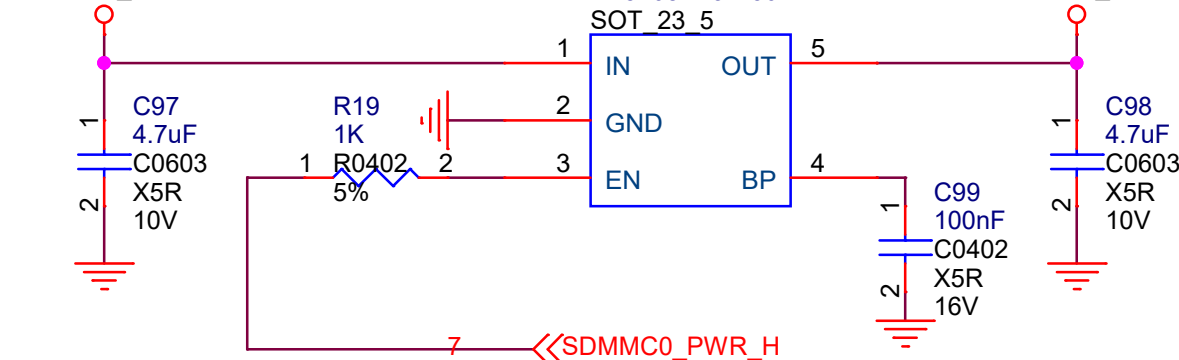


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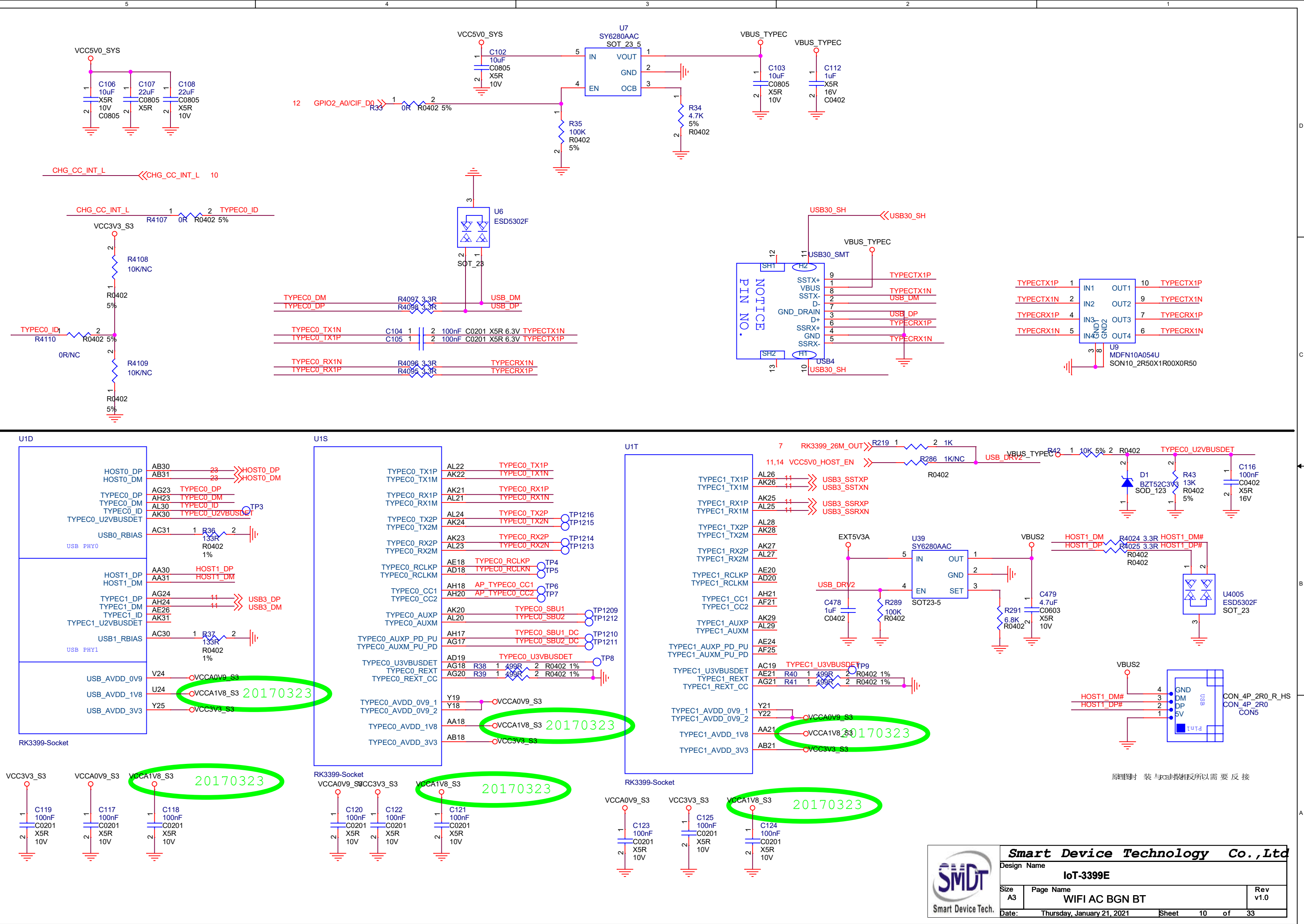
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TF-CKT01-009D  
J88

TF CARD

VCC3V3\_SYS

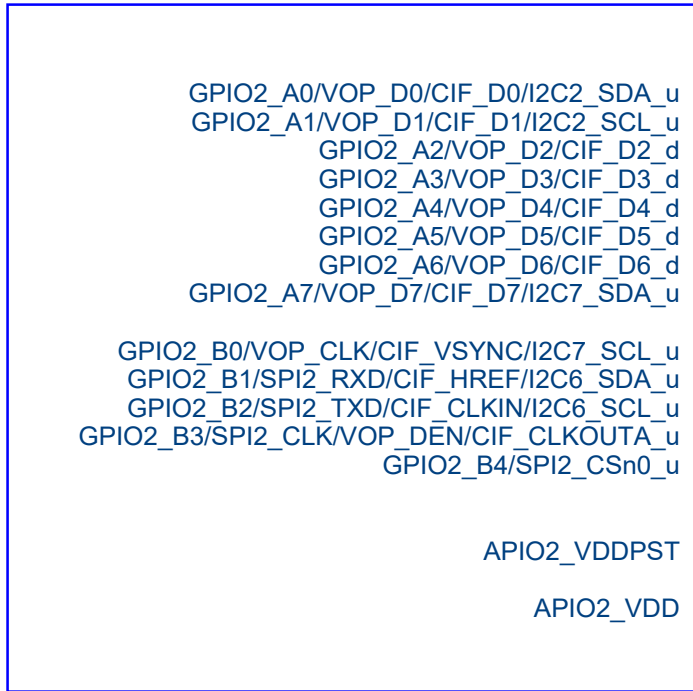


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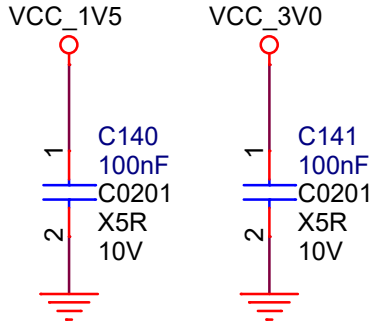
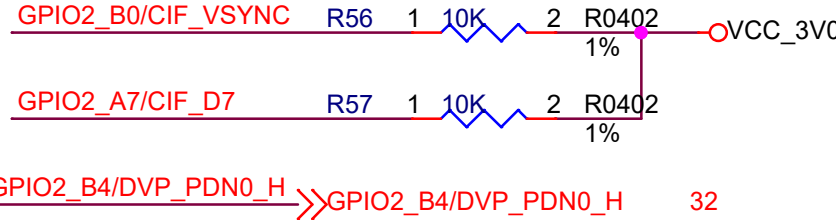
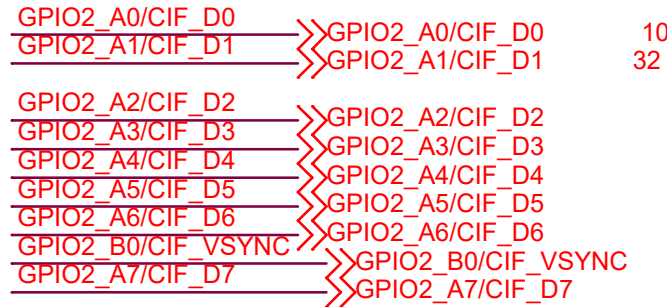
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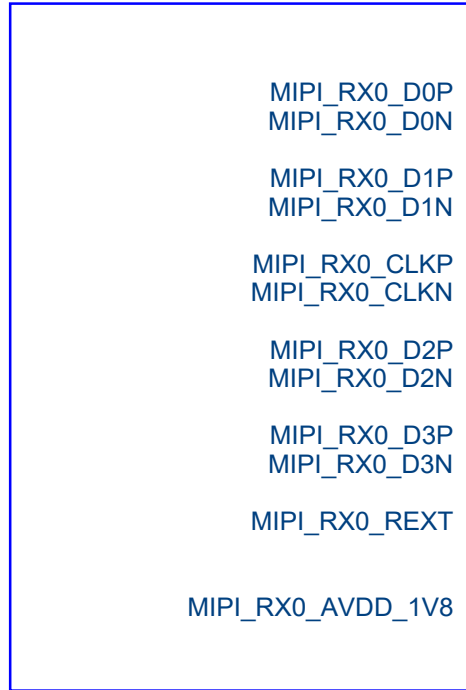
RK3399-Socket

func1	func2	func3
GPIO2_A0/CIF_D0		I2C2_SDA
GPIO2_A1/CIF_D1		I2C2_SCL
GPIO2_A7/CIF_D7		I2C7_SDA
GPIO2_B0/CIF_VSYNC		I2C7_SCL
GPIO2_B1/CIF_HREF	SPI2_RXD	I2C6_SDA
GPIO2_B2/CIF_CLKI	SPI2_TXD	I2C6_SCL
GPIO2_B3/CIF_CLKO	SPI2_CLK	
GPIO2_B4/DVP_PDN0_H	SPI2_CSN	

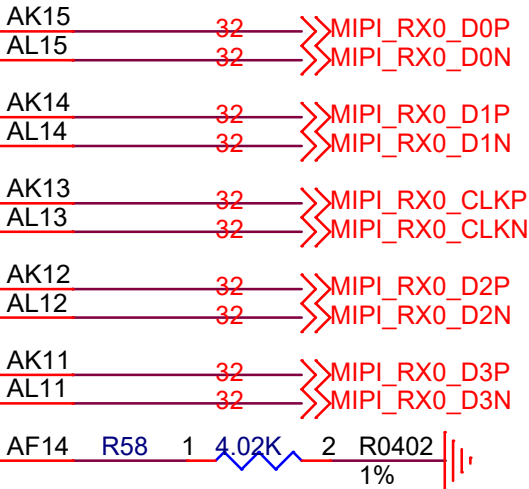
TYPEC PWREN  
MIPI CSI RST



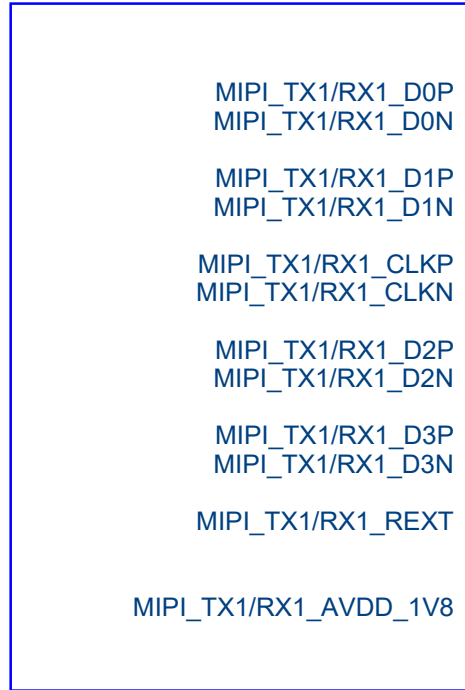
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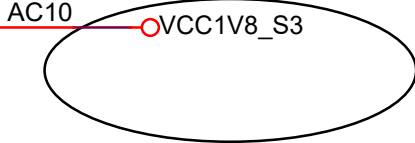
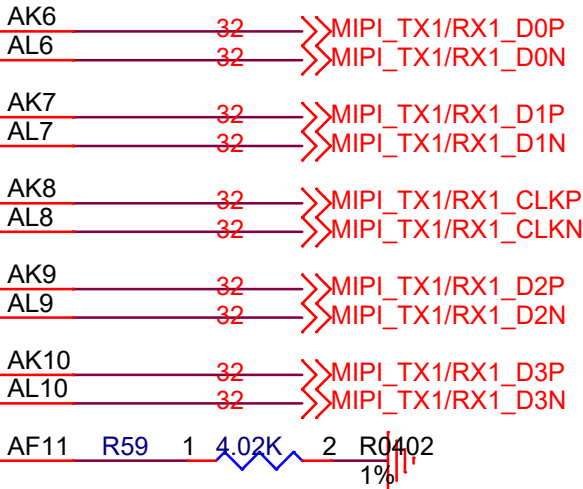
RK3399-Socket



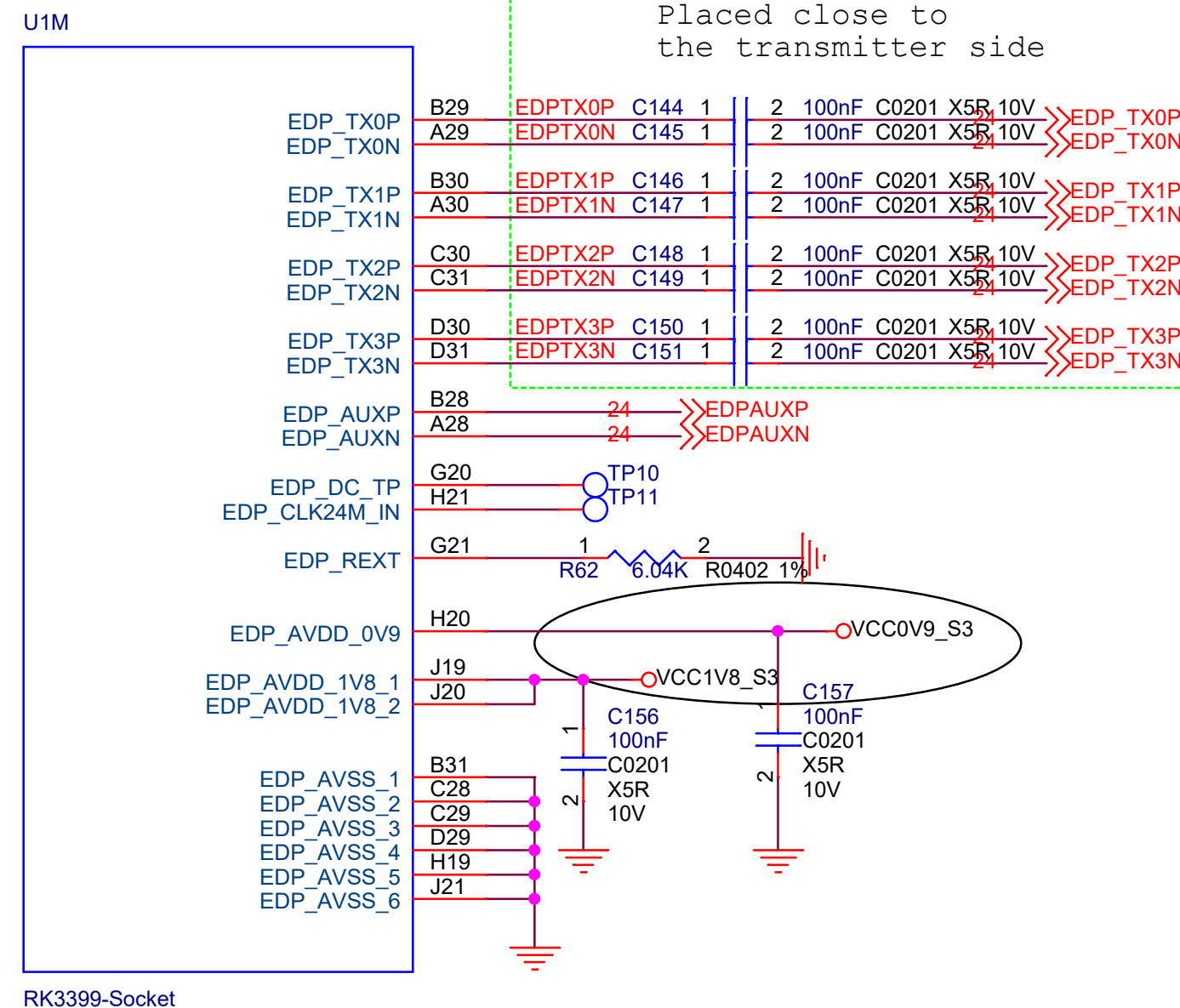
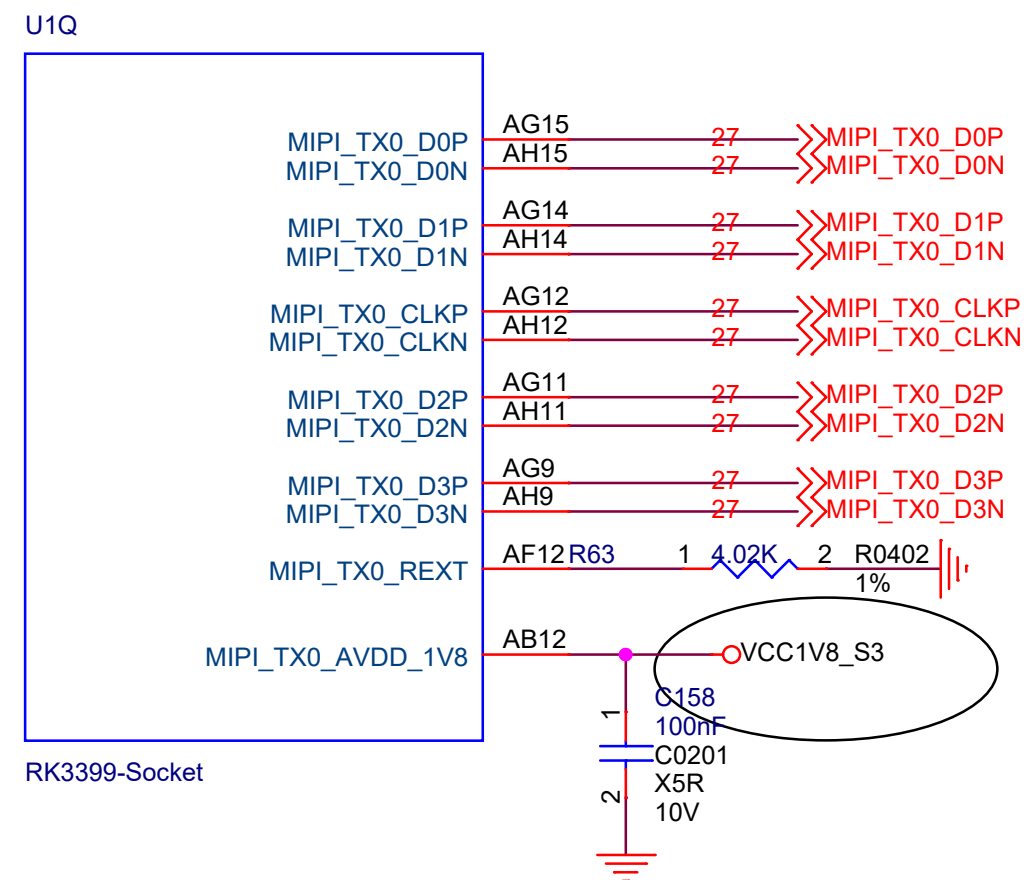
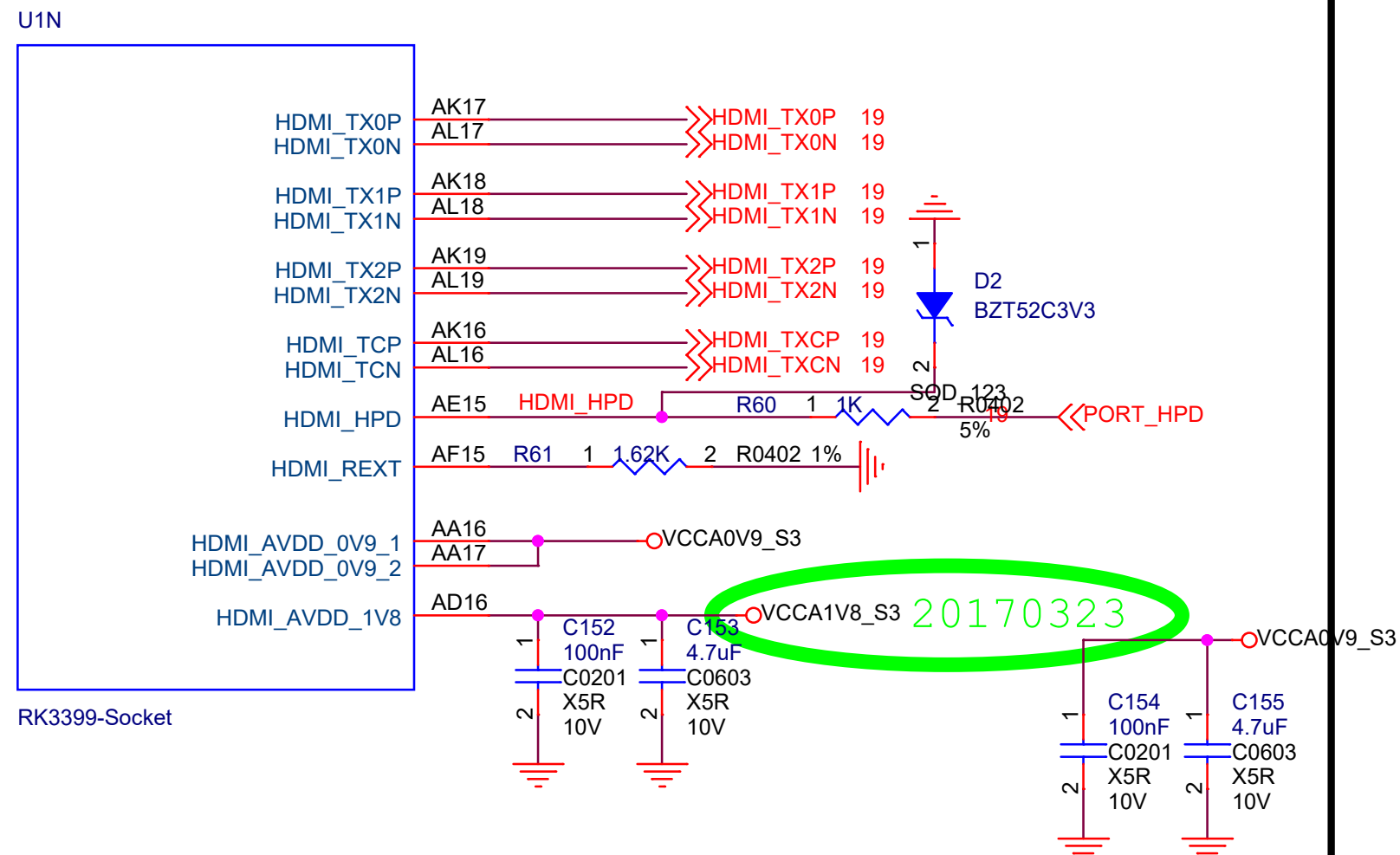
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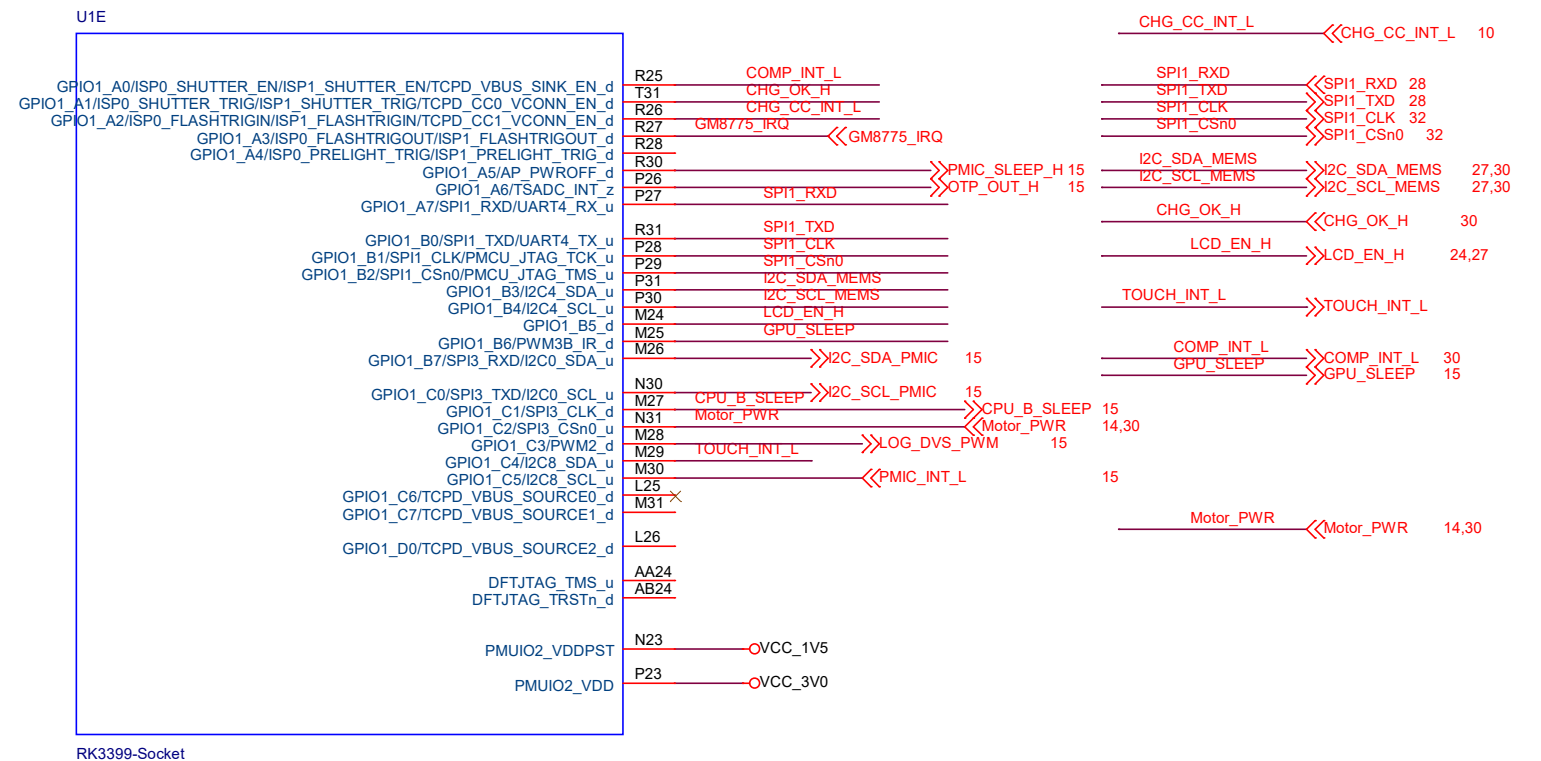
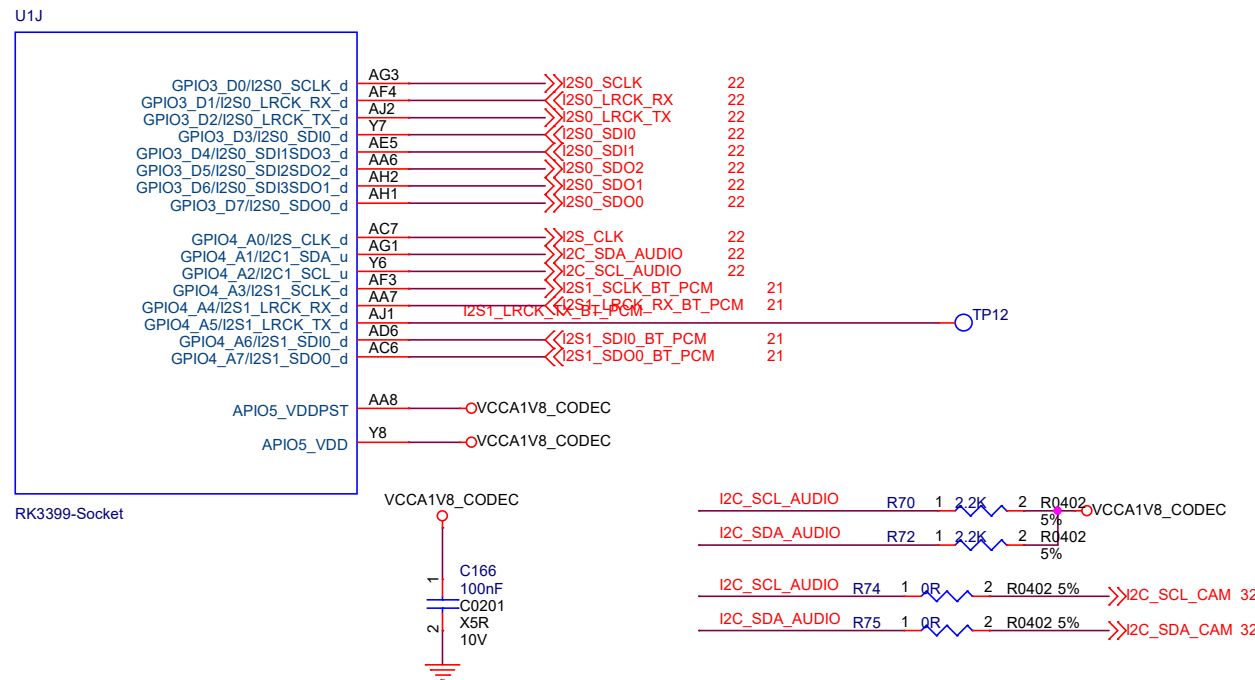
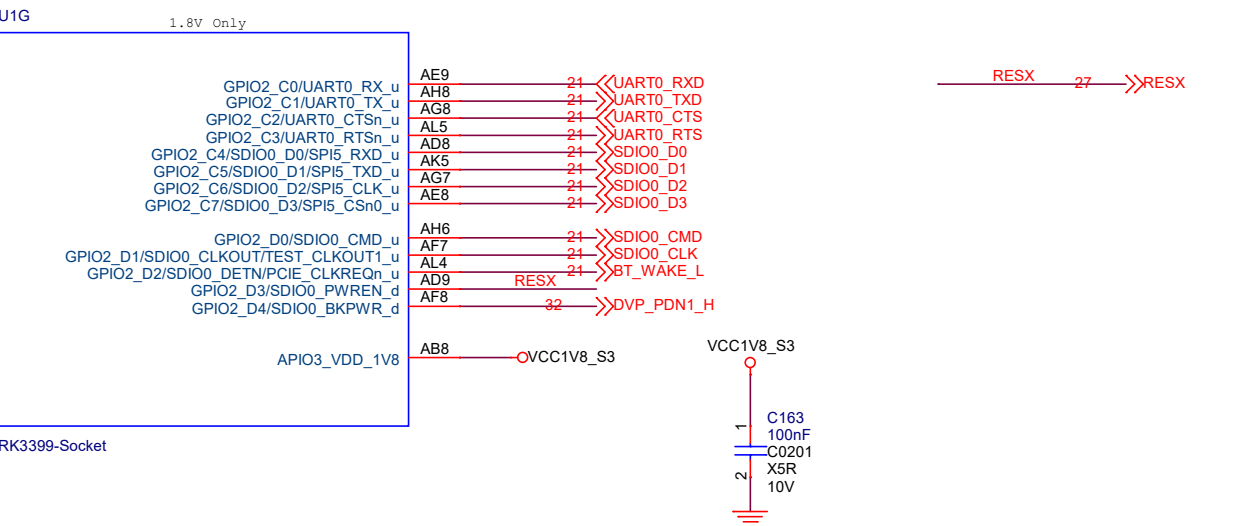
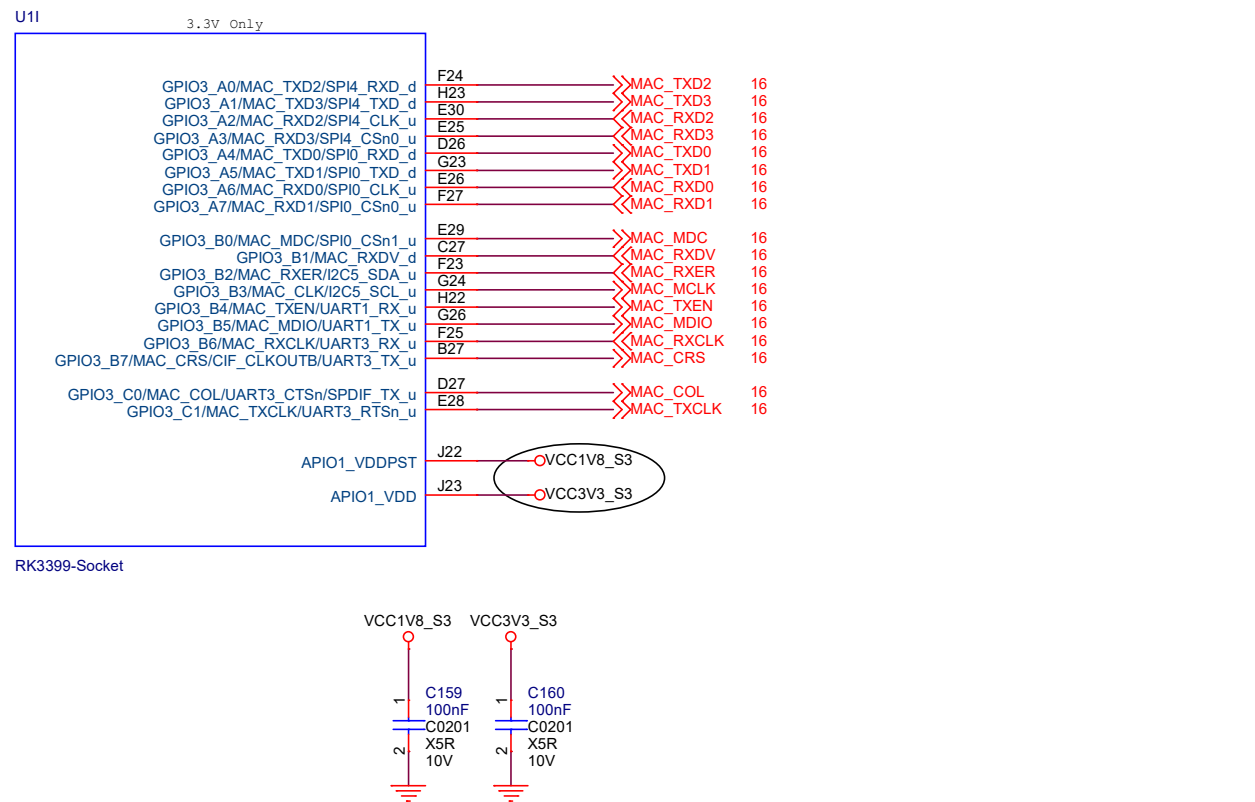
RK3399-Socket



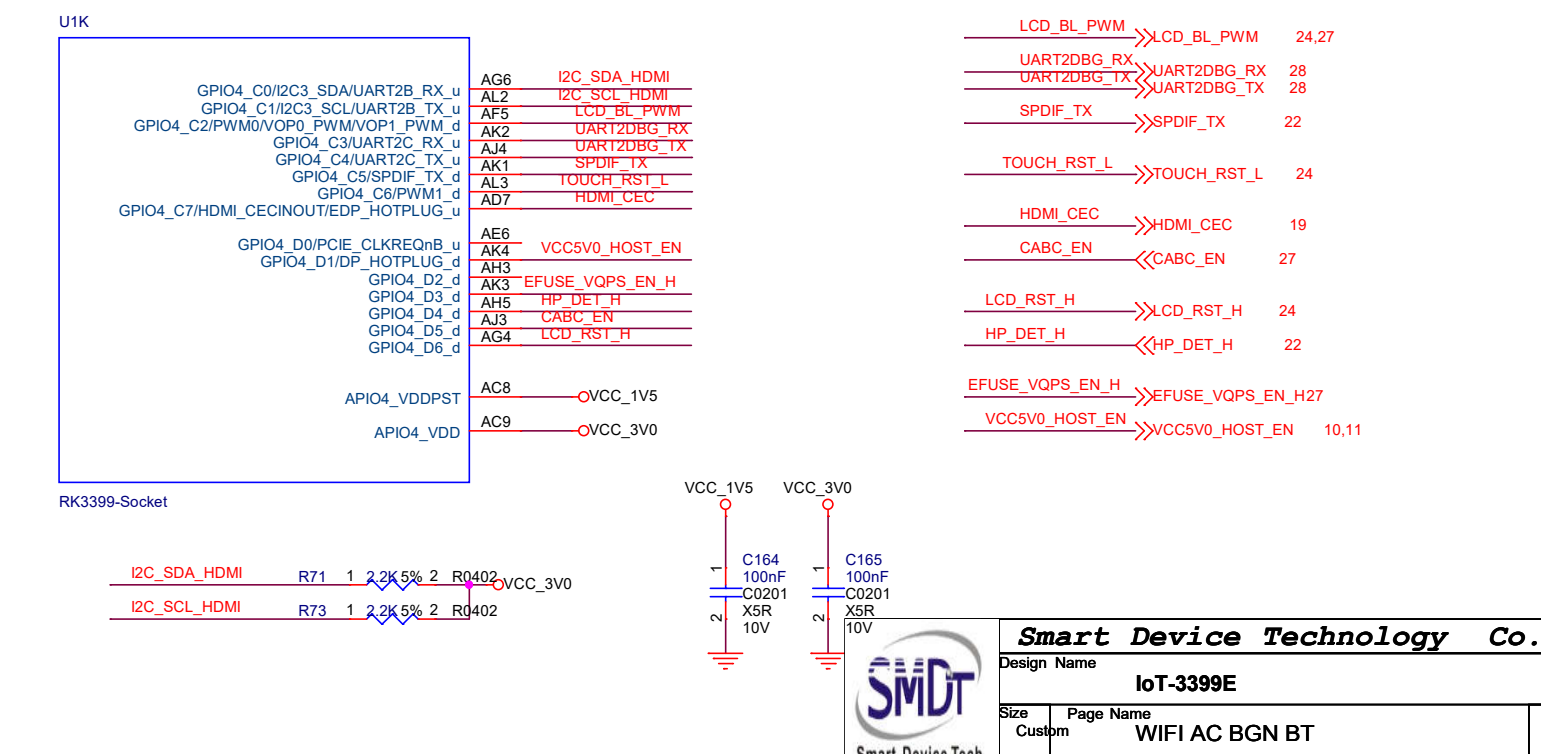




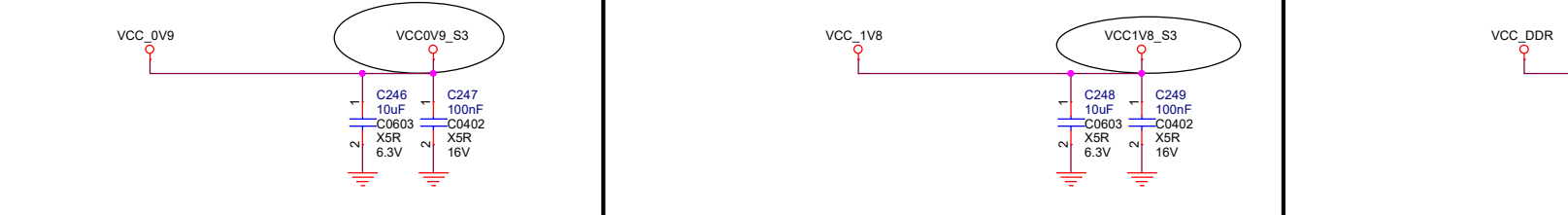
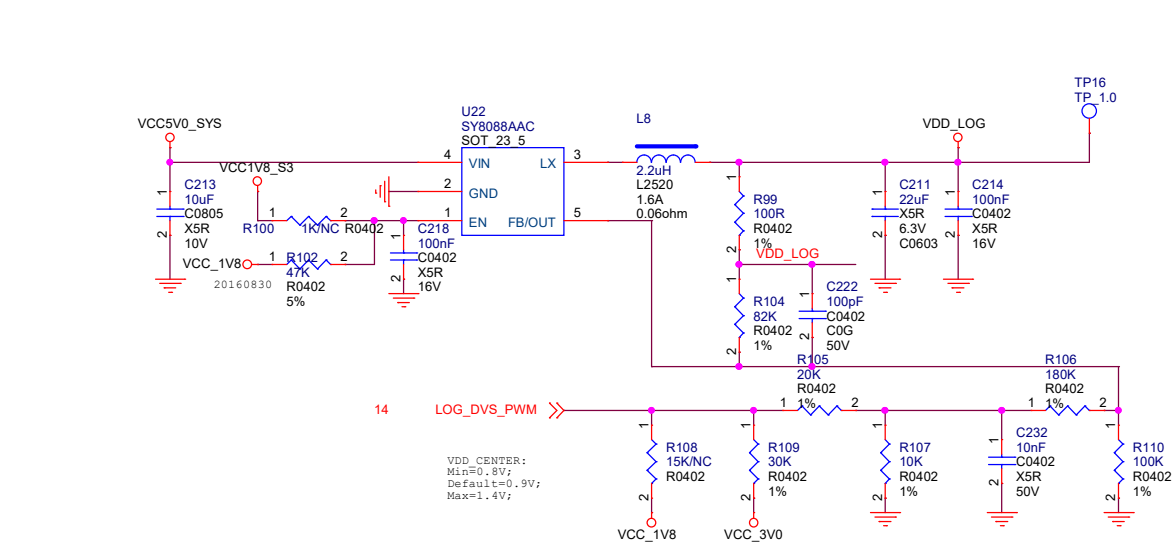
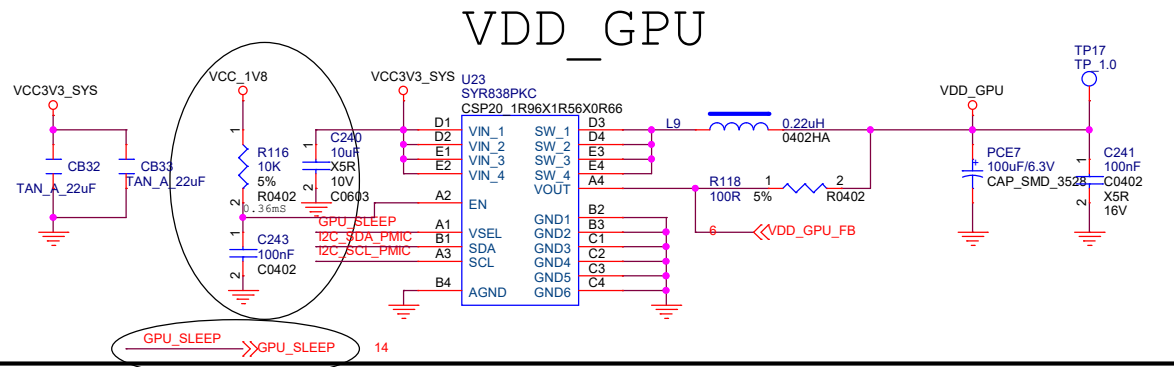
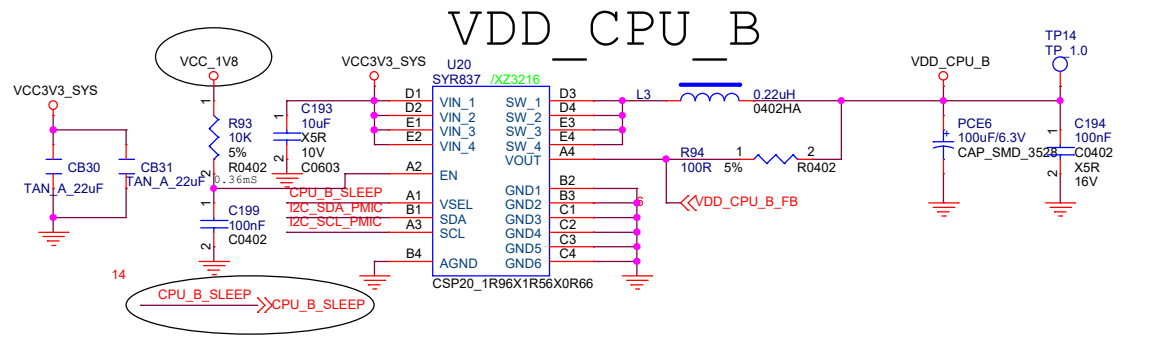
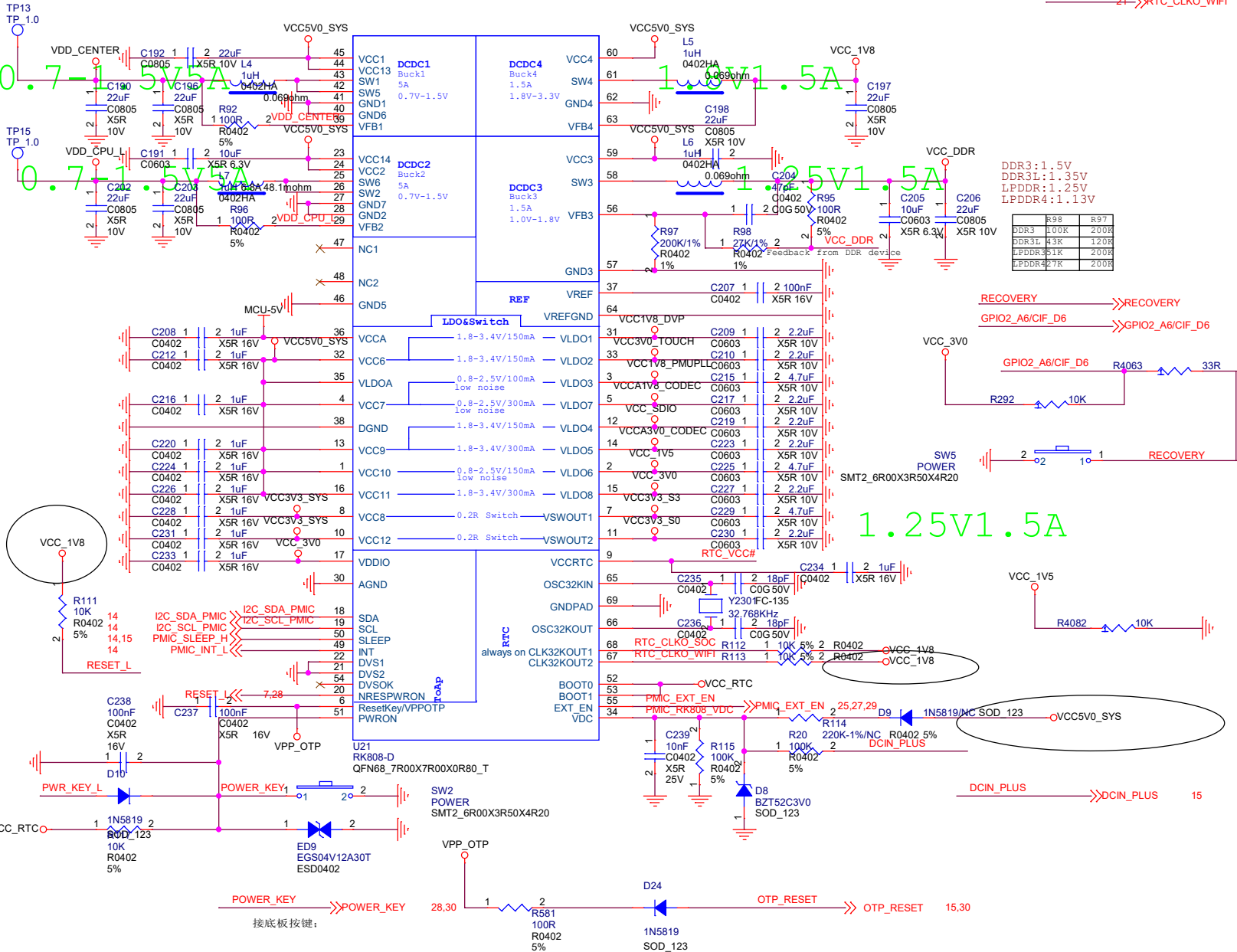
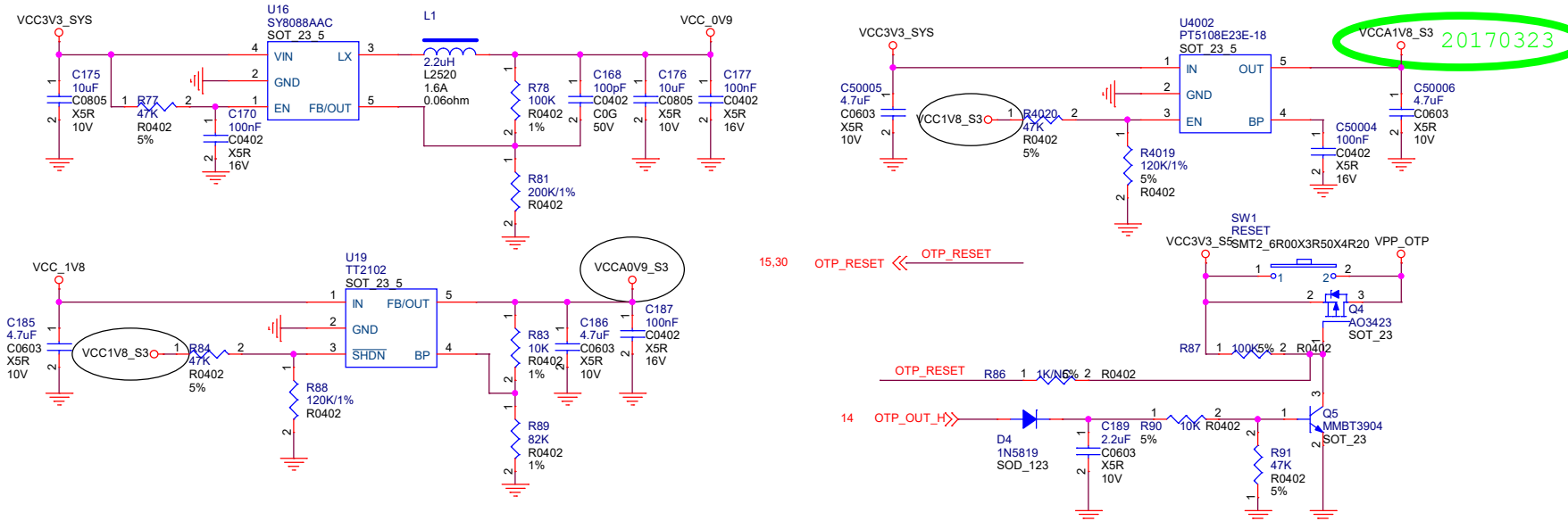
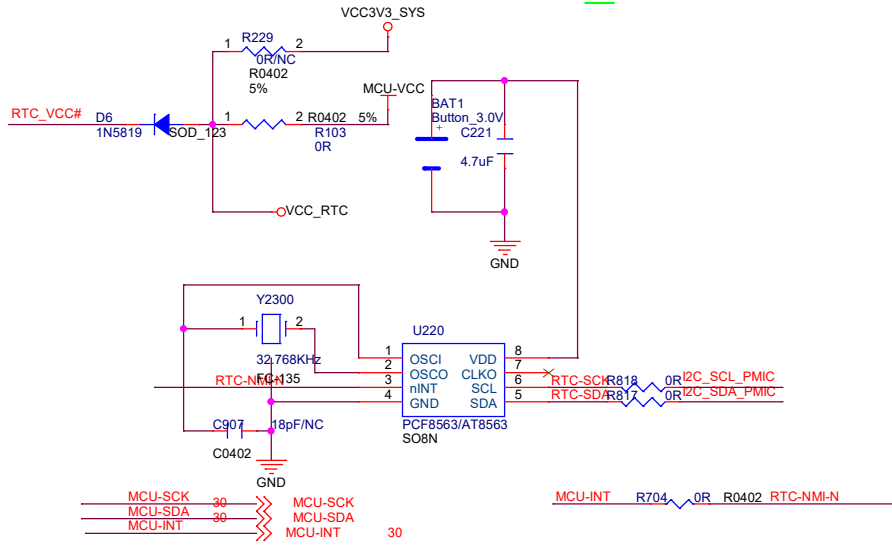
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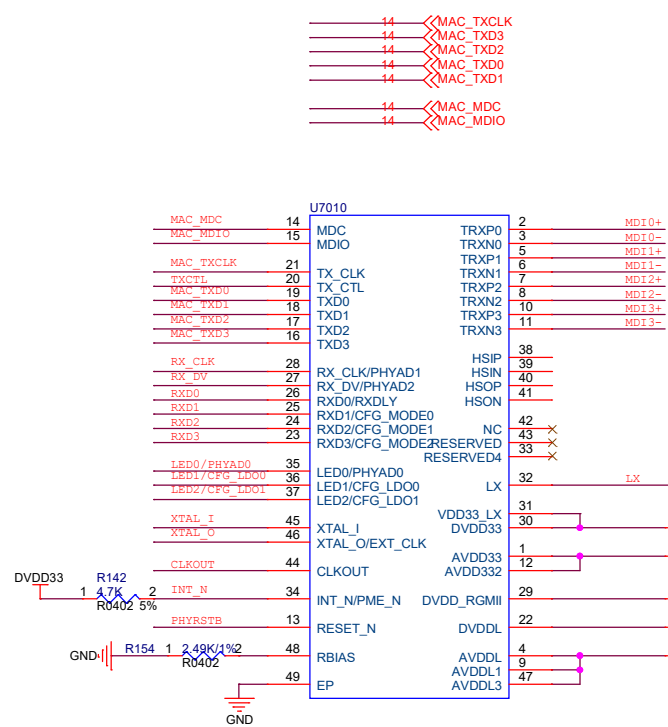
1.8V Only	VDDPST=VDDIO=1.8V
3.3V Only	VDDPST=1.8V, VDDIO=3.3V
other	3.0V mode:VDDPST=1.5V, VDDIO=3.0V 1.8V mode:VDDPST=1.8V, VDDIO=1.8V



# VLD02\_2.8V150MA

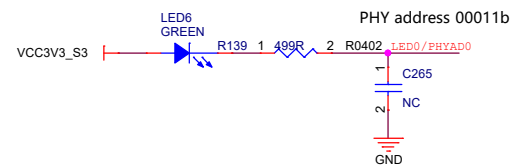






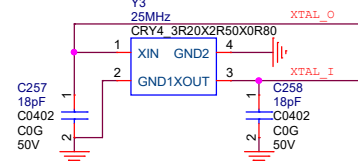
If RESET, INT, LED0, CFG\_LDO[1:0] need to be pulled up, they should be pulled up to DVDD33

If RXD[3:0], RX\_CLK, RX\_DV need to be pulled up, they should be pulled up to DVDD\_RGMII

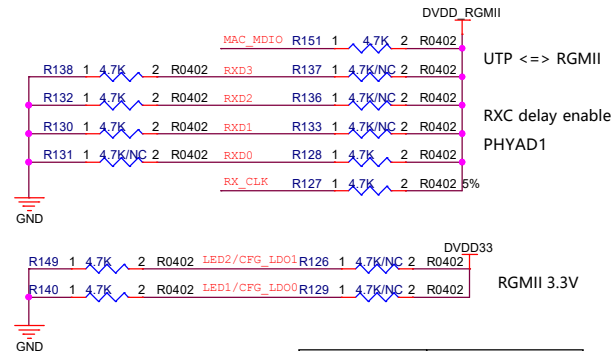
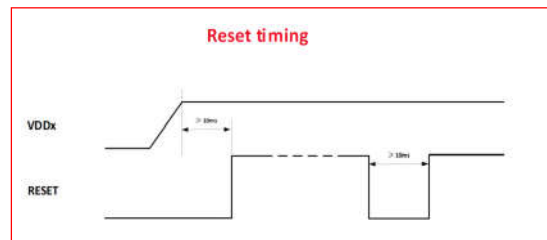


if the LED pin is pulled high, it will be an active low output. And if the LED pin is pulled low, it will be an active high output.

Calculate the value of external load capacitor according to the crystal load capacitance

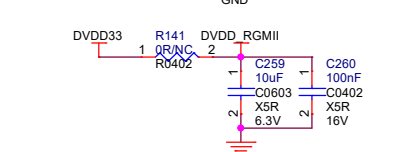
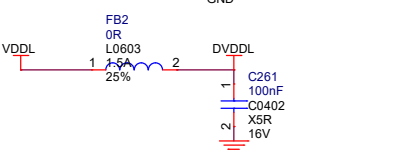
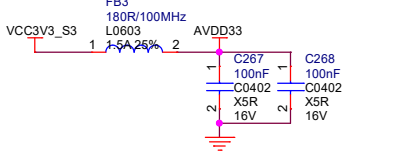
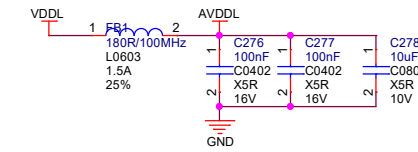
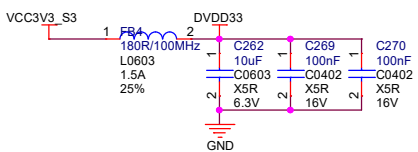


ensure the reset timing is matched

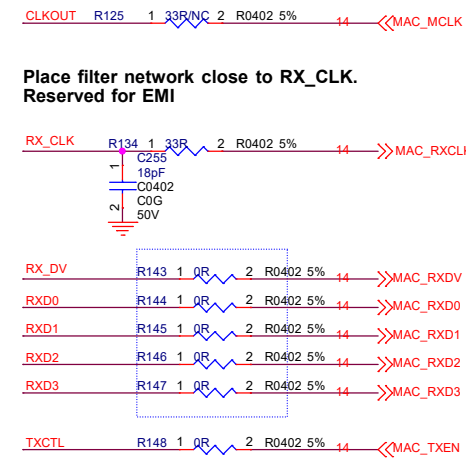


RXD[3:1]	Operation Mode
3'b000	UTP <=> RGMII
3'b001	FIBER <=> RGMII
3'b010	UTP/FIBER <=> RGMII
3'b011	UTP <=> SGMII
3'b100	SGMII (PHY) <=> RGMII
3'b101	SGMII (MAC) <=> RGMII
3'b110	UTP <=> FIBER (AUTO)
3'b111	UTP <=> FIBER (FORCE)

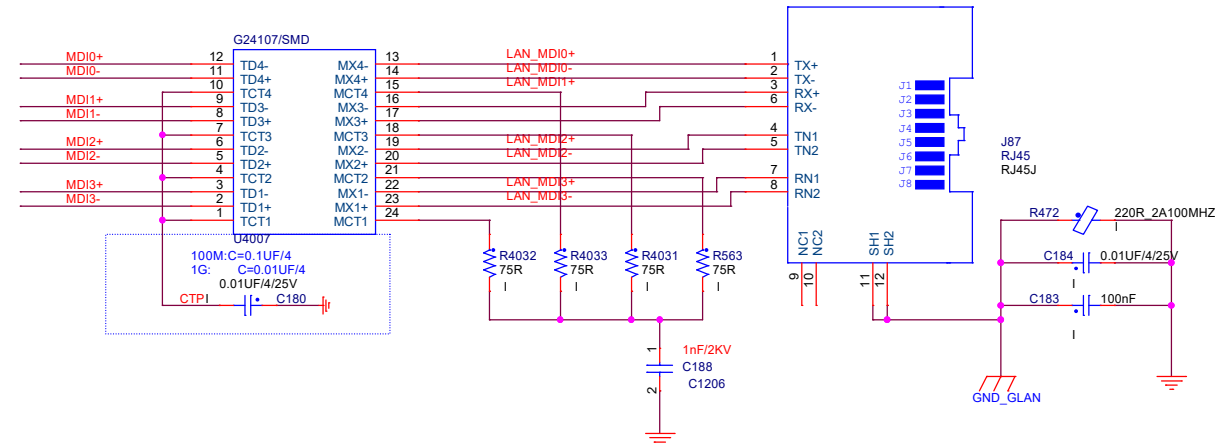
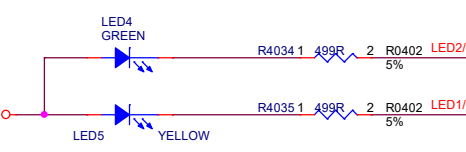
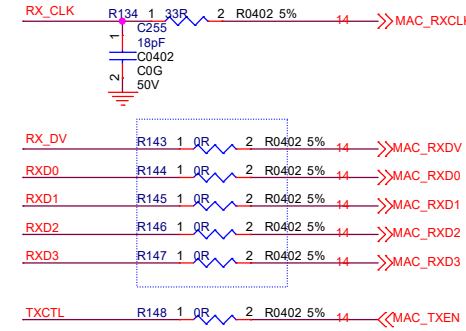
CFG_LDO[1:0]	RGMII Voltage Selection
2'b00	External 3.3V
2'b01	Internal 2.5V
2'b10	Internal 1.8V
2'b11	Not Available

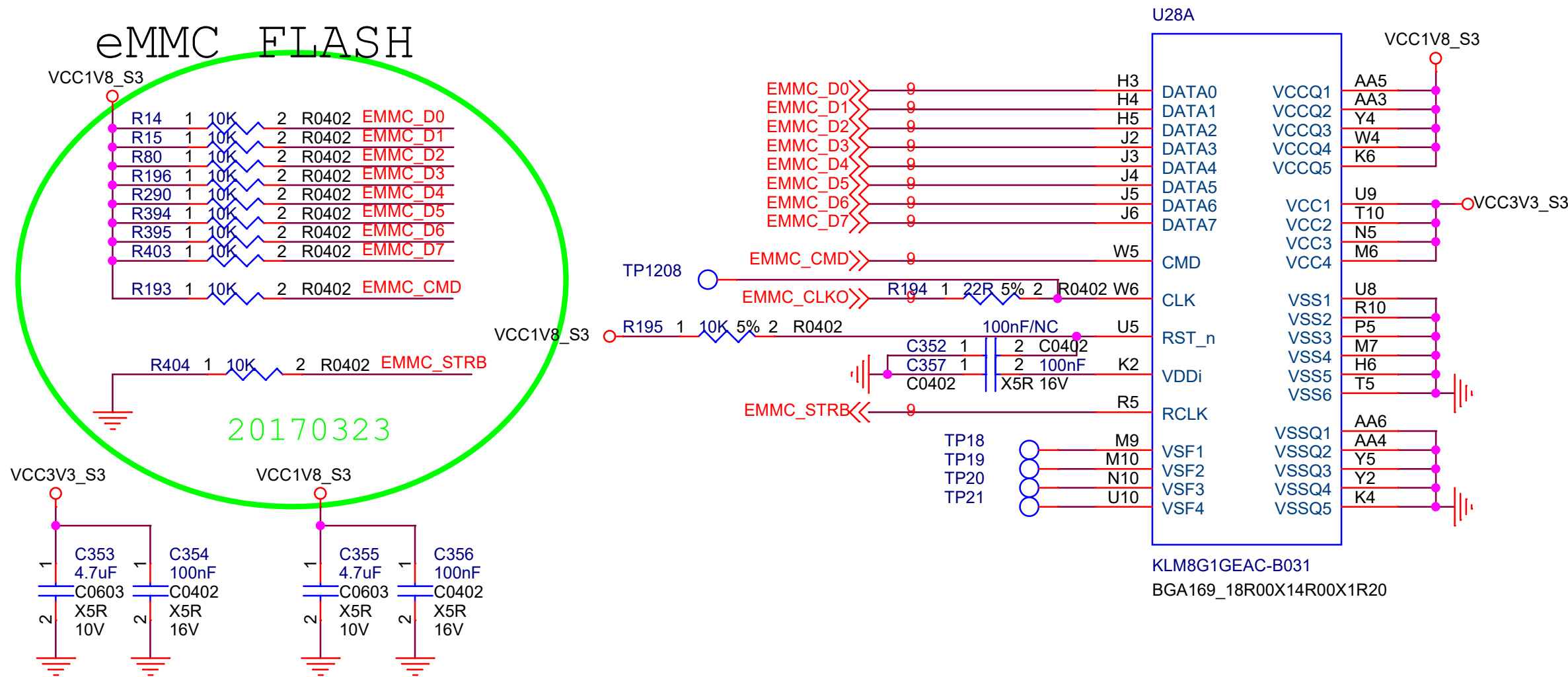


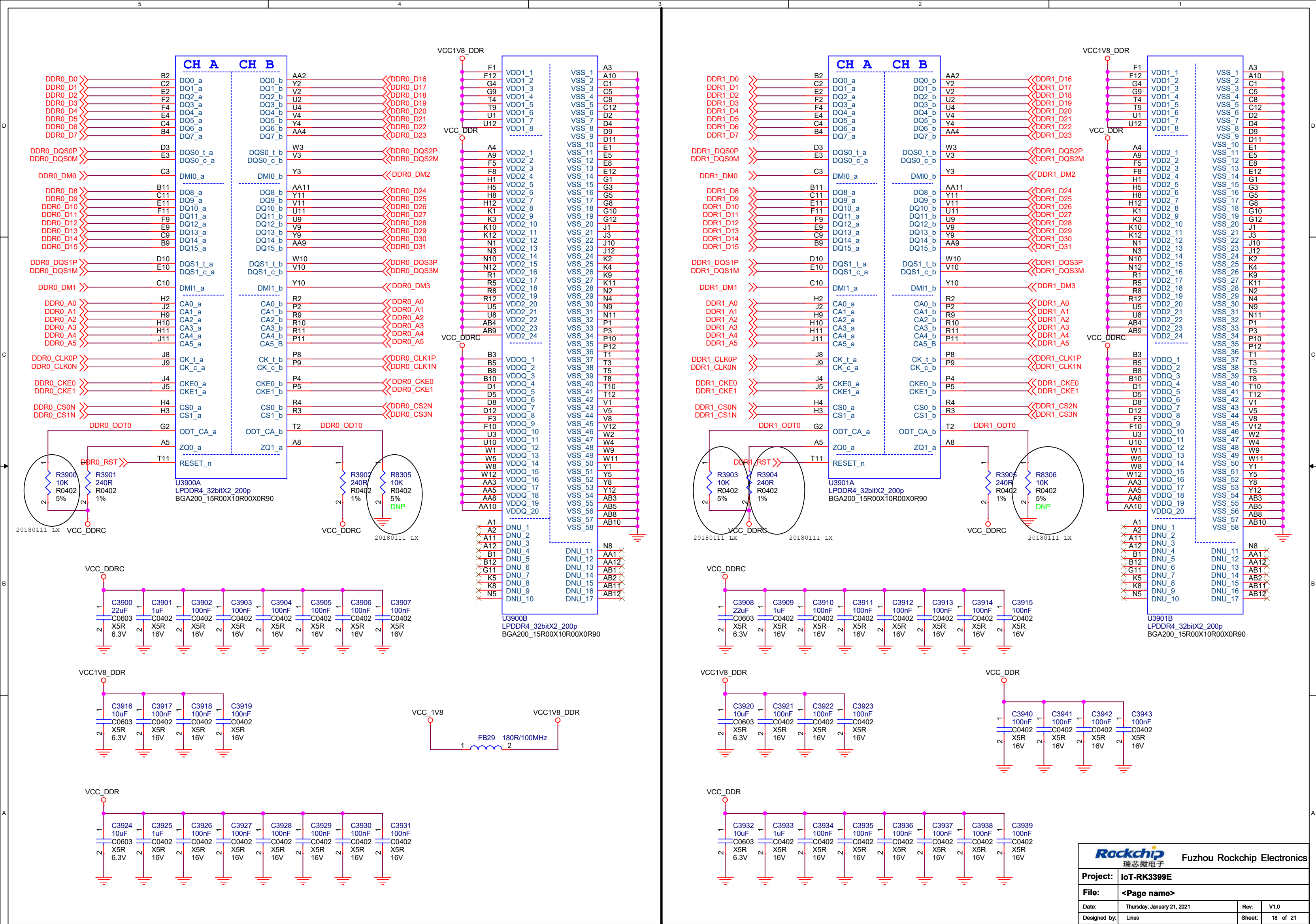
R141 is only for 3.3V RGMII application. R141 must be removed for 2.5V/1.8V RGMII application.



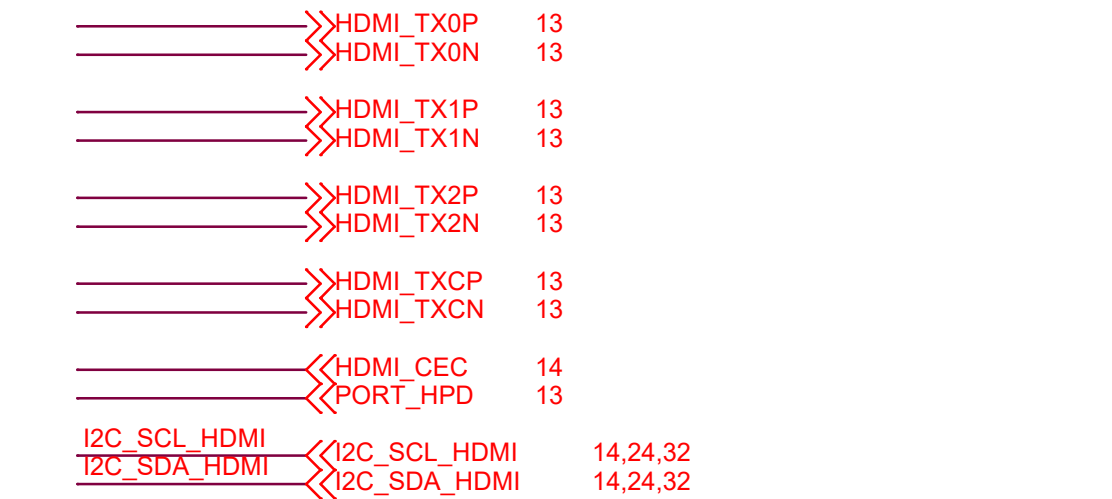
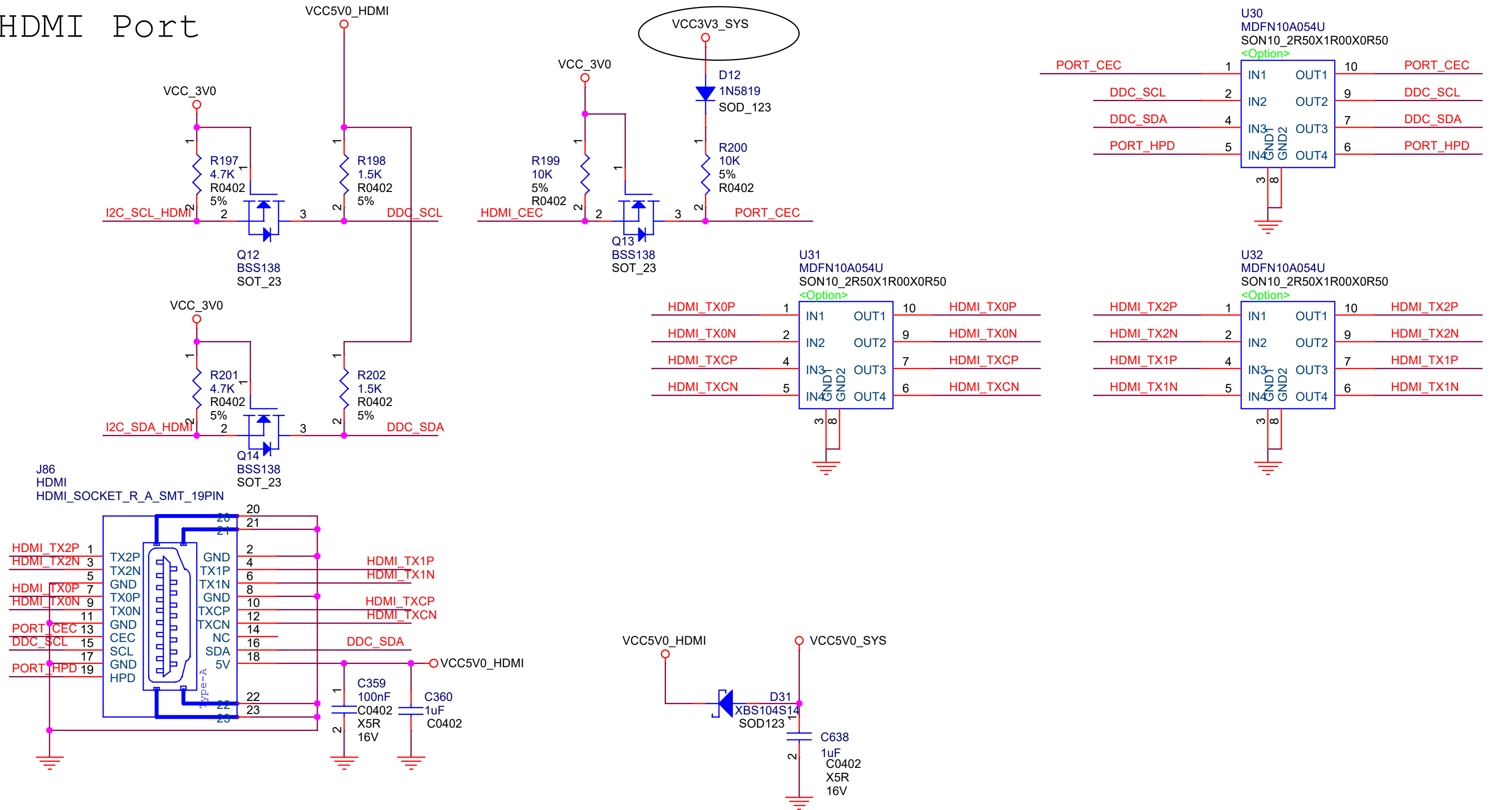
Place filter network close to RX\_CLK. Reserved for EMI








HDMI Port





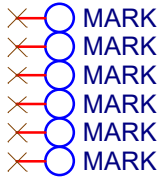
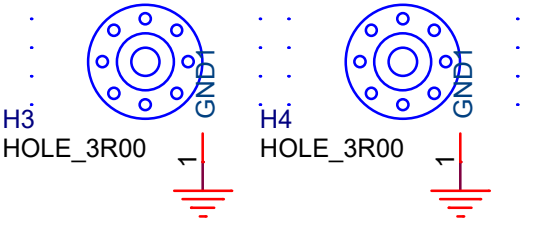
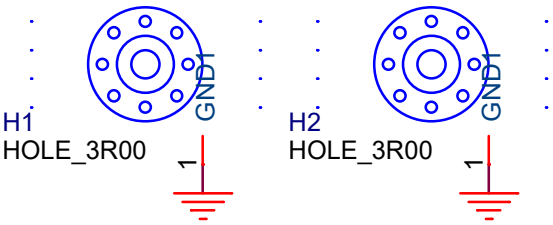
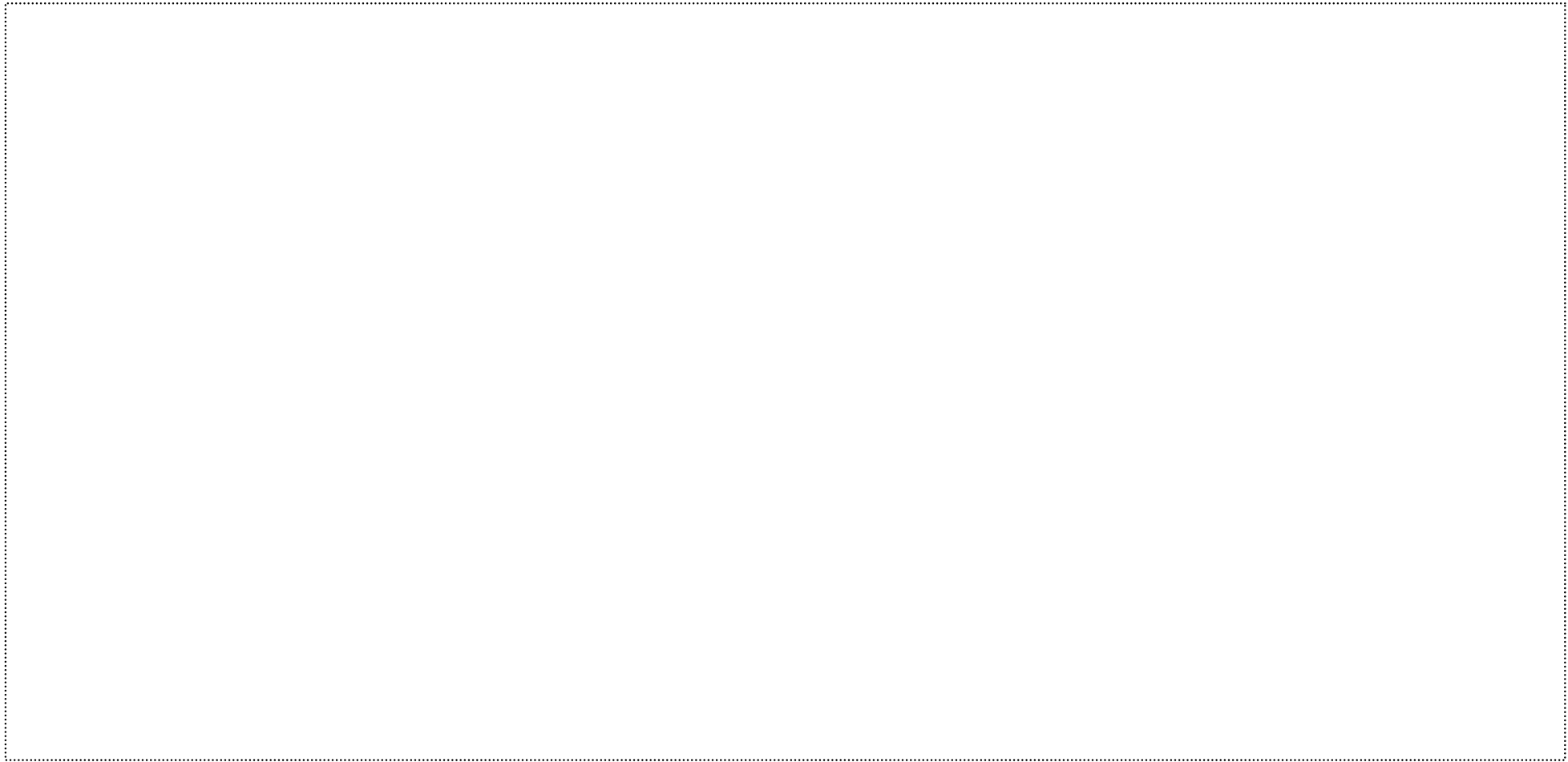
Smart Device Tech.

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Design Name  
**IoT-3399E**

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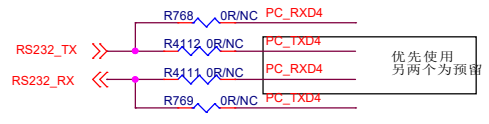
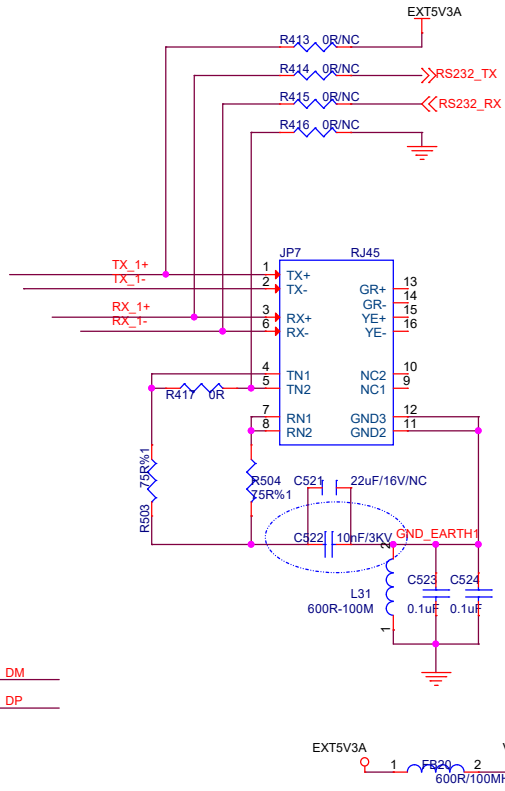
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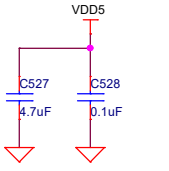
PAGE28 4CH\*UART  
PAGE29 MINI PCI-E  
PAGE30 MCU  
PAGE31 DCIN DCDC8.6V,DCDC5V DCDC3.3V  
PAGE32 PCI-E (M2) (V00 NO OPTION)

			<b>Smart Device Technology Co.,Ltd</b>		
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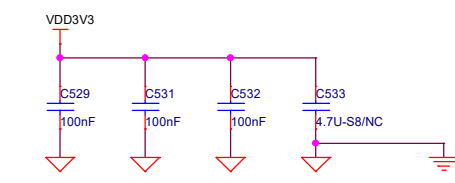
RJ45



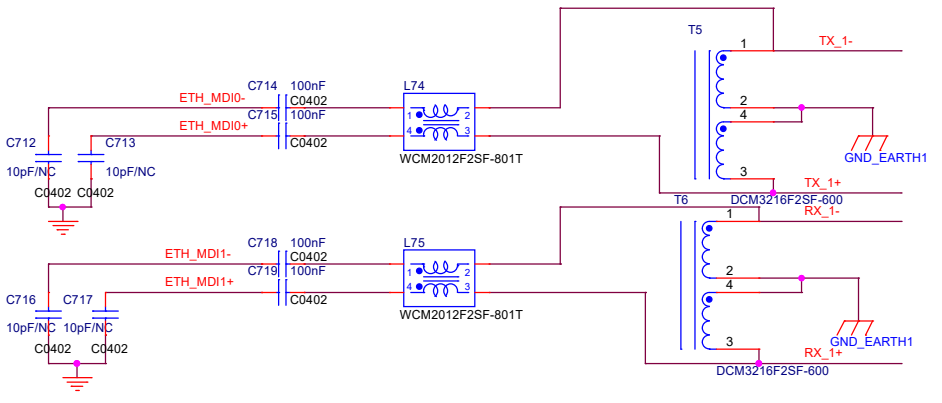
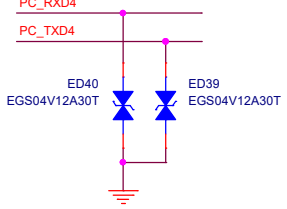
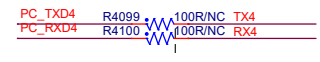
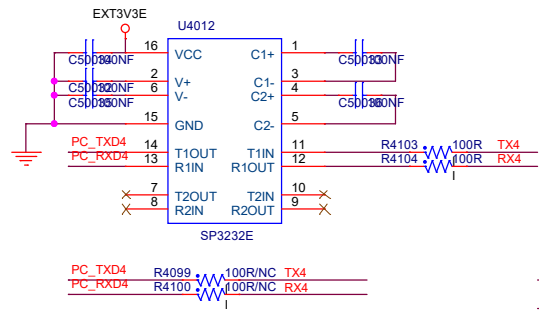
优先使用  
另两个为预留



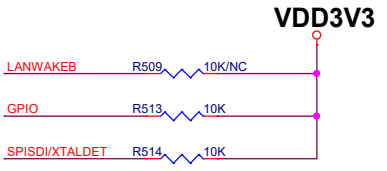
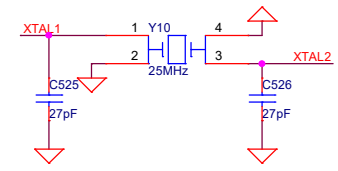
Place C102 and C144 close to VDD5 pin-- 11



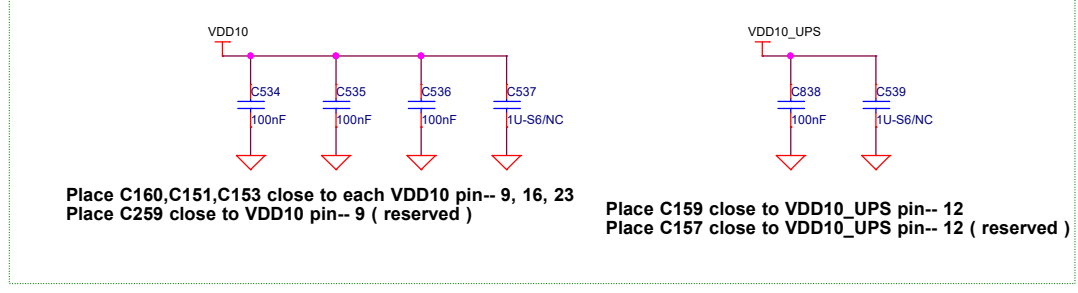
Place C147,C146,C145 close to each VDD33 pin-- 1, 10, 13  
Place C150 close to VDD33 pin-- 10 ( reserved )



网络变压器



pin15/pin17 LED 编程有效  
LED0:网络 连接 指示灯  
LED1:数据 传输 指示灯

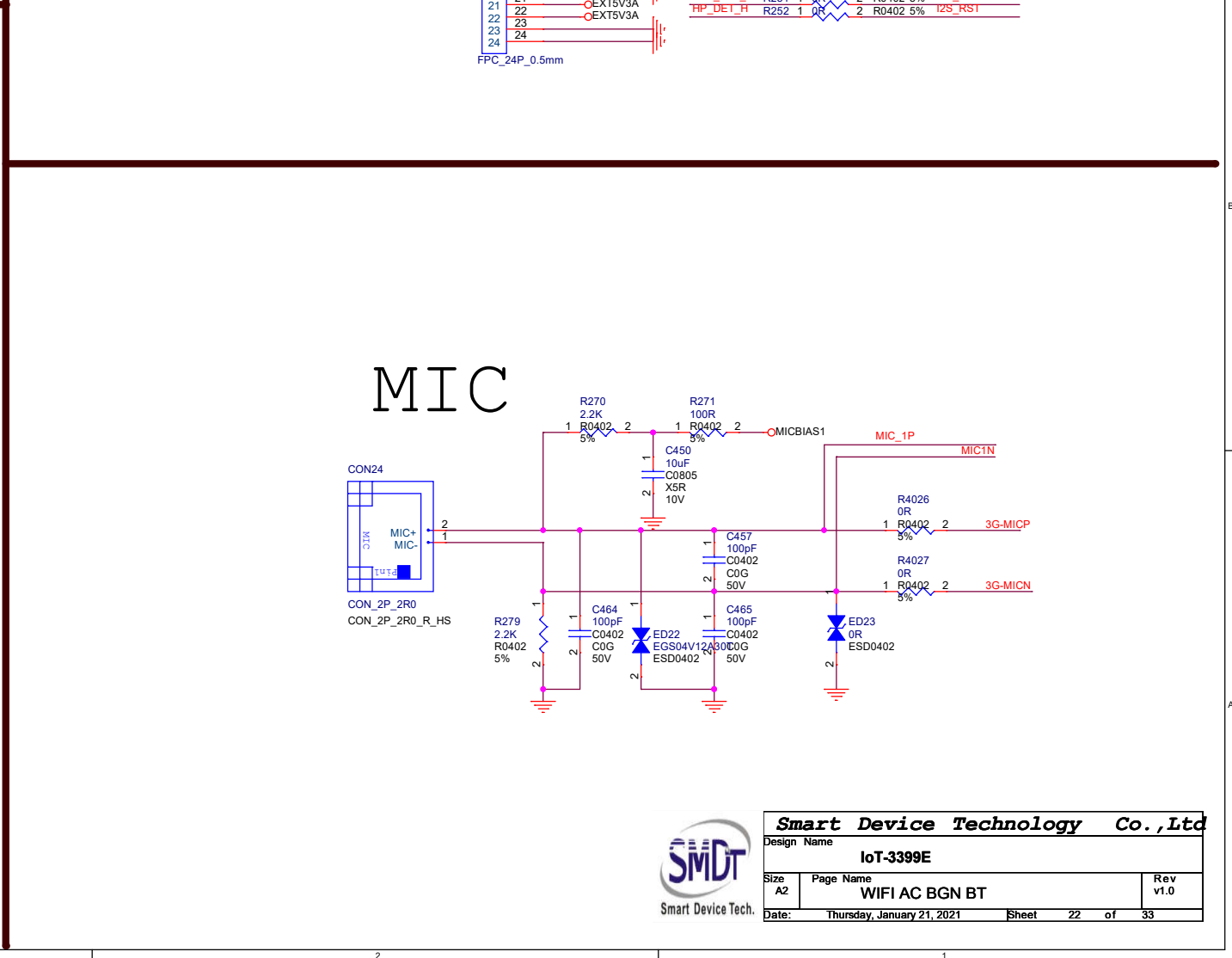
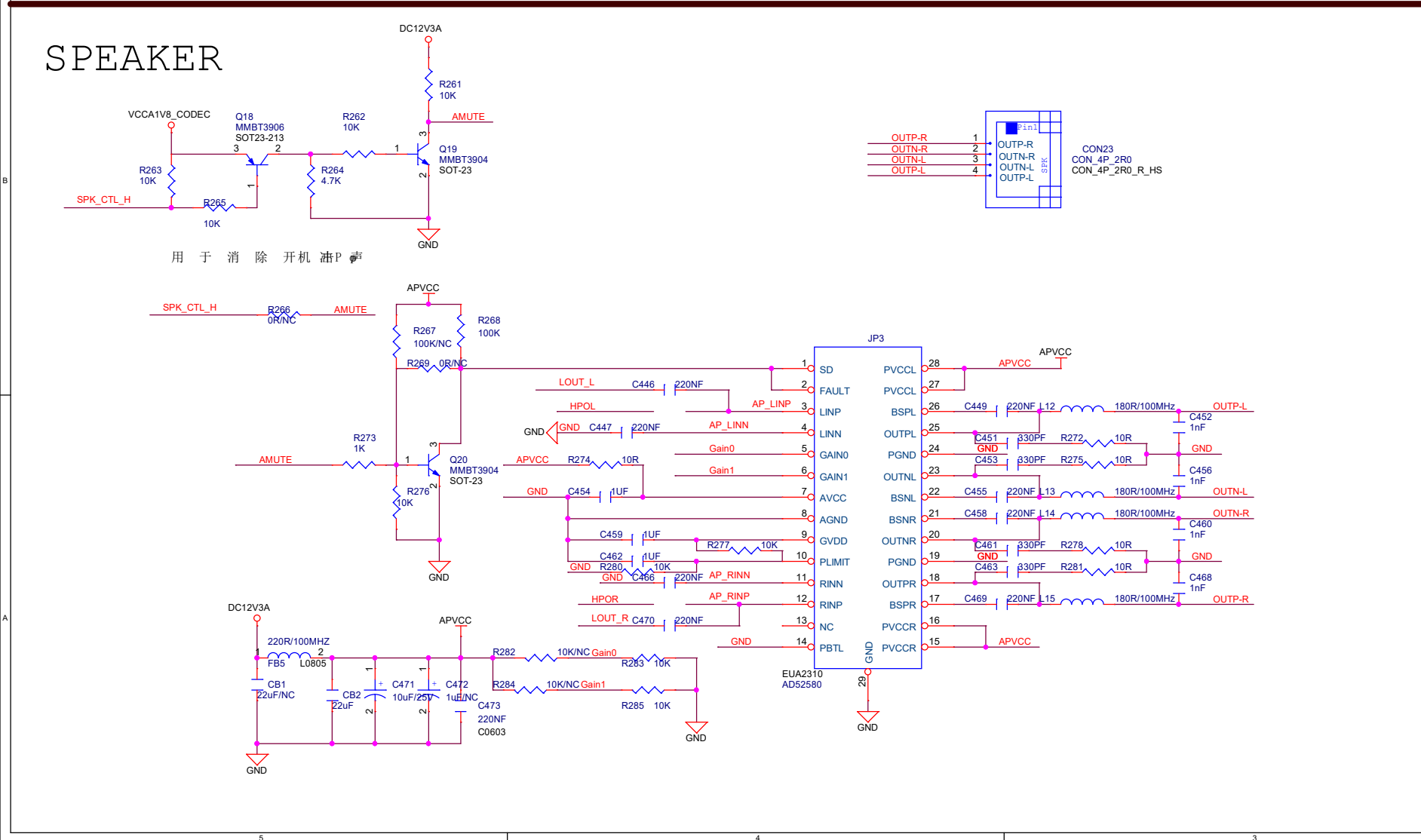


Place C160,C151,C153 close to each VDD10 pin-- 9, 16, 23  
Place C259 close to VDD10 pin-- 9 ( reserved )

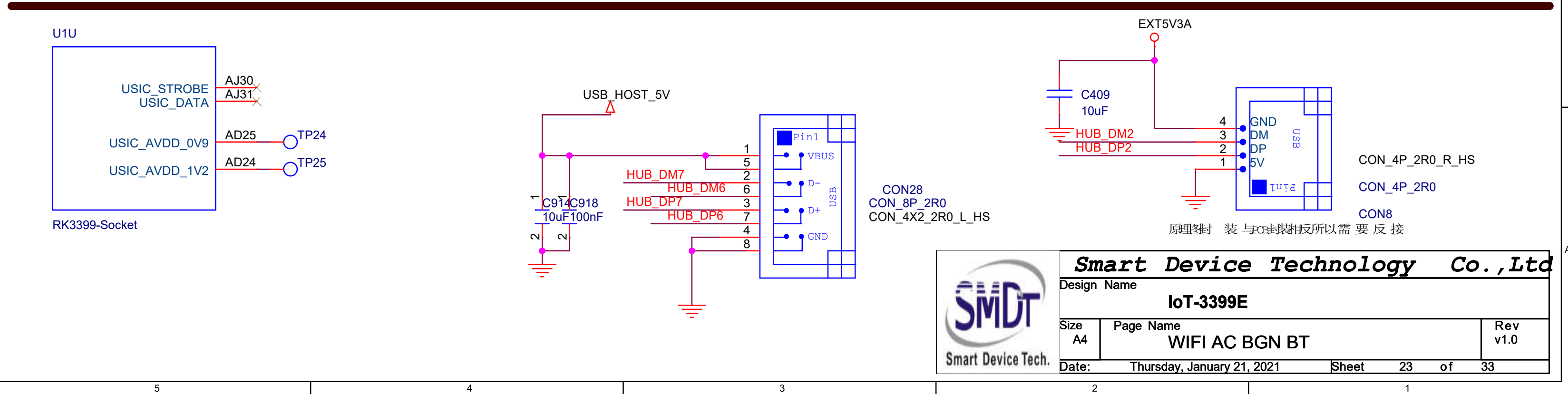
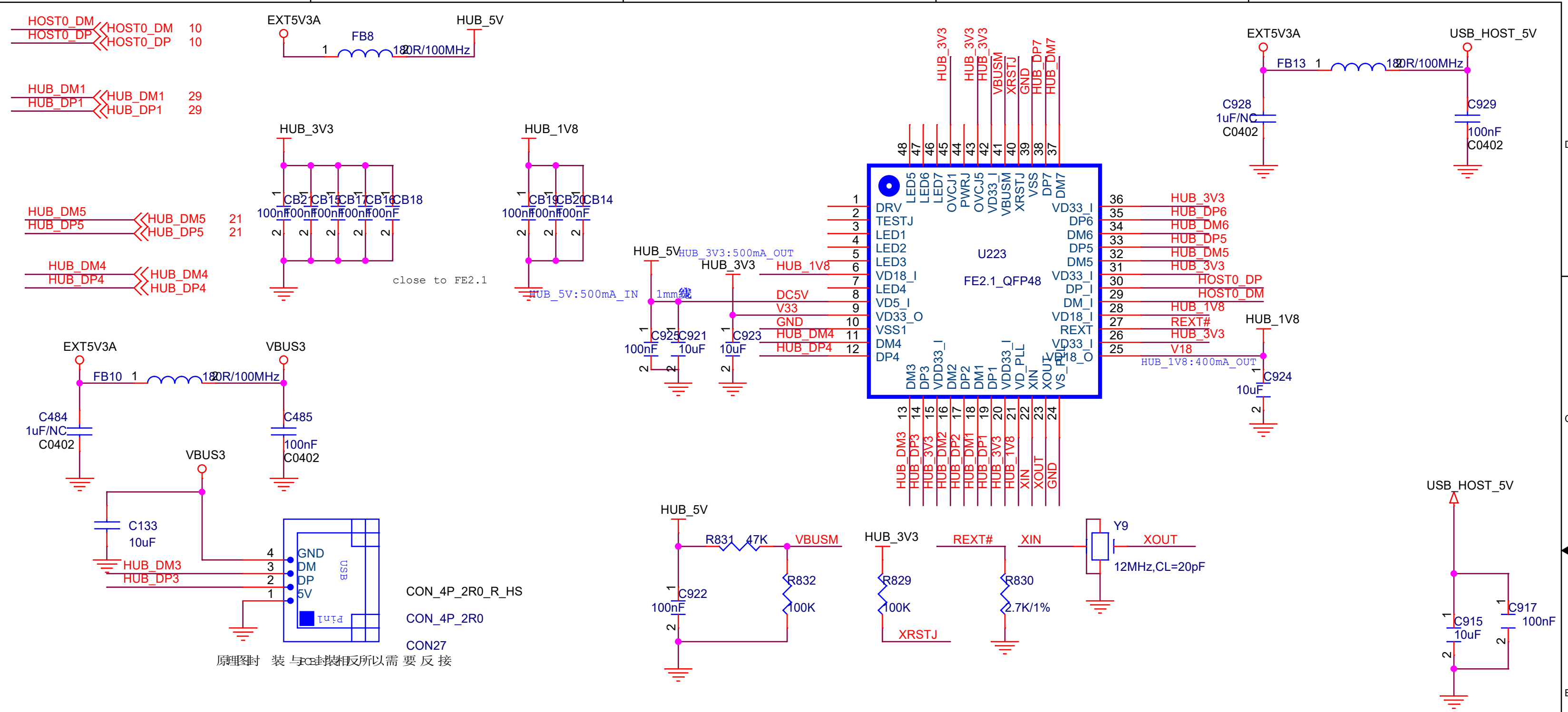
Place C159 close to VDD10\_UPS pin-- 12  
Place C157 close to VDD10\_UPS pin-- 12 ( reserved )



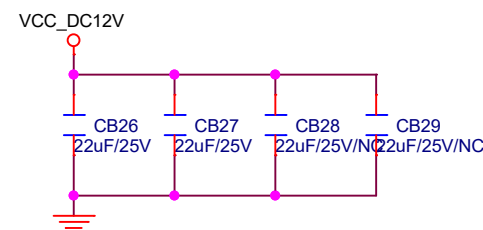
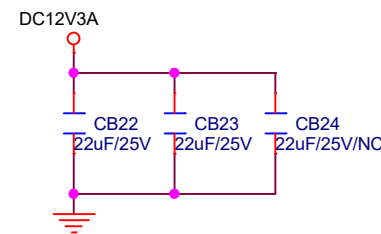
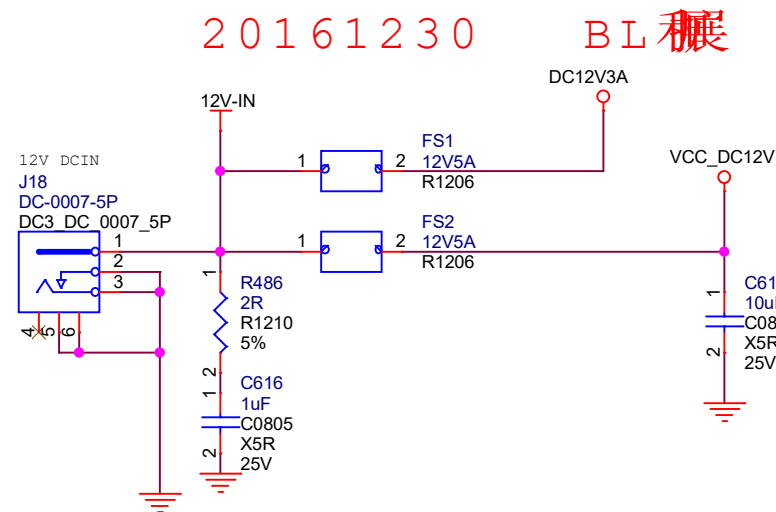
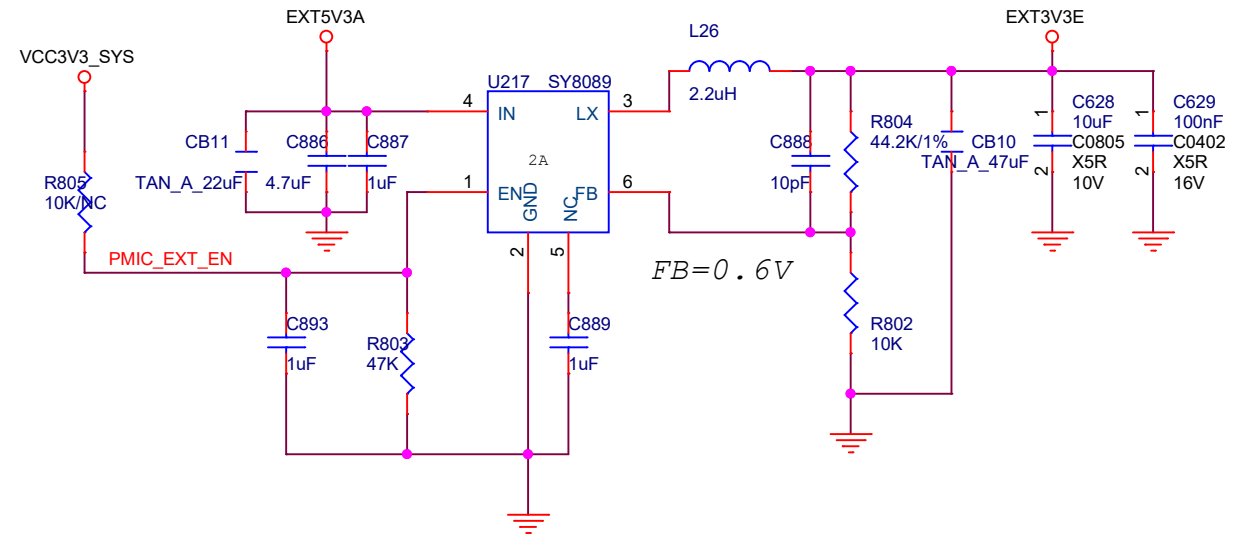
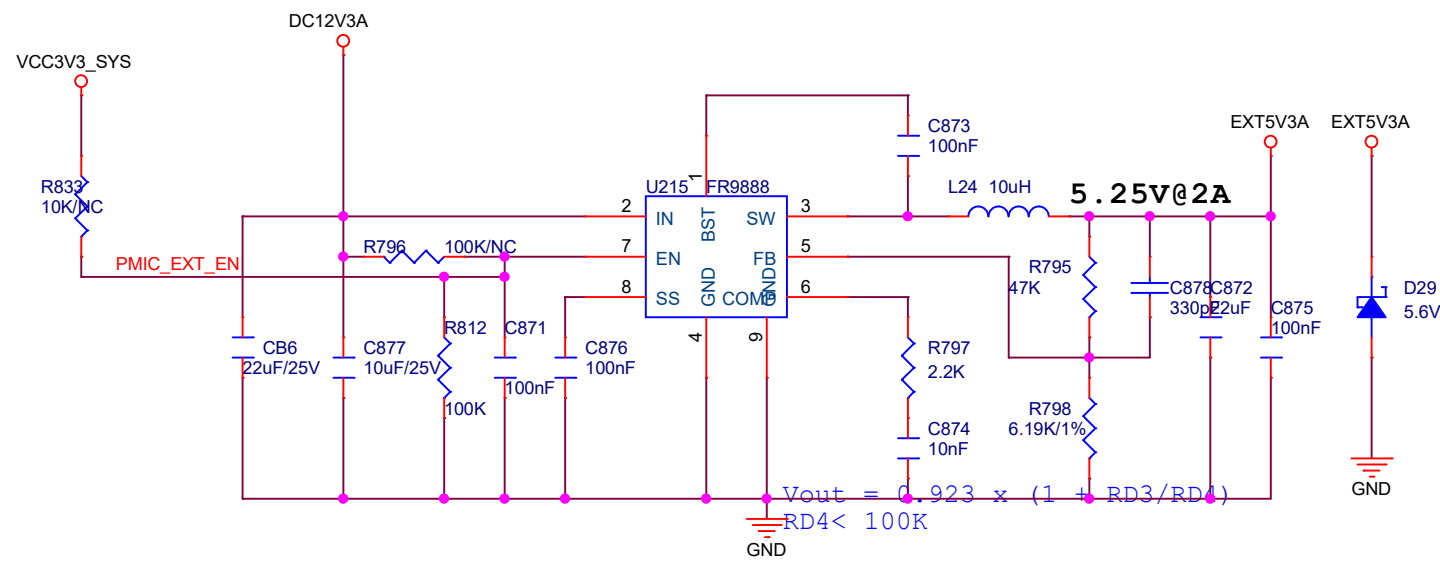
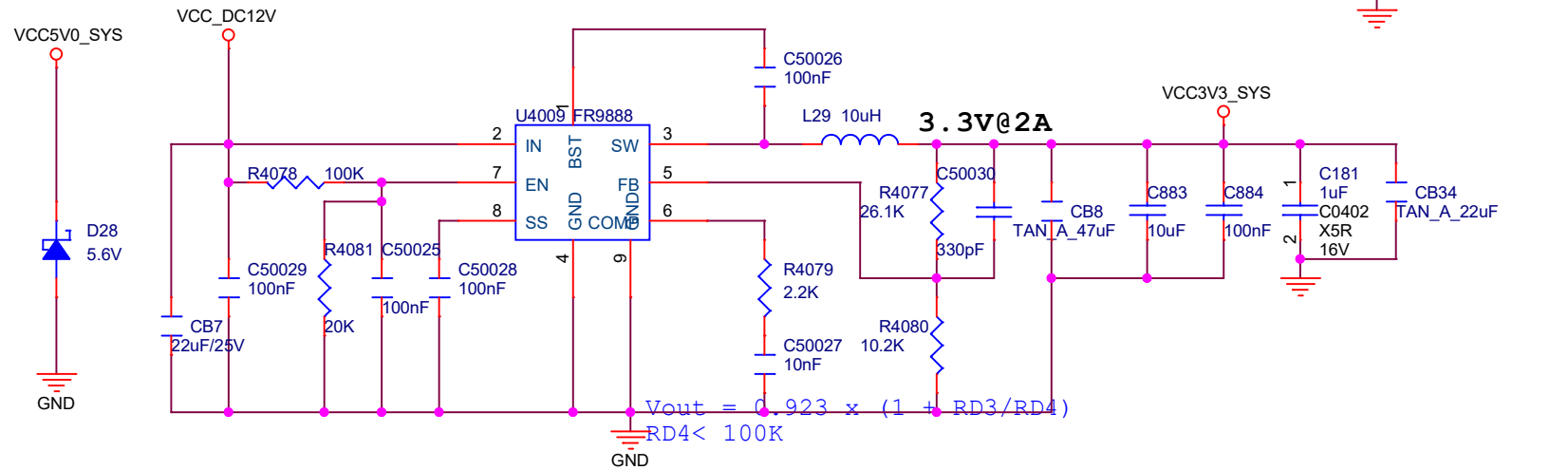
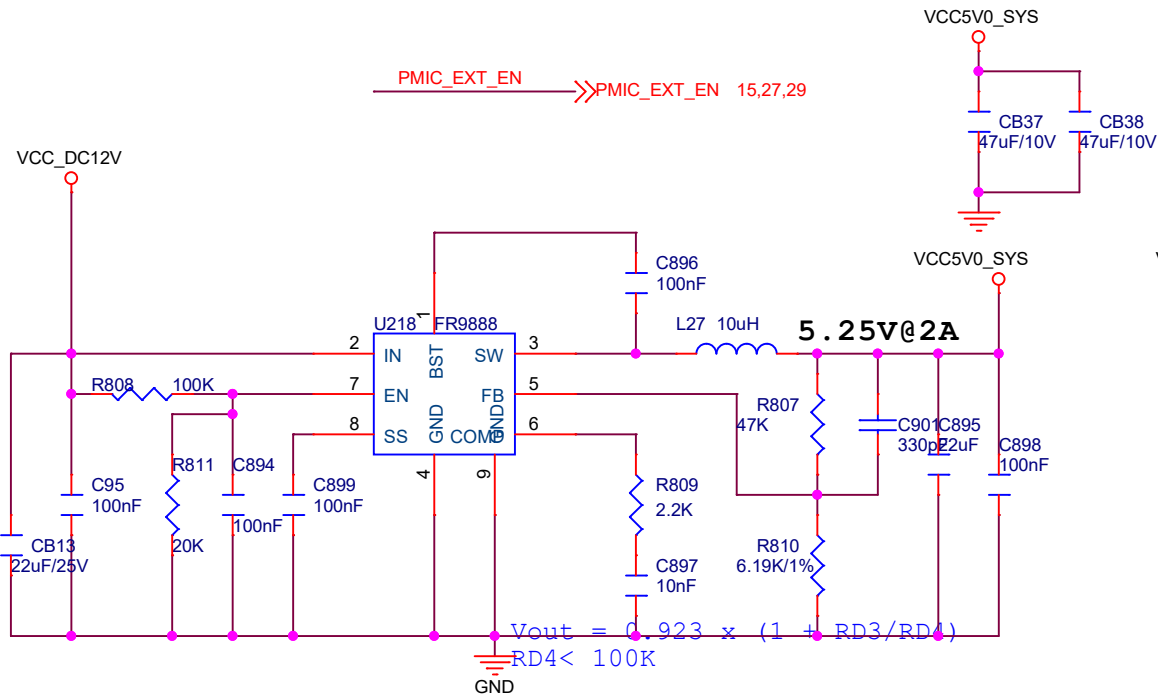
两声道音频信号送入ES8396内部作差分处理，再作ADC转换



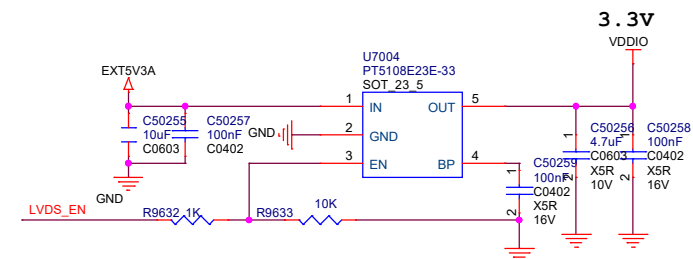
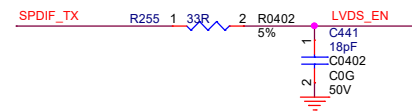




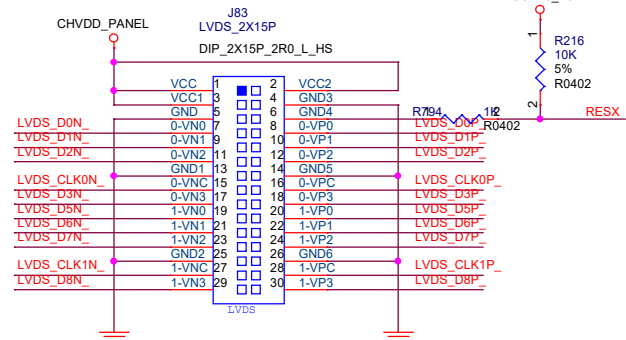
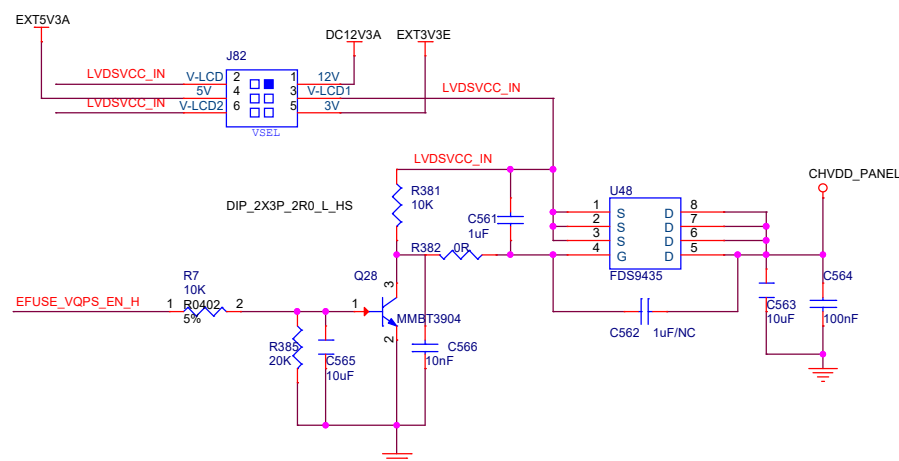
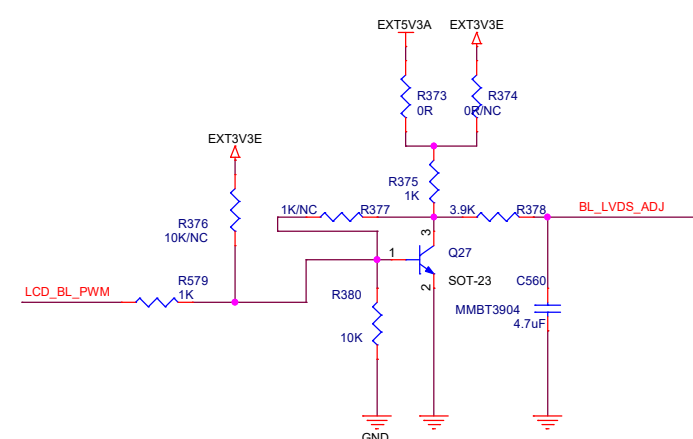
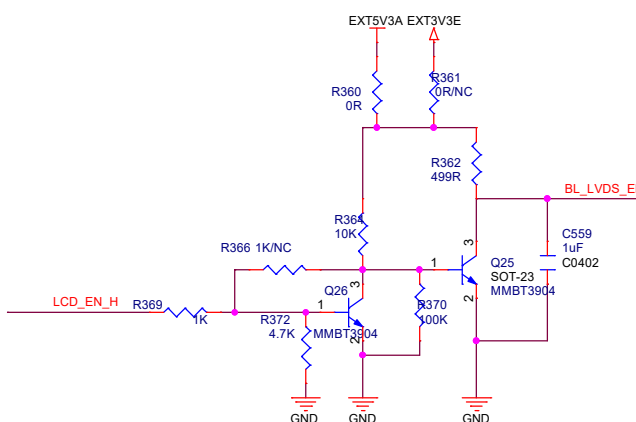
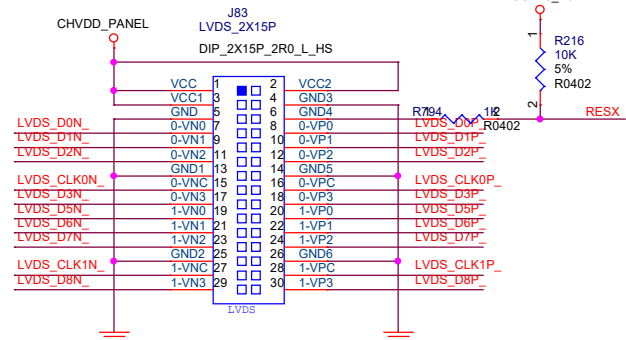
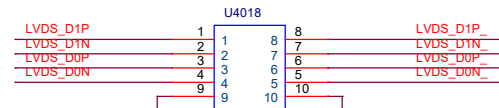
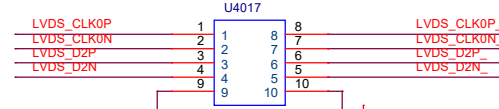
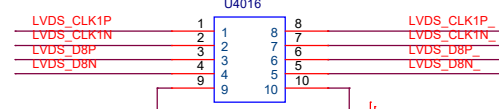
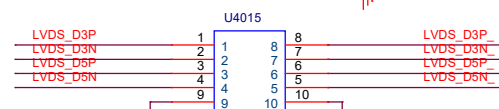
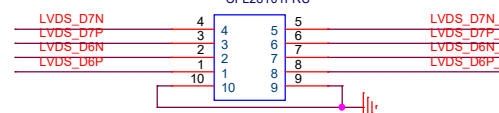
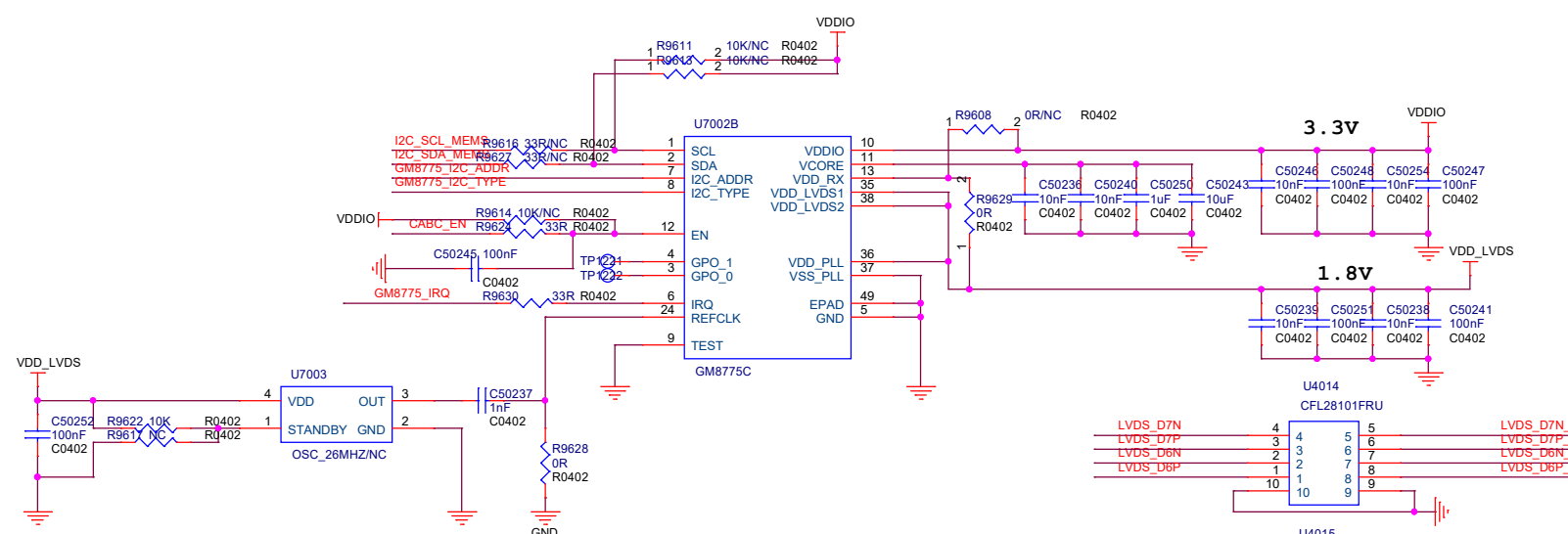
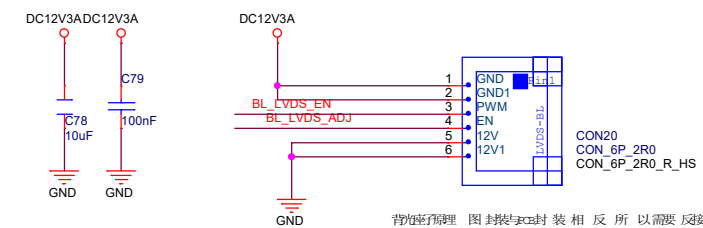
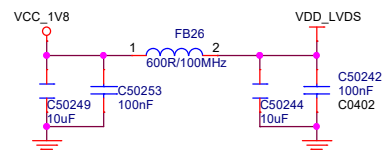
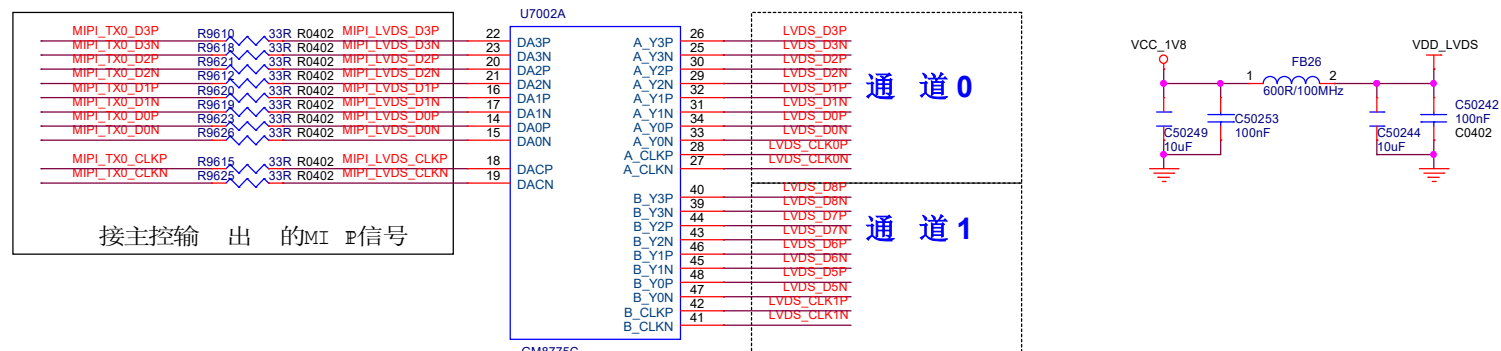




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Design Name		
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接控制 1.8 V 的 IO



GPIO2\_A5/CIF\_D5  
GPIO2\_A6/CIF\_D6

GPIO2\_A5/CIF\_D5 12  
GPIO2\_A6/CIF\_D6 12

POWER\_KEY  
ADKEY\_IN  
RESET\_L

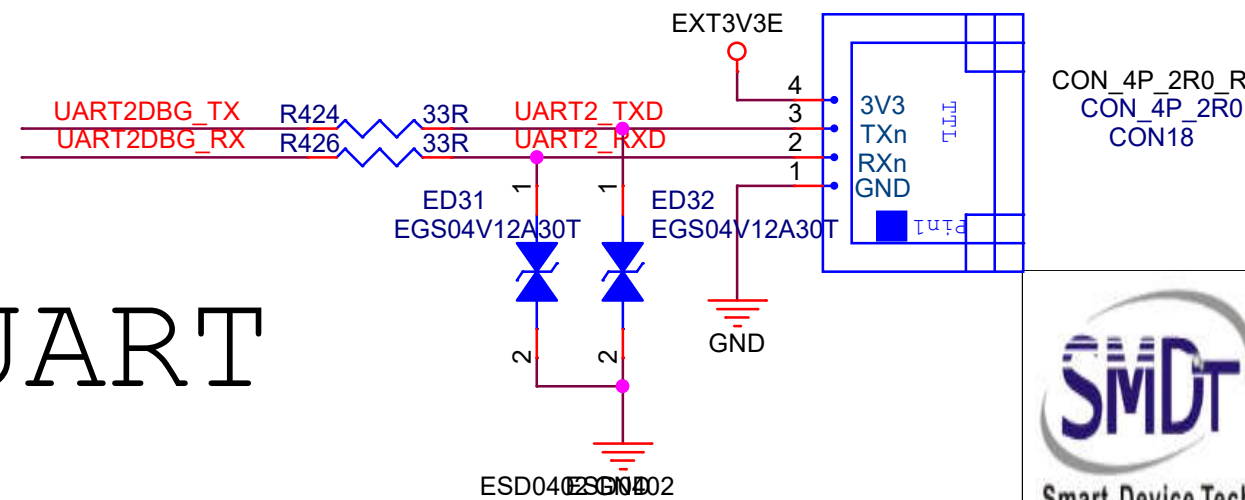
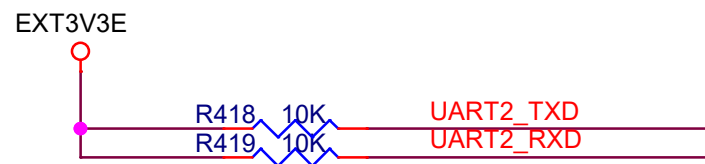
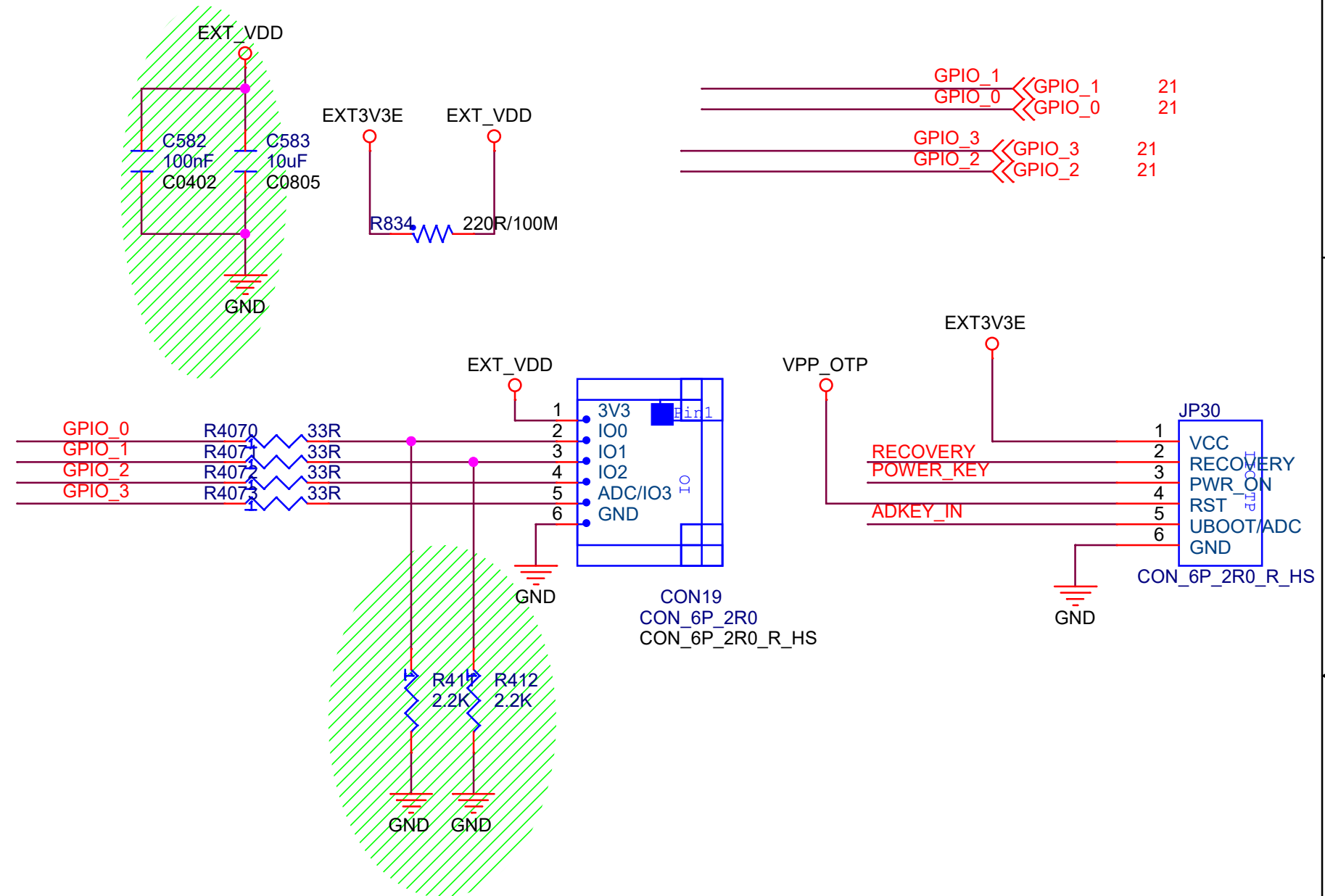
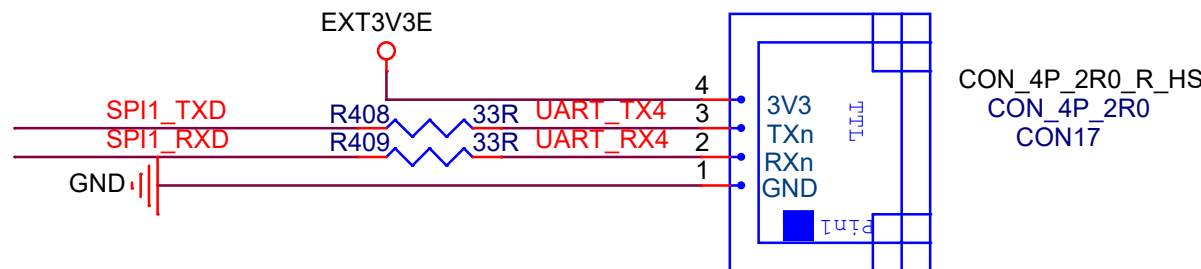
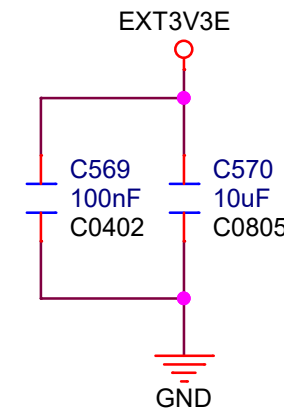
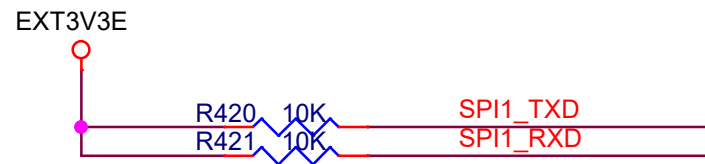
POWER\_KEY 15,30  
ADKEY\_IN 11,30  
RESET\_L 7,15

UART2DBG\_RX  
UART2DBG\_TX

UART2DBG\_RX 14  
UART2DBG\_TX 14

SPI1\_RXD  
SPI1\_TXD

SPI1\_RXD 14  
SPI1\_TXD 14



DEBUG OR UART



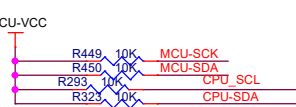
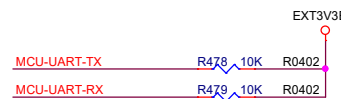
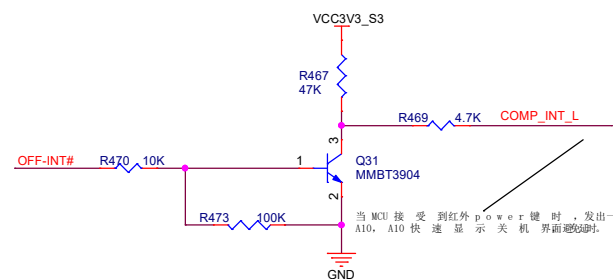
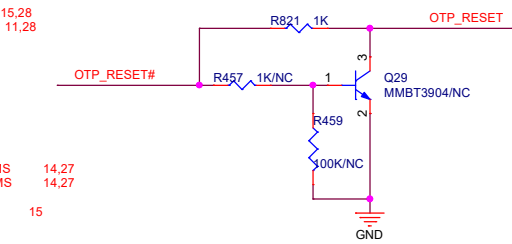
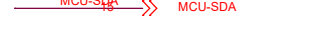
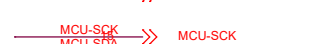
Smart Device Technology Co., Ltd		
Design Name IoT-3399E		
Size A4	Page Name WIFI AC BGN BT	Rev v1.0
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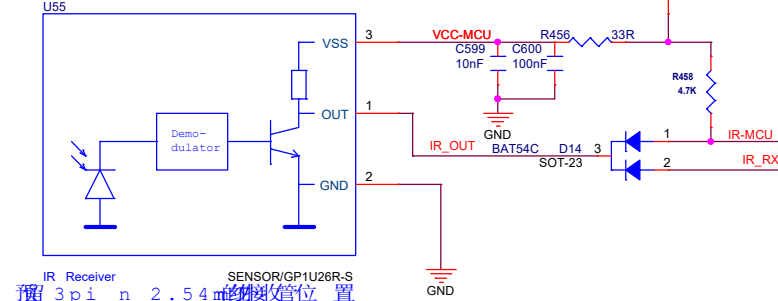
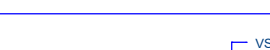
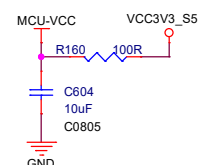
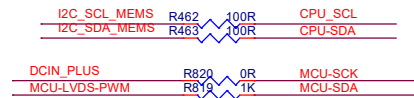
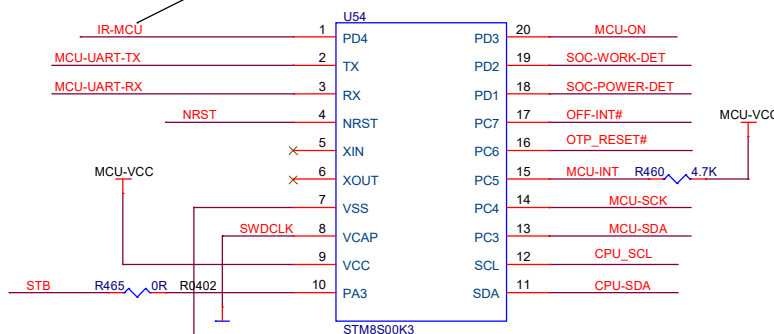
## LED CONTROL



用于MCU检测A10是否处于供电

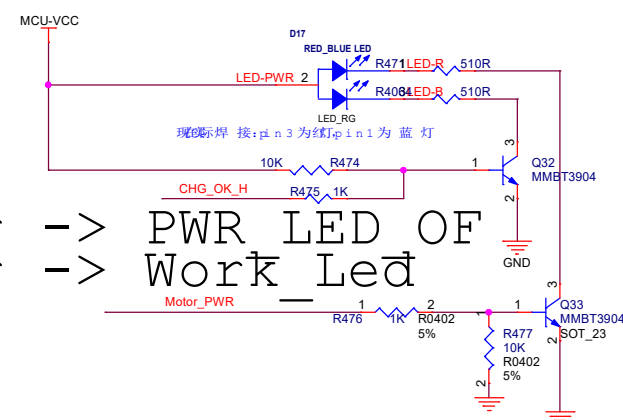


用于MCU检测CPU的工作状态。

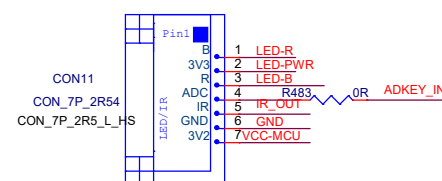
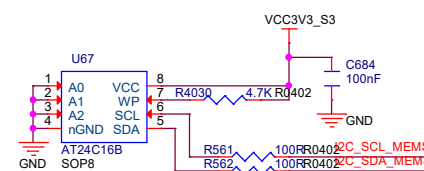
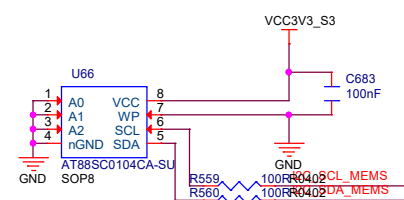
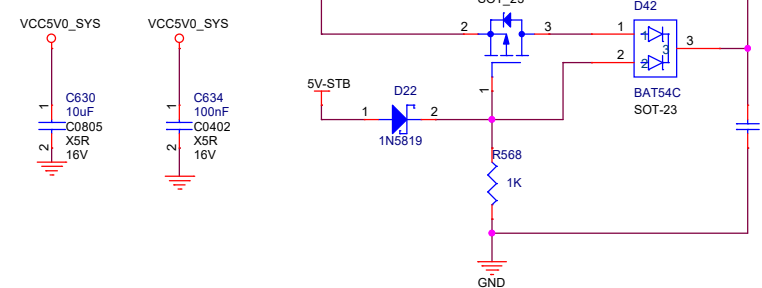
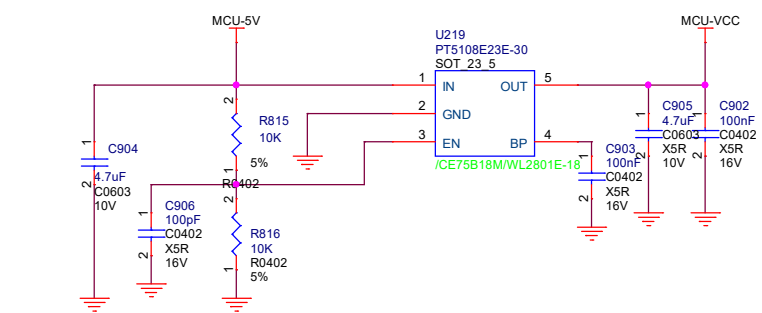
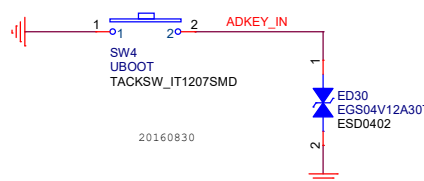


IR Receiver      SENSOR/GP1U26R-S

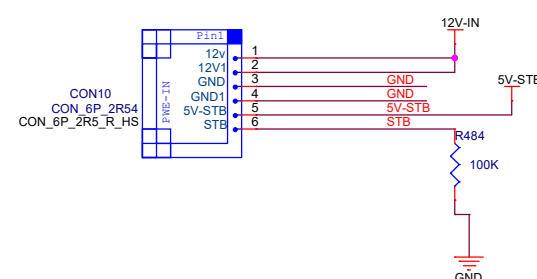
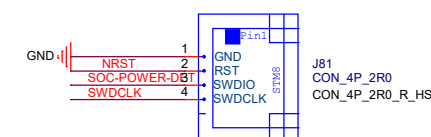
注：VCC-3V3在系统关机时  
MCU-VCC在插入火牛时即



Motor -> PWR LED OF  
Motor -> Work Led

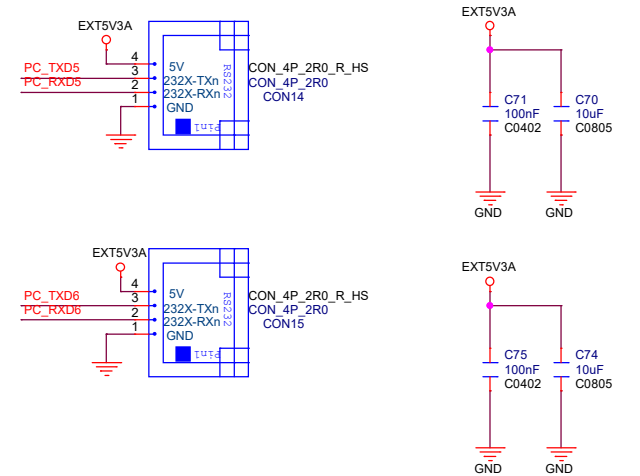
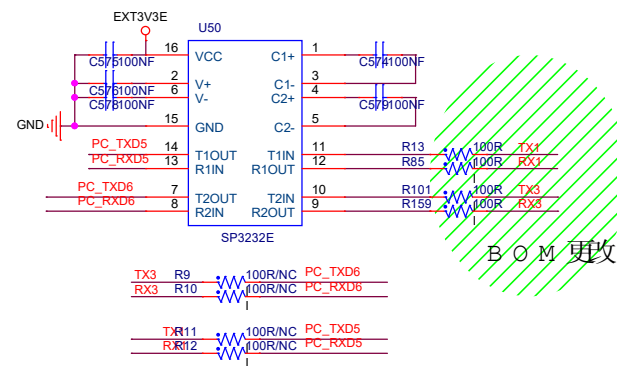
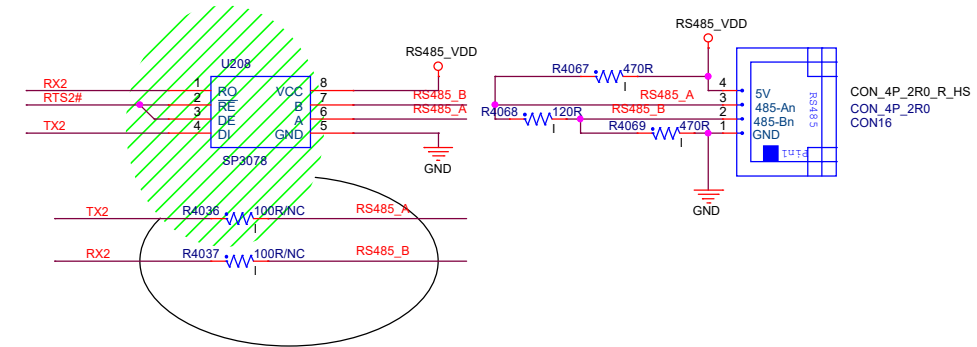
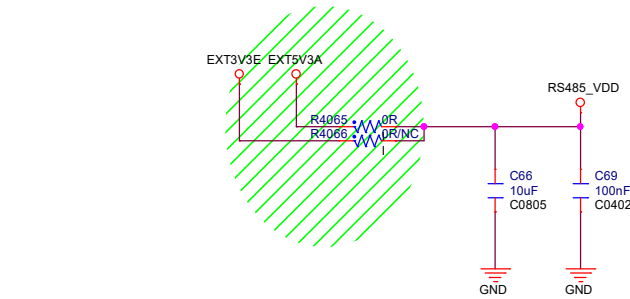
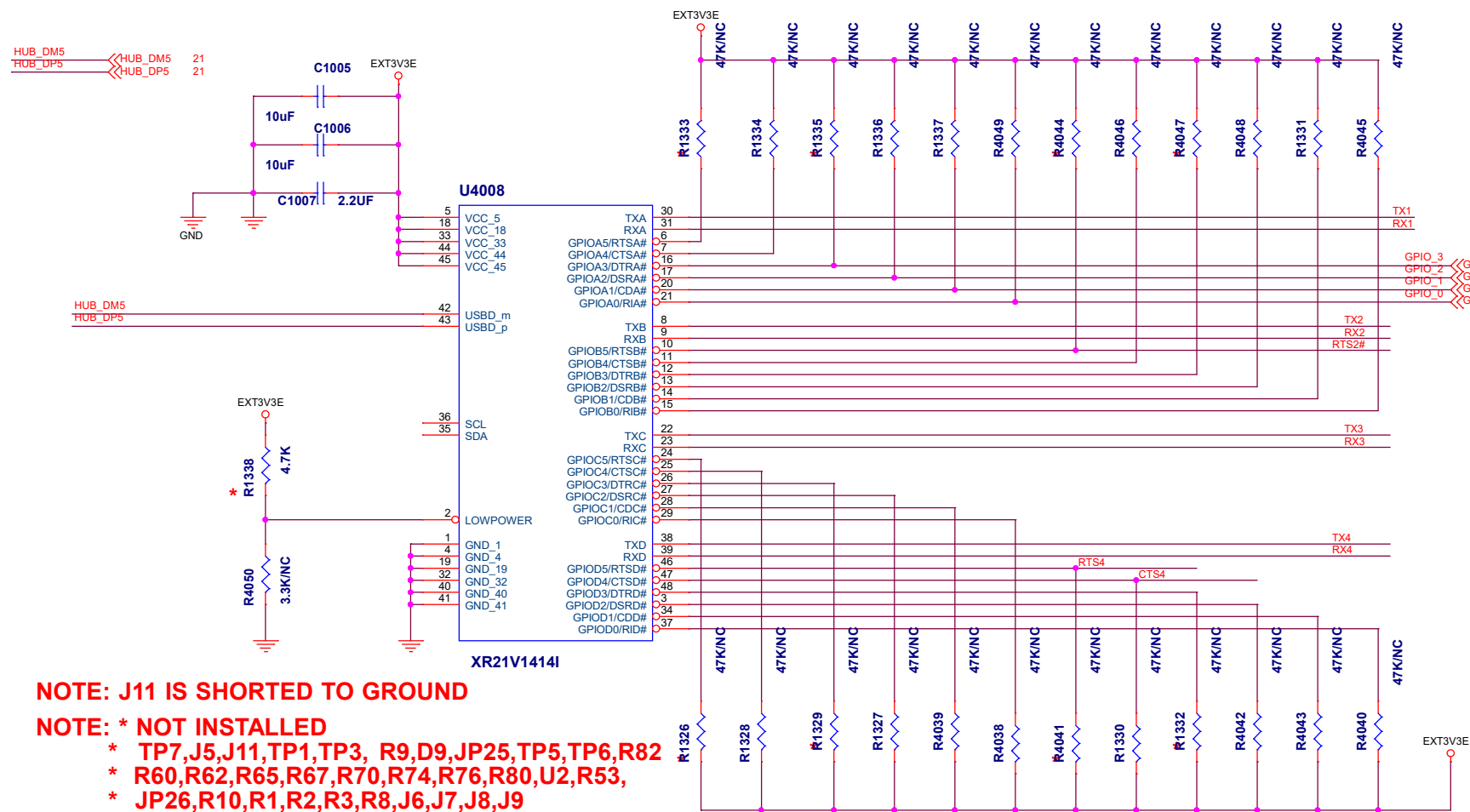


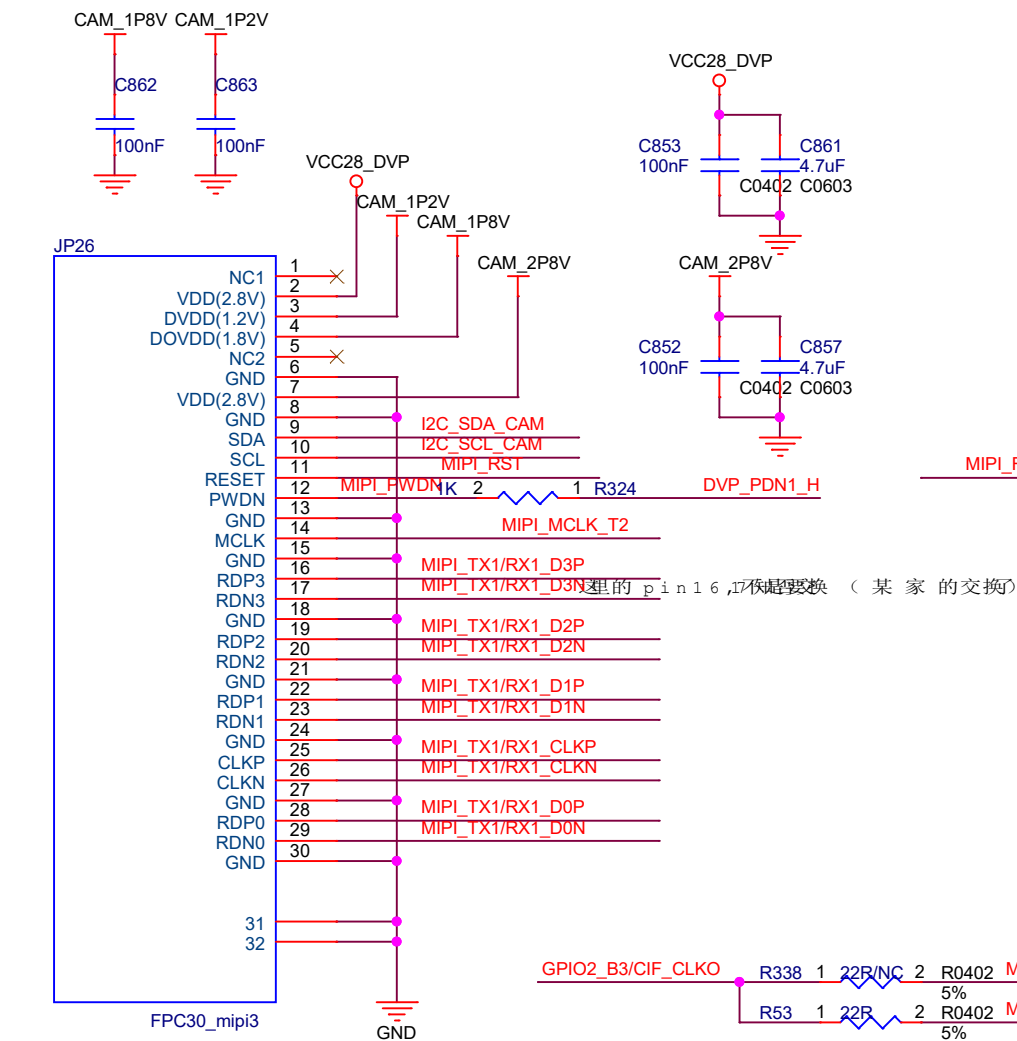
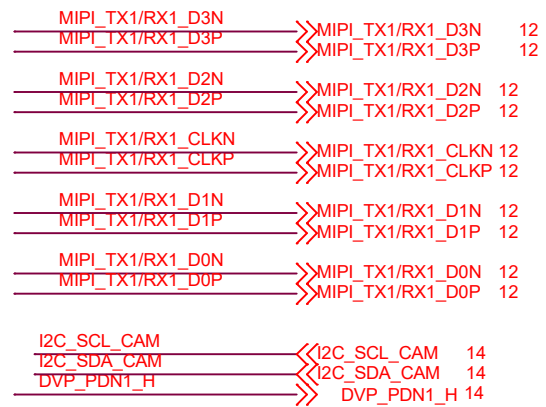
## 指示灯和遥控接口



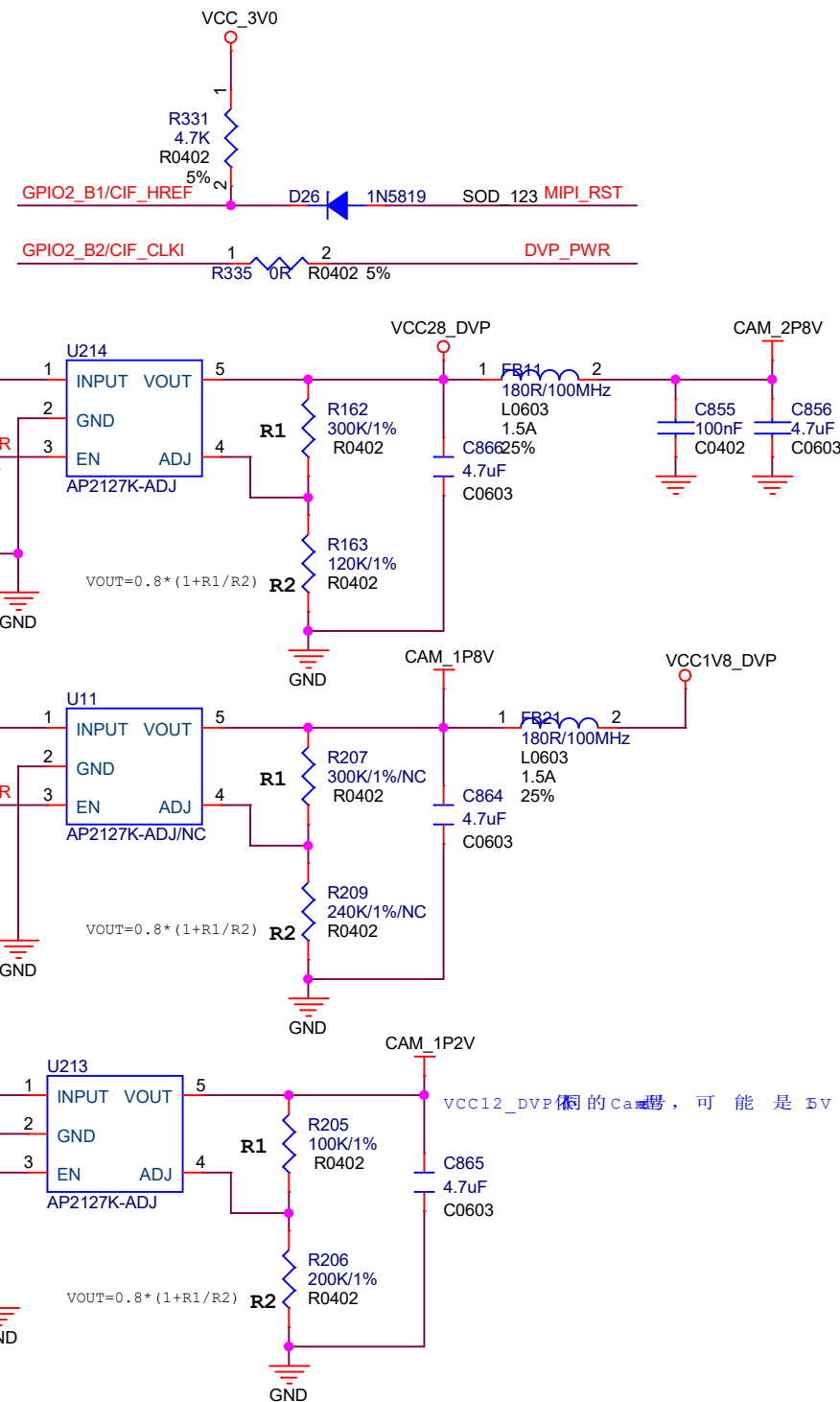


## USB to UART (4CH)



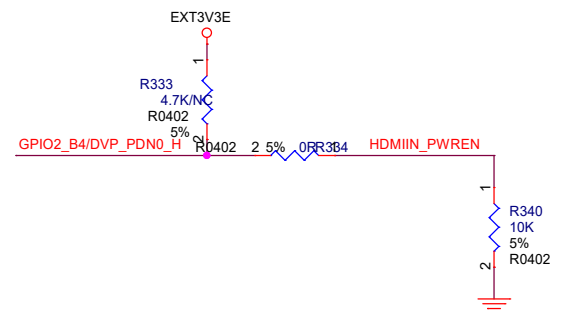
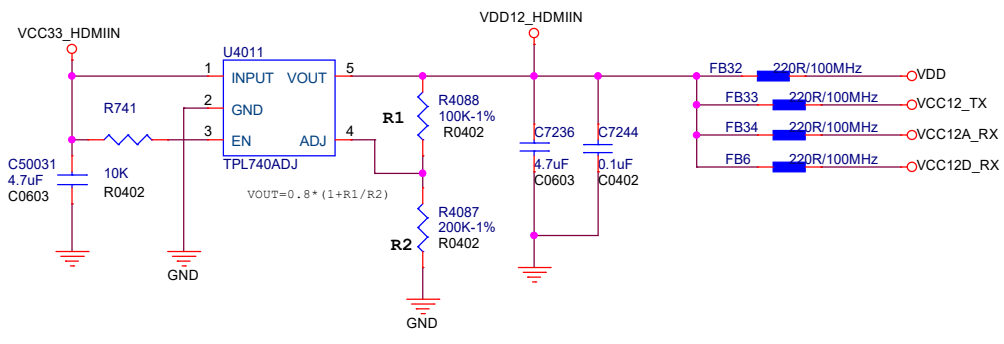
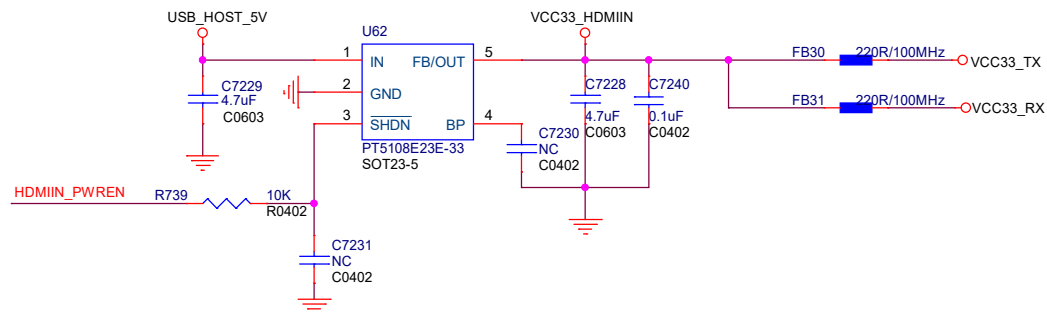
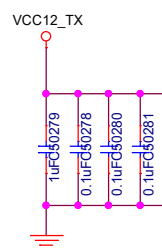
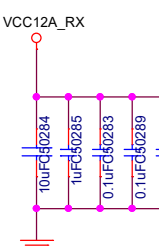
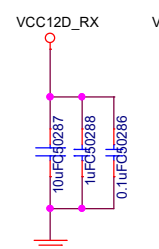
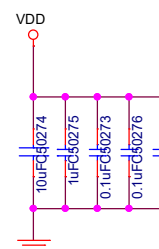
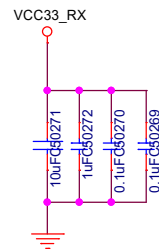
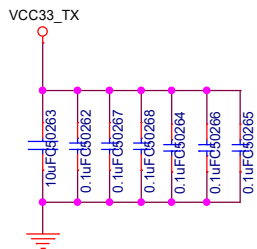
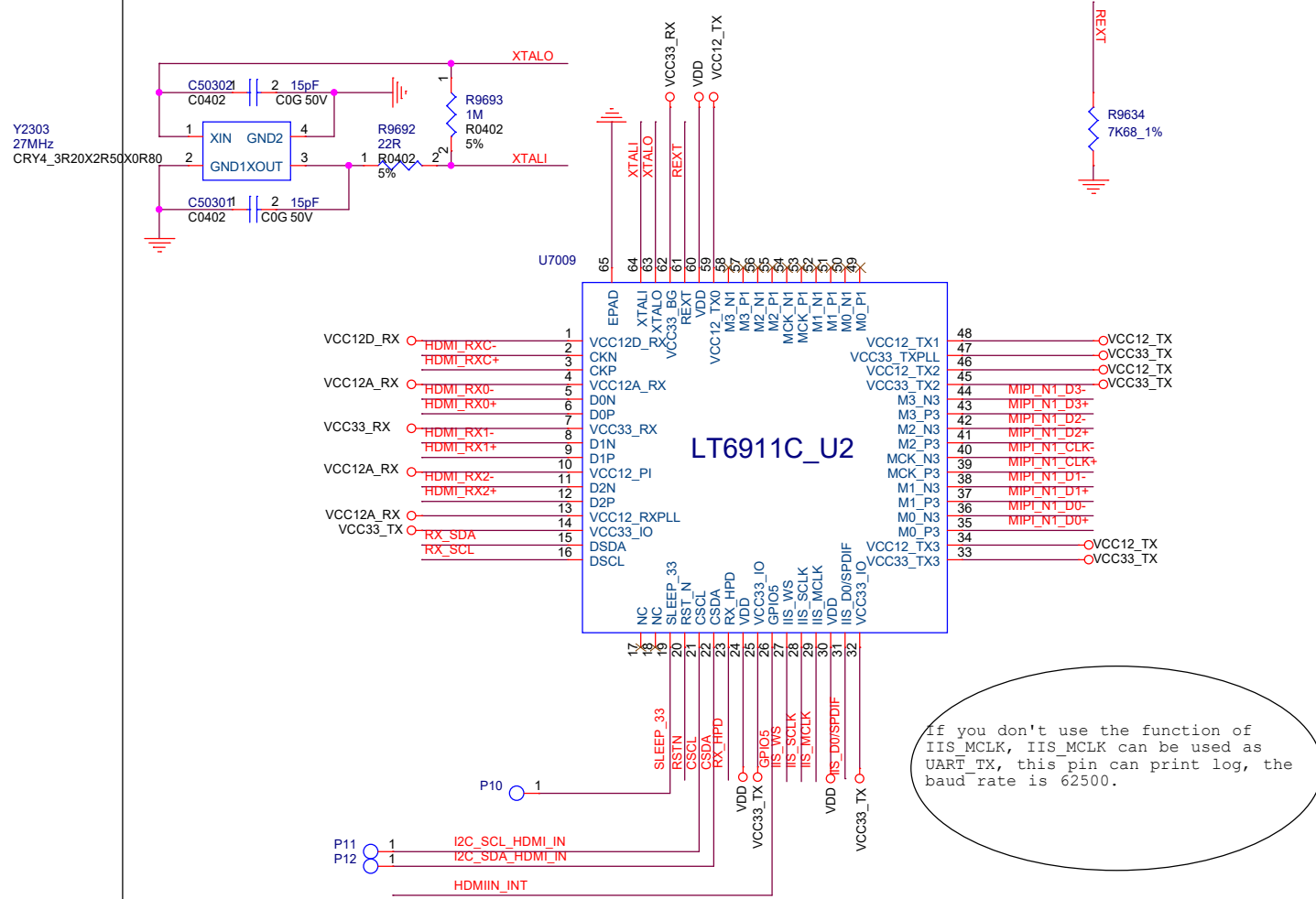
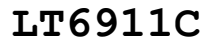
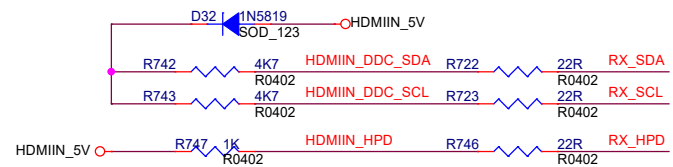
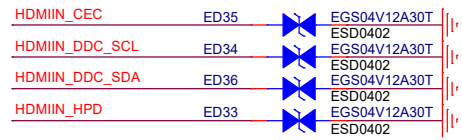
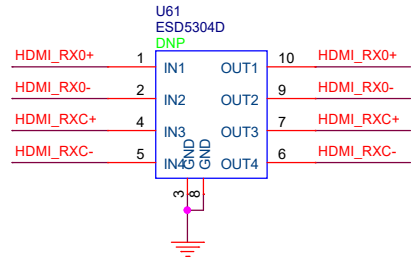
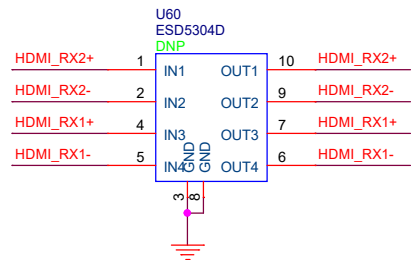
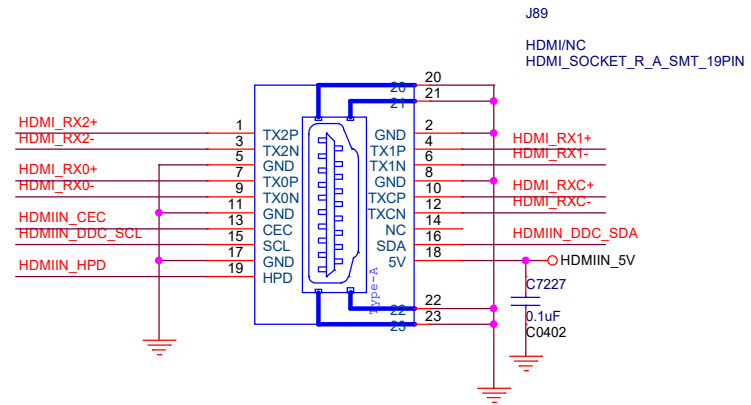


interface for MIPI Camera OV13850  
RK3288 最大支持 1400万像素摄像头

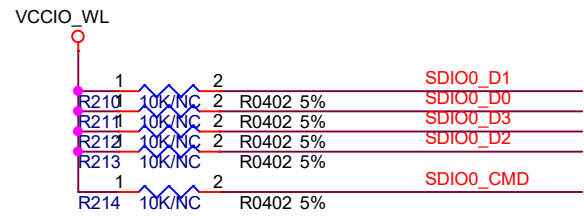
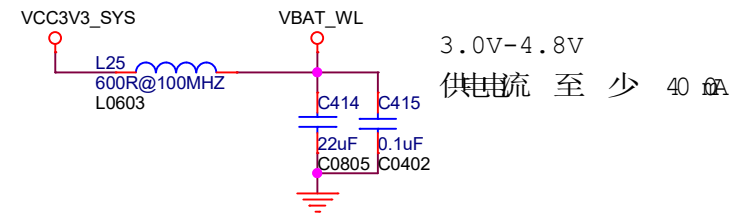
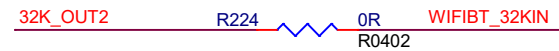
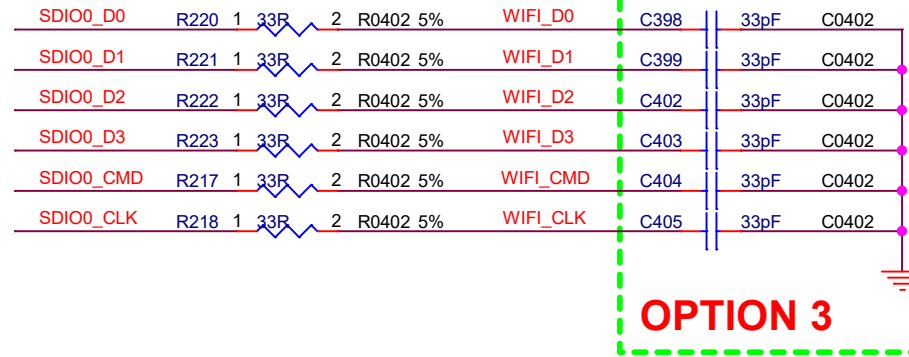
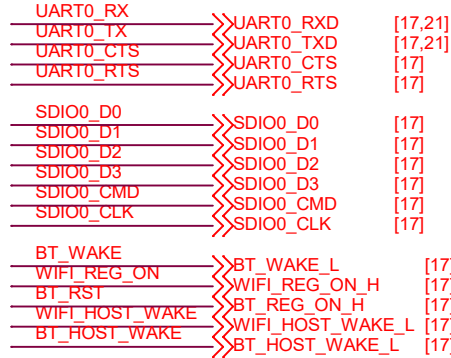


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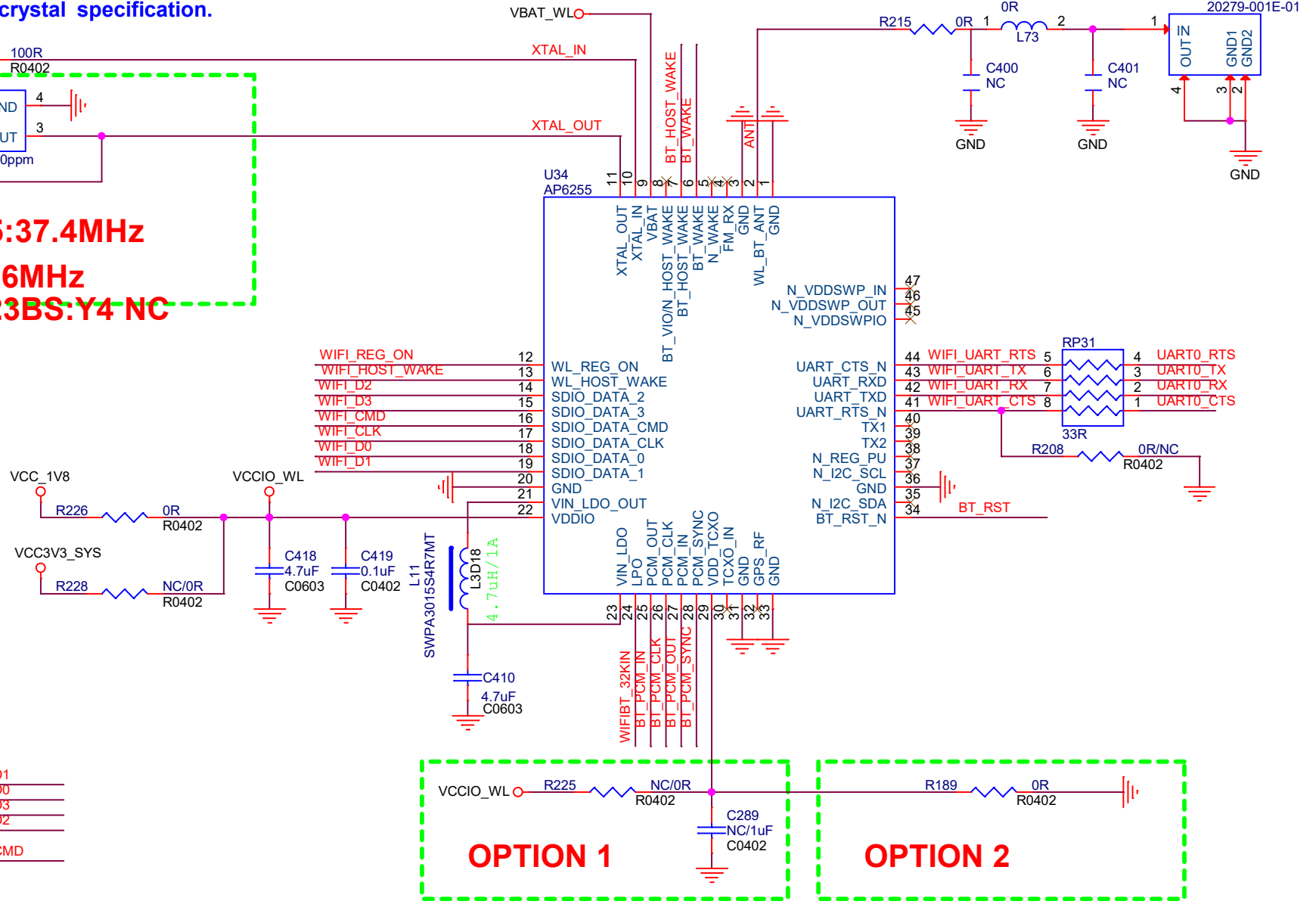
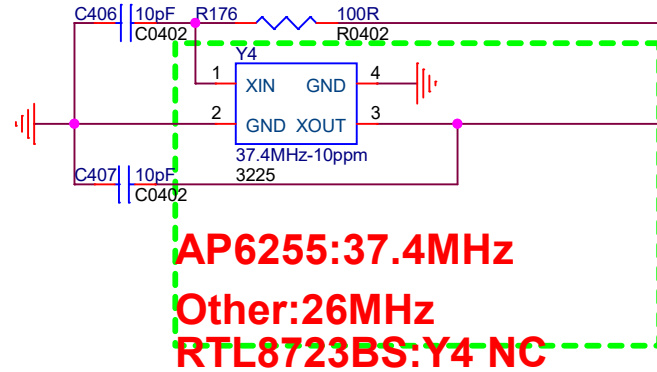
## HDMI IN



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Note:  
Adjusted the load capacitance  
according to the crystal specification.



OPTION	WIFI				BT4.0	Crystals	VDDIO
	a	b/g/n	ac	5GHz			
AP6181		Yes				26MHz	1.71-3.6V
AP6212		Yes			Yes	26MHz	1.71-3.6V
XZ3538		Yes			Yes	26MHz	1.71-3.6V
XZ3660	Yes	Yes		Yes	Yes	26MHz	1.2-2.9V
AP6330	Yes	Yes		Yes	Yes	26MHz	1.2-2.9V
AP6335 (Default)	Yes	Yes	Yes	Yes	Yes	37.4MHz	1.71-3.63V

OPTION	1	2	3
AP6181	No	No	No
AP6212	No	No	No
XZ3538	No	No	No
XZ3660	No	No	No
AP6330	No	No	No
AP6255 (Default)	No	yes	yes

Note:  
Yes : 需要 贴  
No : 不需要 贴

50 Ohm RF trace