REVISION	DATE	COMMENTS
1.0	07/29/2019	initial version
1.1	10/18/2019	Update YT8511 pin name Update power on strapping configuration
1.2	06/19/2020	Update comments and power on strapping Update PHY symbol and application notes

YT8511 Reference Design V1.2

Sheet Title: TITLE		Drawn By: QWB	
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Designed for:		Sheet 1 of 2	
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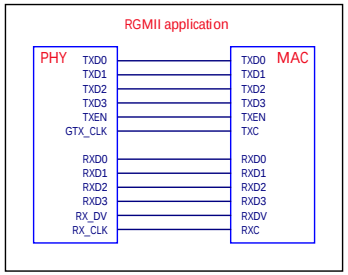
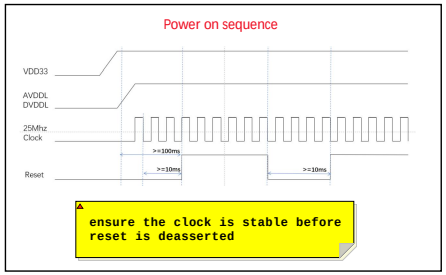
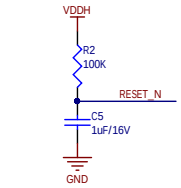
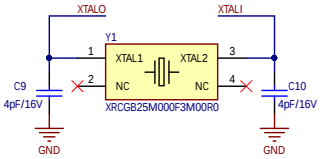


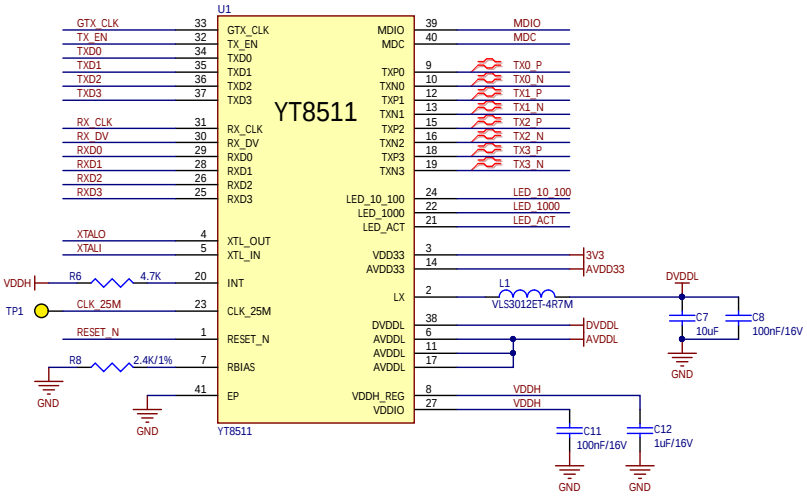


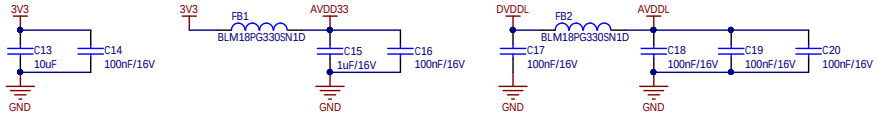


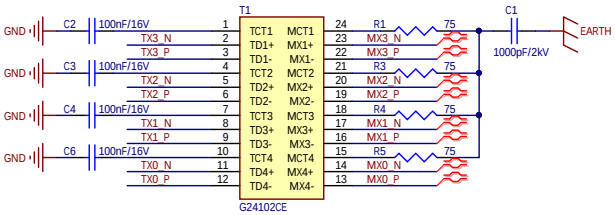


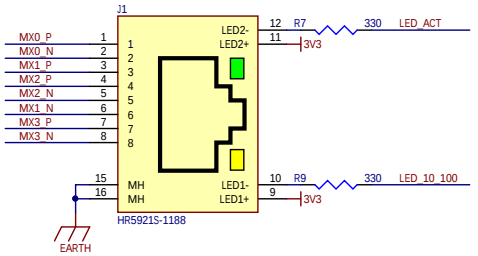
Calculate the value of external load capacitor according to the crystal load capacitance

If external clock is used, the swing clock should be 1.2V

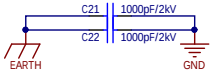






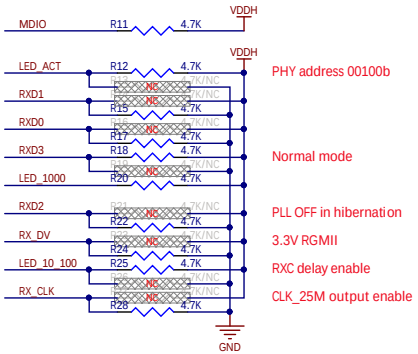


LED_ACT/LED_1000/LED_10_100 active states depend on power on strapping mode. In this reference design, the LED interface is active low



Power on Strapping Pins

LED_ACT (IPU) RXD1 (IPD) RXD0 (IPD)	PHY_ADDR(2) PHY_ADDR(1) PHY_ADDR(0)	PHY Addr = 00000 + PHYA(2:0)
RXD3 (IPD) LED_1000 (IPU)	MODE_SEL(1) MODE_SEL(0)	01 = force low power mode; 11 = normal mode;
RXD2 (IPD)	PLLON	0 = PLL OFF in hibernation; 1 = PLL ON in hibernation;
RX_DV (IPD)	3.3V/2.5V_SEL	0 = RGMII IO 3.3V; 1 = RGMII IO 2.5V;
LED_10_100 (IPU)	RXC_delay_en	0 = RXC delay disable; 1 = RXC delay enable;
RX_CLK (IPD)	CLK_25M_en	0 = CLK_25M enable; 1 = CLK_25M disable;



PHY address 00100b

Normal mode

PLL OFF in hibernation

3.3V RGMII

RXC delay enable

CLK_25M output enable

If power on strapping pins need to be pulled up, they should be pulled up to VDDH

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