

branch
outcome
bit

M bits

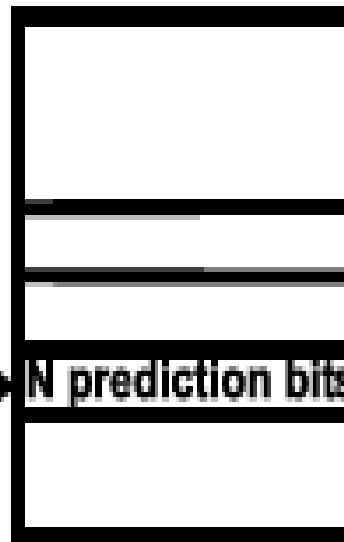
global branch
history register
(shift register)

PC

P bits



branch
prediction buffer



N prediction bits

if M=P then use
EOR
(called *gshare*)