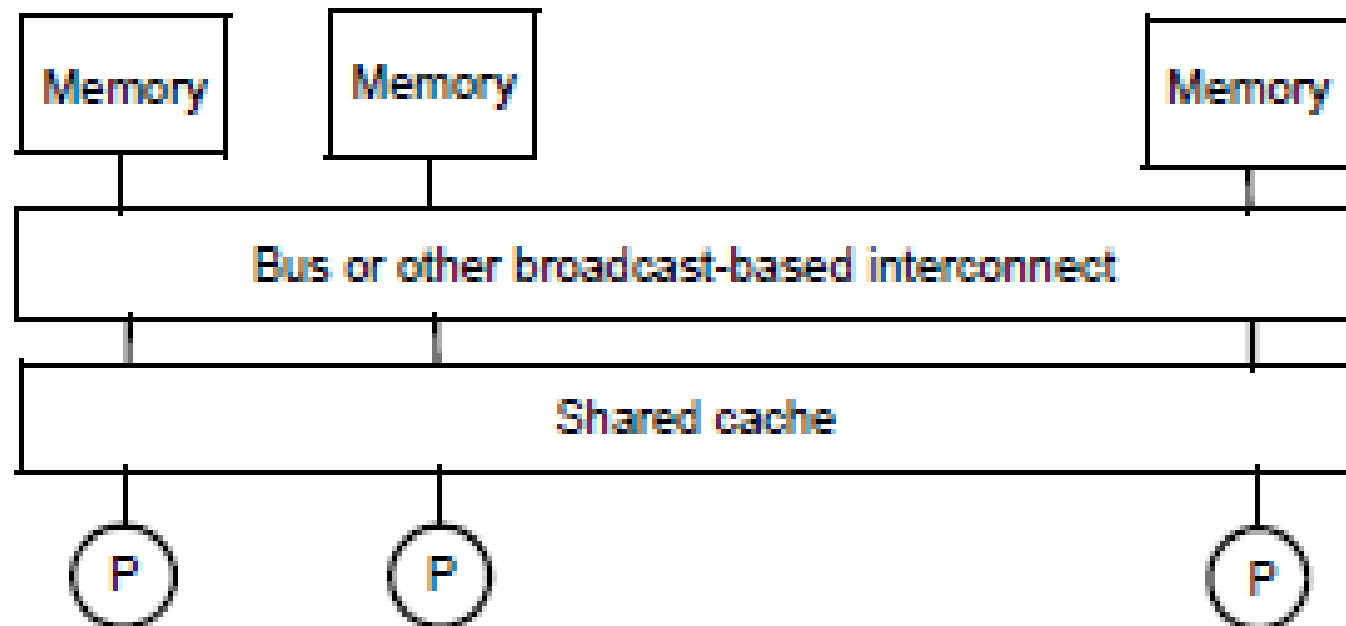


(a) Dance-hall multiprocessor architecture or SMP



(b) SMP with shared level 1 cache