

		Dispatch	Issue	Exec/ start	Exec/ complete	Cache	CDB	COMMENTS
I1	L.S F0,0(R1)	1	2	(3)	3	(4)	(5)	
I2	L.S F1,0(R2)	2	3	(4)	4	(5)	(6)	
I3	ADD.S F2,F1,F0	3	7	(8)	12	–	(13)	wait for F1
I4	S.S-A F2,0(R1)	4	5	(6)	6	–	--	
I5	S.S-B F2,0(R1)	5	14	(15)	15	(16)	--	wait for F2
I6	ADDI R1,R1,#4	6	7	(8)	8	–	(9)	
I7	ADDI R2,R2,#4	7	8	(9)	9	–	(10)	
I8	SUBI R3,R3,#1	8	9	(10)	10	–	(11)	
I9	BNEZ R3,Loop	9	12	(13)	13	–	(14)	wait for R3
I10	L.S F0,0(R1)	15	16	(17)	17	(18)	(19)	wait for I9 (in dispatch)
I11	L.S F1,0(R2)	16	17	(18)	18	(19)	(20)	
I12	ADD.S F2,F1,F0	17	21	(22)	26	–	(27)	wait for F1