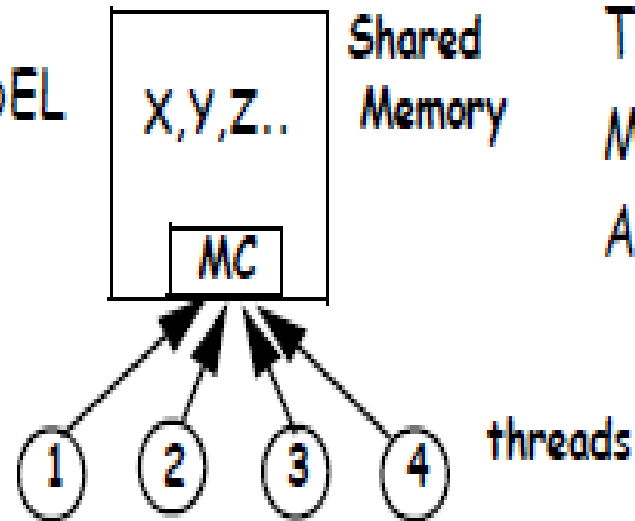


SHARED MEMORY MODEL
FOR EVERY MEMORY
LOCATIONS X,Y,Z



THREADS EXECUTE
MEMORY ACCESSES ONE AT
A TIME IN PROCESS ORDER