

## Will Stotz

wstotz4@gmail.com · 847-532-0052 · <https://cobalt268.github.io> · <https://www.linkedin.com/in/will-stotz-135920255>

---

### Education

**University of Notre Dame**, Notre Dame, IN

5/2026

*Bachelor of Science, Electrical Engineering*

GPA: 3.9

*Honors: Notre Dame College of Engineering Dean's List (FA23, SP24), IEEE-HKN, Tau Beta Pi*

**Iowa State University**, Ames, IA

5/2023

*Computer Science coursework*

GPA: 4.0

*Honors: Top 2% of Class, Iowa State College of Engineering Dean's List*

### Relevant Skills

#### Electrical Engineering

- Completed coursework in embedded systems, signals, amplifiers, digital integrated circuits, analog and digital circuits, and system theory.
- Spring 2025 coursework consists of semiconductor physics, high level synthesis, random phenomena, and quantum computing.
- Expected Fall 2025 coursework will incorporate integrated circuit fabrication and optoelectronic devices.
- Trained in soldering, oscilloscope use, digital multimeter use, power supply use, circuit analysis, LTspice, Cadence Virtuoso, KiCad, Verilog, amateur radio (General class).

#### Linux, Networking, and Computing

- Proficient in Linux and command shell usage, attained the LinkedIn Linux Skill Assessment Badge.
- Experienced with technologies such as Java, Python, C, and Git.

#### Leadership and Social Skills

- Achieved the Eagle Scout rank of the BSA, orchestrated a service project to enhance a conservation garden at the local county fairgrounds, organized construction of a 120 ft. perimeter fence.
- Attained intermediate-level Spanish-language abilities; most skilled in written-language comprehension

### Projects

#### University of Notre Dame

Notre Dame, IN

- Designed standard cells in 0.5u technology which met lambda design rules and passed DRC, LVS, and extraction tests using Cadence Virtuoso, Layout GXL, and Analog Place and Route.
- Synthesized a MIPS 8-bit microprocessor using Genus Synthesis Solution and Innovus Implementation System.
- Currently learning SOC design through Google XLS, SystemC, and Cadence Stratus HLS.

### Work Experience

#### Inventus Power

Woodridge, IL

*Electrical Engineering Intern*

5/2024-8/2024

- Created firmware and specifications for ITAR-controlled defense projects to detect five different signatures of lithium-ion thermal runaway.

#### Pathway to Adventure Council, Scouting America

Chicago, IL

*Program Specialist, Summer Camp Instructor*

6/2018-8/2022

- Developed and refined pedagogical and cognitive skills via working as a merit badge instructor in three separate earth sciences fields at Camp Napowan in Wild Rose, Wisconsin.
- Aided in the facilitation of supply and maintenance logistics for over twenty dispersed campsites as the program specialist at Camp Wolverine in Twin Lake, Michigan.

### Club Experience

#### IrishSat

Notre Dame, IN

*G.O.A.T Lab Electrical Engineer, CubeSat Power Responsible Engineer*

9/2023-present

- Conceptualized, manufactured, and troubleshot two systems to simulate and measure spaceflight environments
- Composed and prototyped PCBs for power and peripheral connection for a CubeSat satellite. Implemented a battery management system for the satellite central power pack.

#### IEEE Notre Dame

Notre Dame, IN

*Vice President*

5/2023-present

- Arranged the revival of the IEEE student chapter on campus to nearly 100 participants, currently organizing trips to industry conventions and electronics activities for students