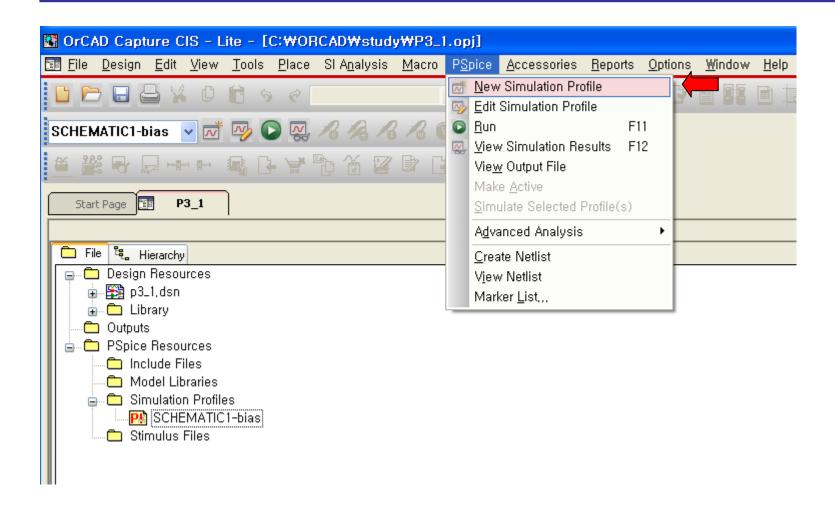
# E3. 정현파 교류회로의 기초

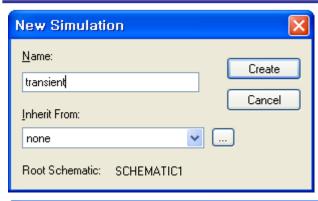


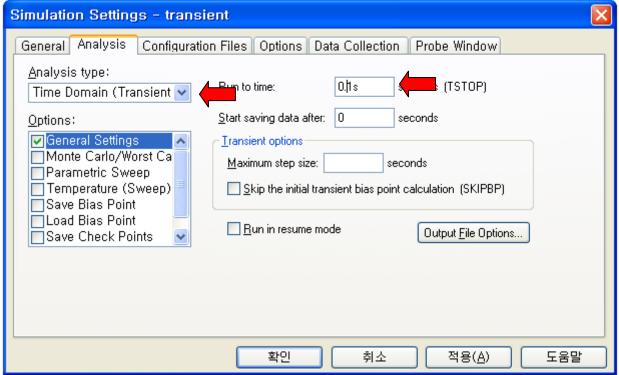
## Simulation Profile 추가





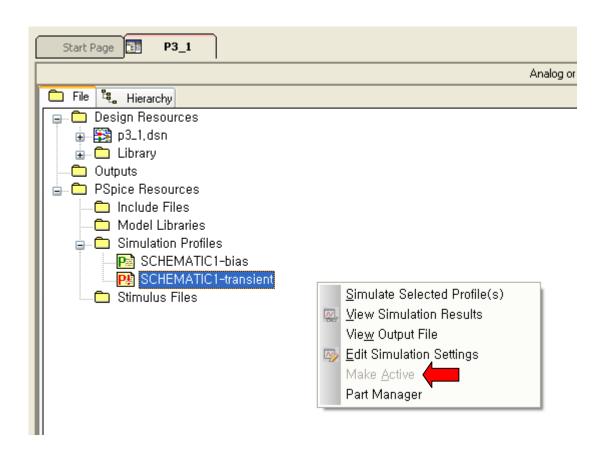
## Simulation Profile 추가





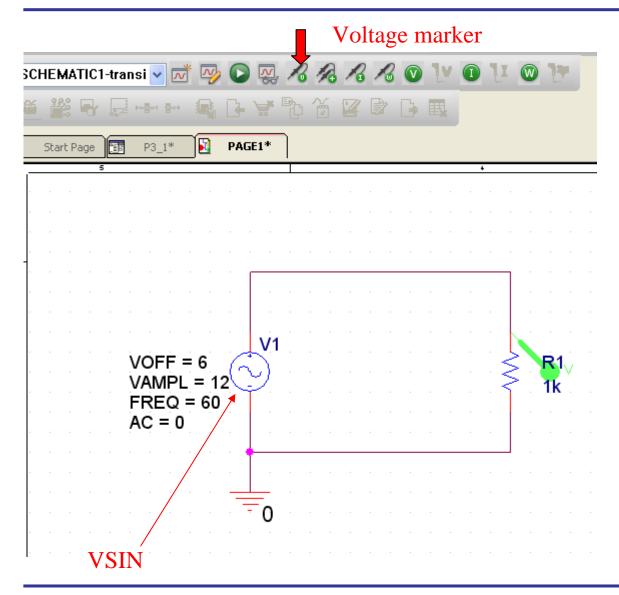


## Simulation Profile 추가



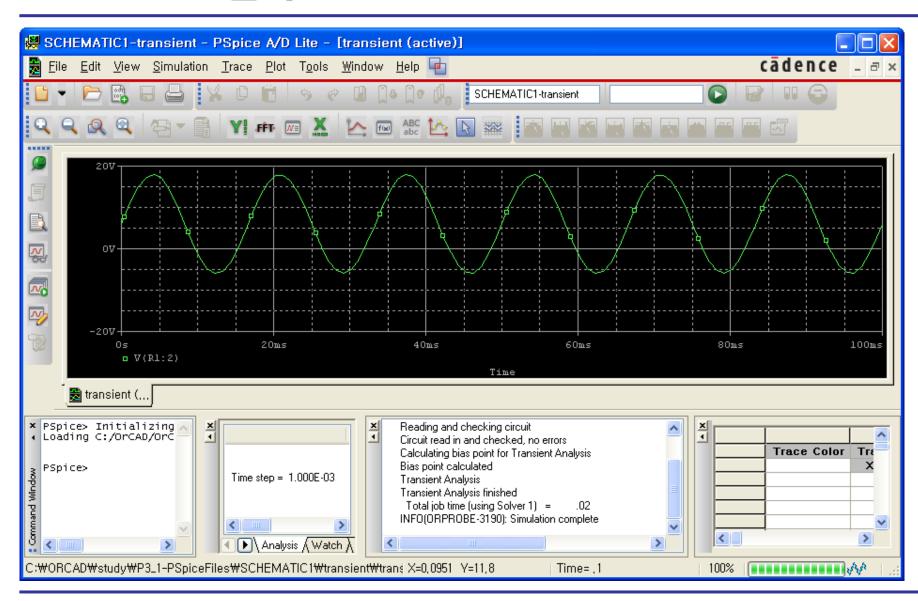


## 회로 입력





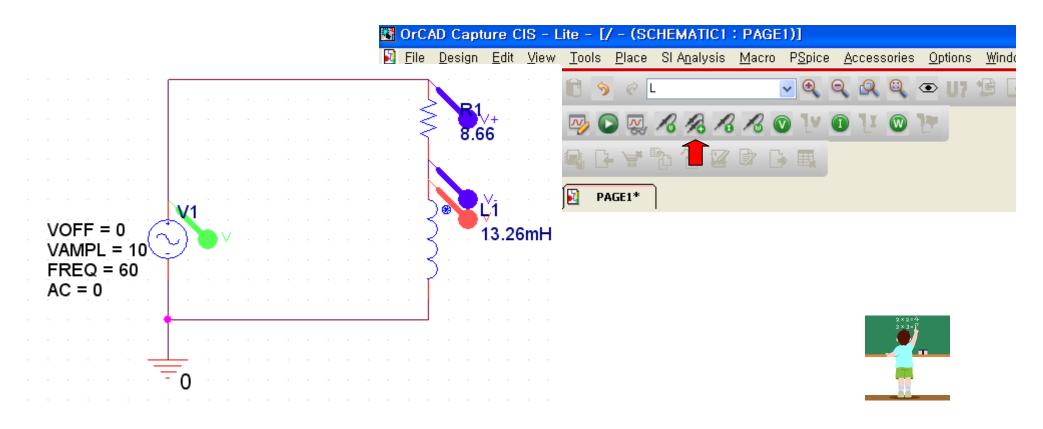
## Simulation 결과





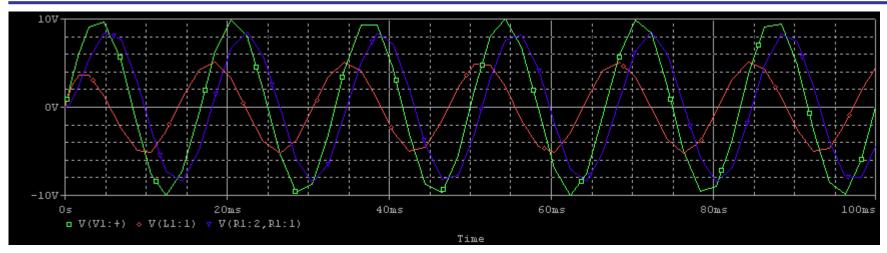
#### 숙제

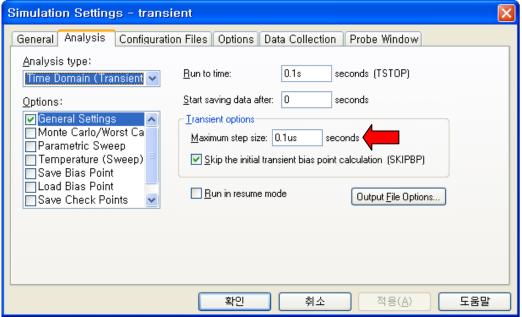
- 1) 아래와 같이 예제5-14의 회로를 구성하라.
- 2) vR. vL이 계산 결과와 일치하는지 확인하시오.













## 숙제

