Performance Modeling - RISC-V processor

This will be an INDIV輕好的代写代做 CS编程辅导

Phase 1: (Due November 7th 11:59PM)

1) Draw the schema processor and fill in your code in the provided file to run the simulator.

2) Measure and report of the secution cycles, and Instructions per cycle by adding performance monitor of the secution cycles, and Instructions per cycle by adding secution cycles, and Instructions per cycle by adding performance monitor.

3) What optimizations or realures can be added to improve performance? (Extra credit)

Your code will be tested against 10 test cases. 3 of which will be released 5 days prior to your submission. (2nd November Chat: CStutorcs

Assignment Project Exam Help

- 1. Please modify the existing code to use the correct way to handle folder paths. (Use os.path.join() instead of hardcoding OS dependent forward/back slashes).
- 2. You'll only submit a zipped folder named netID.zip

Make sure you follow the folder structure grown below.

Your code will be in one folder named netlin with an entry point file (main.py or main.cpp).

There'll be a second folder called input/ with each subfolder named testcase0/,

 $\begin{array}{lll} \text{testcase1,} & \text{etc.,} & \text{and each, of these test cases will contain 2 files, imem.txt} & \text{and } \\ \text{dmem.txt.} & & & & \\ \end{array}$

After running your code, a third folder should be created as an output folder with the name output_netID/ with subfolders named testcase0/, testcase1. Each subfolder must contain 4 files: PerformanceMetrics_Result.txt, SS_RFResult.txt, StateResult_SS.txt.

3. A sample test case is already on brightspace under the project section.

https://tutorcs.com

Solve with your folder structure as:

Name	Status	Date modified	Туре
I input		11/1/2023 5:43 PM	File folder
netid		11/1/2023 5:48 PM	File folder

After running

\$ python3 netid/main.py

The expected output folder structure should be as follows:



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Assignment Project Exam Help

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