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Assignment Project Exam Help

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**SEC204** 

#### Overview



- Introduction
- Unconditional brancheseChat: cstutorcs
- Conditional branches Assignment Project Exam Help

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# INTRODUCTION Project Exam Help

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#### **BRANCHES**

- EIP advances sequilibrium nless otherwise directed
  - With prefetching out-of-order execution, variable CISC instructions length, this is harder than simply incrementing by a static value WeChat: cstutorcs
- EIP cannot be modified directly by the program (ie with a MOV instruction)

  Assignment Project Exam Help
- Instructions that attenthe value of all pare called branches
  - Unconditional branches
  - Conditional branches: 749389476



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## UNCONDITIONAL BRANCHES Email: tutores@163.com

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UNCONDITION MARCHES

• Jumps

Calls

• Interrupts

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#### **JUMP**

jmp location
 Jump EIP to memory add



At command line:

```
Assignment Project Exam Hejumptest.o jumptest.s # Margin Hejumptest jumptest.o  
$ ./jumptest  
$ ./jumptest
```

#### **CALLS**

- call address
  Similar to jump, but it remem
- Return to the original part of Land Letter the call function was called The stack is used to save the return address, local variables/function parameters
- Create calltest.s file with the following that the series. Assemble, link, and run it on command line:

```
# calltest.s - An example of using the CALL instruction add $ pushl $0 output:
    .asciz "This is section * Fimail: tutorcs@163.co overhere:
    .section .text
    .globl _start
    _start:
    pushl $1
    pushl $0
    pushl $1
    pushl $0
    pushl $1
    pushl $0
    pushl
```

Cont...

umped from and can return if needed

```
pushl $output

and lerintf
add $8, %esp

pushl $0

call exit

overhere:

pushl %ebp

movl %esp, %ebp

pushl $2

pushl $output

call printf

add $8, %esp

movl %ebp, %esp

popl %ebp

ret
```

### LEA — LOAD EF直接宣旨 ADDRESS

- Often when using the LEA instruction
- lea operand1, operand2
   Computes the effective address of approad1 and loads it into register operand2
- Example: lea -4 ( Seppendant Project Exam Help The memory address to be loaded into %eax will be 4 bytes less than the address stored in %ebp. tutorcs@163.com

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#### **INTERRUPTS**

- Hardware interrupts
  - Hardware devices genera incoming signal)

nterrupts (ie an I/O port receives an

- Software interrupts
  - Programs generate software interrupts to stand off control to another program
  - When a program is called by an interrupt, the calling program is put on hold and the called program takes Assignment Project Exam Help
  - A lot of Microsoft DOS interrupts use the 0x21 software interrupt code
  - A lot of Linux programs use the gx80finterrupt toprovide lowy level kernel functions

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## CONDITIONAL BRANCHES Email: futores@163.com

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## CONDITIONAL直線論的HES

		<u>. T</u>	
<ul> <li>Will only execute on cor</li> </ul>	Tutor CS	nstruction	Description
(and value of EFLAGS reg		<b>.</b> A	Jump if above (unsigned numbers)
<ul><li>Parity Flag (PF)</li><li>Sign Flag (SF)</li><li>Zero Flag (ZF)</li></ul>		JAE	Jump if above or equal (unsigned)
	WeChat	:Jestutor	unsigned)
		JBE	Jump if below or equal (unsigned)
	Assignn	nemt Pro	eot ExamteHelp
		JE	Jump if equal
<ul> <li>Conditional jumps depend on these values</li> <li>A subset of these jumps are Q shown in the table</li> </ul>	nemail: to	utorcs@	Jump if (tess (signed numbers)
		JG	Jump if greater (signed numbers)
		£89476	Jump if greater or equal (signed numbers)
		JNE	Jump if not equal
https://tutorcs.comp if zero			

#### **CMP**

• cmp operand1, operand2 to operand3 to op

```
# cmptest.s - An example Wind at: cstutorcs # the CMP and JGE instructions
                                               At command line:
.section .text
                                                ld —o cmptest cmptest.o
.globl start
                         Assignment Pro
start:
   nop
                                                ./cmptest
                        Email: tutorcs@169.com
   movl $15, %eax
   movl $10, %ebx
   cmp %eax, %ebx
                         QQ: 749389476
   jge greater
   movl $1, %eax
   int $0x80
                        https://tutorcs.com
greater:
   movl $20, %ebx
   movl $1, %eax
   int $0x80
```

#### LOOPS

Loop address
 Loop to memory label ac

the ECX register is zero.

```
# loop.s - An example of the loop instruction
.section .data
                         WeChat: cstutorcs
output:
  .asciz "The value is: %d\n
.section .text
                         Assignment Project E
.globl start
start:
  movl $100, %ecx
  movl $0, %eax
                         Email: tutorcs@163.com
loop1:
  addl %ecx, %eax
  loop loop1
                         QQ: 749389476
  pushl %eax
  pushl $output
  call printf
  add $8, %esp
                         https://tutorcs.com
  movl $1, %eax
  movl $0, %ebx
  int $0x80
```

#### At command line:

```
$ as -o loop.o loop.s
$ ld -dynamic-linker /lib/ld-
linux. Felp lc -o loop loop.o
```

### 

1. Create an assembly

```
int main()
{
   int a = 100;
   int b = 25;
   int greater = 0;
   if (a > b)
   {
      greater = a;
   } else greater = b;
   return 0;
}
```

r the following c code:

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In (gdb), watch the value of greater change:

```
break *main
run
x/4d &greater
amp Help
x/4d &greater
cont
```

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FURTHER REAL

Professional Assembl

**it**e, chapter 6

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