Ftrace Kernel Hooks: More than just tracing

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Ftrace Function Hooks

- Function Tracer
- Function Graph Tracer
- Function Profiler
- Stack Tracer
- Kprobes
- Uprobes
- Perf
- Pstore
- SystemTap

Function Tracing

```
# cd /sys/kernel/debug/tracing
# echo function > current tracer
  cat trace
# tracer: function
 entries-in-buffer/entries-written: 205022/119956607
                                                      #P:4
#
                              ----=> irgs-off
                             / ----> need-resched
#
                             / ---=> hardirg/softirg
                             / / _--=> preempt-depth
#
                                     delay
                                              FUNCTION
           TASK-PID
                      CPU#
                                   TIMESTAMP
                            \Pi\Pi\Pi
         <idle>-0
                      [002] dN.1
                                 1781.978299: rcu eas exit <-rcu idle exit
         <idle>-0
                                 1781.978300: rcu_eqs_exit_common <-rcu_eqs_exit
                      [002] dN.1
         <idle>-0
                      [002] .N.1 1781.978301: arch_cpu_idle_exit <-cpu_startup_entry
         <idle>-0
                      [002] .N.1 1781.978301: tick nohz idle exit <-cpu startup entry
                      [002] dN.1
                                1781.978301: ktime_get <-tick_nohz_idle_exit
         <idle>-0
                                 1781.978302: update_ts_time_stats <-tick_nohz_idle_exit
         <idle>-0
                      [002] dN.1
                                 1781.978302: nr iowait cpu <-update ts time stats
         <idle>-0
                      [002] dN.1
         <idle>-0
                      [002] dN.1 1781.978303: tick do update jiffies64 <-tick nohz idle exit
         <idle>-0
                      [002] dN.1 1781.978303: update cpu load nohz <-tick nohz idle exit
                      [002] dN.1 1781.978303: calc load exit idle <-tick nohz idle exit
         <idle>-0
```

Function Graph Tracer

```
# echo function_graph > current_tracer
  cat trace
 tracer: function graph
#
 CPU
       DURATION
                                  FUNCTION CALLS
#
      7.879 us
                      } /* context tracking user exit */
 2)
                        do page fault() {
 2)
 2)
                        down read trylock();
      0.070 us
 2)
      0.057 us
                          _might_sleep();
 2)
      0.096 us
                        find_vma();
 2)
                        handle mm fault() {
 2)
                            do fault() {
                            filemap_fault() {
 2)
 2)
                              find_get_page() {
 2)
      0.057 us
                                __rcu_read_lock();
 2)
                                  rcu_read_unlock();
      0.061 us
 2)
      1.241 us
 2)
      0.074 us
                                _might_sleep();
 2)
      2.201 us
 2)
                            raw spin lock() {
 2)
      0.069 us
                              preempt_count_add();
 2)
      0.528 us
 2)
      0.063 us
                            add_mm_counter_fast();
 2)
      0.070 us
                            page_add_file_rmap();
 2)
                            _raw_spin_unlock() {
                              preempt_count_sub();
 2)
      0.070 us
```

Dynamic Function Tracing

```
# echo '*sched*' > set_ftrace_filter
# echo function > current_tracer
  cat trace
# tracer: function
 entries-in-buffer/entries-written: 193727/240417 #P:4
#
                              ----=> iras-off
                             / ----> need-resched
#
                            | / ---=> hardirg/softirg
#
                             || / --=> preempt-depth
#
                                      delav
                                    TIMESTAMP FUNCTION
           TASK-PID
                      CPU#
                            \Pi\Pi\Pi
         <idle>-0
                                 6325.742705: resched task <-check preempt curr
                      [003] d.h3
                      [003] dNh3
                                 6325.742712: native smp send reschedule <-engueue task fair
         <idle>-0
         <idle>-0
                      [003] dNh3
                                 6325.742714: resched_task <-check_preempt_curr
                                 6325.742719: smp reschedule interrupt <-reschedule interrupt
         <idle>-0
                      [003] dN.1
                                 6325.742720: scheduler_ipi <-smp_reschedule_interrupt
         <idle>-0
                      [003] dN.1
                      [003] dNh1 6325.742722: sched_ttwu_pending <-scheduler ipi
         <idle>-0
                      [003] .N.1 6325.742728: schedule_preempt_disabled <-cpu_startup_entry
         <idle>-0
                                 6325.742729: schedule <-schedule_preempt_disabled
         <idle>-0
                      [003] .N..
         <idle>-0
                      [003] .N..
                                 6325.742731: __schedule <-preempt_schedule
                      [003] .N.1 6325.742732: rcu_sched_qs <-rcu_note_context_switch
         <idle>-0
         <idle>-0
                      [003] dN.2 6325.742733: pre_schedule_idle <-__schedule
          aprsd-3467
                      [003] ....
                                 6325.742746: schedule <-do nanosleep
                      [003] ....
                                 6325.742747: schedule <-schedule
          aprsd-3467
                      [003] ...1 6325.742748: rcu_sched_qs <-rcu_note_context_switch
          aprsd-3467
                      [003] .... 6325.742767: schedule <-do_nanosleep
          aprsd-3454
          aprsd-3454
                      [003] ....
                                 6325.742767: schedule <-schedule
          aprsd-3454
                      [003] ...1 6325.742768: rcu sched qs <-rcu note context switch
                      [003] d..2 6325.742788: smp_reschedule_interrupt <-reschedule_interrupt
    rcu_preempt-9
    rcu_preempt-9
                      [003] d..2 6325.742789: scheduler ipi <-smp reschedule interrupt
```

How it works?

- gcc's profiler option: -pg
- Adds special mcount function call
 - all functions call mcount
 - mcount is a trampoline

A function call

```
asmlinkage __visible void __sched schedule(void)
{
    struct task_struct *tsk = current;

    sched_submit_work(tsk);
    __schedule();
}
```

A function call

Disassembled

```
<schedule>:
       55
                                push
                                       %rbp
       48 8b 04 25 80 c0 0e
                                       0xffffffff810ec080,%rax
                               mov
       81
       48 89 e5
                                       %rsp,%rbp
                               mov
       48 8b 00
                                       (%rax),%rax
                               mov
       5d
                                       %rbp
                               pop
       e9 db fa ff ff
                                       ffffffff810bb100 < schedule>
                               jmpq
                               data16 nopw %cs:0x0(%rax,%rax,1)
       66 66 2e 0f 1f 84 00
       00 00 00 00
```

A function call With -pg option

Disassembled

```
<schedule>:
       55
                                push
                                       %rbp
                                       %rsp,%rbp
       48 89 e5
                                mov
                                       ffffffff810f7430 <mcount>
       e8 37 2e 00 00
                                callq
       5d
                                       %rbp
                                pop
       48 8b 04 25 80 d0 15
                                       0xffffffff8115d080,%rax
                                mov
       81
       48 8b 00
                                       (%rax),%rax
                                mov
       e9 96 fa ff ff
                                       ffffffff810f40a0 < schedule>
                                jmpq
       66 Of 1f 44 00 00
                                       0x0(%rax, %rax, 1)
                                nopw
```

The kernel at boot up

<schedule>:

push %rbp mov %rsp,%rbp

<mcount>

callq %rbp pop

<mcount>: retq

Where's the mcounts?

- Can't just call the mcounts
 - too much overhead
 - retq only is 13% added overhead!
- Need to convert them to nops at boot up
- Need to know where they are
- Best to find them at compile time

recordmcount

- scripts/recordmcount.c (and a perl version)
- reads the object files one at a time
- reads the relocation tables
 - finds all the calls to mcount
 - creates a table
 - links the table back into the object file
 - New section called __mcount_loc

recordmcount

kernel/sched/core.o:

```
<schedule>:
       %rbp
push
       %rsp,%rbp
mov
callq
      <mcount>
pop
       %rbp
[...]
empt_schedule_irq>:
push
       %rbp
mov
       %rsp,%rbp
       %rbx
push
callq
       <mcount>
pop
       %rbp
[...]
< cond resched>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
       <mcount>
callq
       %rbp
pop
[...]
<yield>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
       <mcount>
callq
       %rbp
pop
[...]
```

```
<__mcount_loc>:
&schedule + 0x4
&preempt_schedule_irq + 0x4
&_cond_resched + 0x4
&yield + 0x4
```

recordmcount

kernel/sched/core.o:

```
<schedule>:
push
      %rbp
      %rsp,%rbp
mov
callq <mcount>
pop
      %rbp
[...]
empt_schedule_irq>:
push
      %rbp
      %rsp,%rbp
mov
      %rbx
push
callq
      <mcount>
pop
      %rbp
[...]
< cond resched>:
push
      %rbp
      %rsp,%rbp
mov
push
      %rbx
      <mcount>
callq
      %rbp
pop
[...]
<yield>:
push
      %rbp
      %rsp,%rbp
mov
push
      %rbx
      <mcount>
callq
      %rbp
pop
[...]
< mcount loc>:
&schedule + 0x4
&preempt_schedule_irq + 0x4
&_cond_resched + 0x4
&vield + 0x4
```

- vmlinux.lds
 - include/linux/vmlinux.lds.h
 - arch/x86/kernel/vmlinux.lds.S
- Magic variables
 - __start_mcount_loc
 - stop_mcount_loc

vmlinux:

kernel/sched/core.o:

```
<__mcount_loc>:
&schedule + 0x4
&preempt_schedule_irq + 0x4
&_cond_resched + 0x4
&yield + 0x4
```

mm/swap.o:

```
<__mcount_loc>:
&put_page + 0x4
&__get_page_tail + 0x4
&put_pages_list + 0x4
&get_kernel_pages + 0x4
```

fs/read_write.o:

```
<__mcount_loc>:
&new_sync_read + 0x4
&vfs_setpos + 0x4
&fixed_size_llseek + 0x4
&default_llseek + 0x4
```

vmlinux:

```
<__start_mcount_loc>:
&schedule + 0x4
&preempt_schedule_irq + 0x4
&_cond_resched + 0x4
&yield + 0x4
&put_page + 0x4
&_get_page_tail + 0x4
&put_pages_list + 0x4
&put_pages_list + 0x4
&get_kernel_pages + 0x4
&new_sync_read + 0x4
&vfs_setpos + 0x4
&fixed_size_llseek + 0x4
&default_llseek + 0x4
[...]
< end mcount loc>:
```

kernel/sched/core.o:

```
<__mcount_loc>:
   &schedule + 0x4
   &preempt_schedule_irq + 0x4
   &_cond_resched + 0x4
   &yield + 0x4
```

mm/swap.o:

```
<_mcount_loc>:
&put_page + 0x4
&__get_page_tail + 0x4
&put_pages_list + 0x4
&get_kernel_pages + 0x4
```

fs/read_write.o:

```
<__mcount_loc>:
&new_sync_read + 0x4
&vfs_setpos + 0x4
&fixed_size_llseek + 0x4
&default_llseek + 0x4
```

vmlinux:

<__start_mcount_loc>:
0xffffffff810f45f4
0xffffffff810f4635
0xffffffff810f4684
0xffffffff81087ad4
0xffffffff81087b14
0xffffffff81087bd5
0xffffffff81087c41
0xffffffff810a7aa0
0xffffffff810a7bd4
0xffffffff810a7d34
0xffffffff810a7d7d
[...]

end mcount loc>:

kernel/sched/core.o:

```
<__mcount_loc>:
  &schedule + 0x4
  &preempt_schedule_irq + 0x4
  &_cond_resched + 0x4
  &yield + 0x4
```

mm/swap.o:

```
<__mcount_loc>:
    &put_page + 0x4
    &__get_page_tail + 0x4
    &put_pages_list + 0x4
    &get_kernel_pages + 0x4
```

fs/read_write.o:

```
<__mcount_loc>:
&new_sync_read + 0x4
&vfs_setpos + 0x4
&fixed_size_llseek + 0x4
&default_llseek + 0x4
```

. .

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
callq <mcount>
       %rbp
qoq
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
callq <mcount>
       %rbp
pop
[...]
< cond resched>:
push
       %rbp
mov
       %rsp,%rbp
push
       %rbx
callq <mcount>
pop
       %rbp
[...]
<yield>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
callq
       <mcount>
       %rbp
pop
[...]
<__start_mcount_loc>:
[...]
    _end_mcount_loc>:
```

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
callq <mcount>
       %rbp
qoq
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
callq
       <mcount>
       %rbp
pop
[...]
< cond resched>:
push
       %rbp
mov
       %rsp,%rbp
push
       %rbx
callq
       <mcount>
pop
       %rbp
[...]
<yield>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
callq
       <mcount>
       %rbp
pop
[...]
<__start_mcount_loc>:
[...]
    _end_mcount_loc>:
```

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
nop
       %rbp
qoq
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
< cond resched>:
push
       %rbp
mov
       %rsp,%rbp
       %rbx
push
nop
pop
       %rbp
[...]
<yield>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
<__start_mcount_loc>:
[...]
    _end_mcount_loc>:
```

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
nop
       %rbp
qoq
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
< cond resched>:
push
       %rbp
mov
       %rsp,%rbp
       %rbx
push
nop
pop
       %rbp
[...]
<yield>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
<__start_mcount_loc>:
[...]
    _end_mcount_loc>:
```



```
<schedule>:
push
       %rbp
mov
       %rsp,%rbp
nop
       %rbp
qoq
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
< cond resched>:
push
       %rbp
mov
       %rsp,%rbp
push
       %rbx
nop
pop
       %rbp
[...]
<yield>:
push
       %rbp
       %rsp,%rbp
mov
       %rbx
push
nop
       %rbp
pop
[...]
```



What about tracing?

- Need a way to enable tracing
- We threw away the mcount section
- The mcount section wasn't enough for us
- Tracing also requires saving state

struct dyn_ftrace

struct dyn_ftrace

struct dyn_ftrace

- Copy from mcount_loc before deleting
- Sorted for quick lookup
- Allocated in groups of pages
 - details out of scope for this talk
- Data reported at boot up

```
# dmesg |grep ftrace
[ 0.139656] ftrace: allocating 24683 entries in 97 pages
```

- Allocated 24,683 dyn_ftrace structures
- Used up 97 (4K) pages to do so
- Total of 397,312 bytes

vmlinux:

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
nop
       %rbp
gog
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
<_cond_resched>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
pop
       %rbp
[...]
<vield>:
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
<__start_mcount_loc>:
[...]
    _end_mcount_loc>:
```

<ftrace_pages>

```
= 0xfffffffff81087ad4
flags = 0
      = 0xffffffff81087b14
ip
flags = 0
      = 0xffffffff81087bd5
ip
flags = 0
      = 0xfffffffff81087c41
ip
flags = 0
      = 0xffffffff810a7aa0
ip
flags = 0
      = 0xfffffffff810a7bd4
ip
flags = 0
      = 0xfffffffff810a7d34
iр
flags = 0
      = 0xfffffffff810a7d7d
ip
flags = 0
      = 0xfffffffff810f45f4
ip
flags = 0
      = 0xffffffff810f4635
ip
flags = 0
      = 0xfffffffff810f4684
ip
flags = 0
      = 0xfffffffff810f4734
flags = 0
[...]
```

<ftrace_pages>

```
= 0xfffffffff81087ad4
ip
flags = 0
      = 0xfffffffff81087b14
ip
flags = 0
      = 0xfffffffff81087bd5
flags = 0
      = 0xfffffffff81087c41
ip
flags = 0
      = 0xffffffff810a7aa0
iρ
flags = 0
      = 0xffffffff810a7bd4
flags = 0
      = 0xfffffffff810a7d34
ip
flags = 0
      = 0xfffffffff810a7d7d
ip
flags = 0
      = 0xfffffffff810f45f4
ip
flags = 0
      = 0xfffffffff810f4635
ip
flags = 0
      = 0xfffffffff810f4684
flags = 0
      = 0xfffffffff810f4734
flags = 0
[...]
```

cat available_filter_functions
put_page
__get_page_tail
put_pages_list
get_kernel_pages
new_sync_read
vfs_setpos
fixed_size_llseek
default_llseek
schedule
preempt_schedule_irq
_cond_resched
yield

<ftrace_pages>

```
= 0xffffffff81087ad4
flags = 0
      = 0xffffffff81087b14
ip
flags = 0
      = 0xffffffff81087bd5
flags = 0
      = 0xffffffff81087c41
ip
flags = 0
      = 0xfffffffff810a7aa0
ip
flags = 0
      = 0xfffffffff810a7bd4
flags = 0
      = 0xfffffffff810a7d34
ip
flags = 0
      = 0xfffffffff810a7d7d
ip
flags = 0
      = 0xfffffffff810f45f4
flags = 0
      = 0xfffffffff810f4635
flags = 0
      = 0xfffffffff810f4684
flags = 0
      = 0xfffffffff810f4734
flags = 0
[...]
```

```
# echo yield > set_ftrace_filter
# echo schedule >> set_ftrace_filter
# cat set_ftrace_filter
schedule
yield
```

dyn_ftrace.flags

- First 29 bits are for counter
 - Every registered callback increments +1
- bit 29 (starts from zero) ENABLED
- bit 30 REGS
- bit 31 REGS_EN

Enabling tracing

vmlinux:

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
nop
       %rbp
qoq
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
pop
       %rbp
[...]
<_cond resched>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
<vield>:
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
```

<ftrace_pages>

```
= 0xfffffffff81087ad4
flags = 0
      = 0xffffffff81087b14
ip
flags = 0
      = 0xffffffff81087bd5
ip
flags = 0
      = 0xfffffffff81087c41
iр
flags = 0
      = 0xffffffff810a7aa0
ip
flags = 0
      = 0xffffffff810a7bd4
ip
flags = 0
      = 0xfffffffff810a7d34
İΒ
flags = 0
      = 0xfffffffff810a7d7d
ip
flags = 0
ip
      = 0xfffffffff810f45f4
flags = 0x20000001
      = 0xffffffff810f4635
in
flags = 0
      = 0xfffffffff810f4684
iρ
flags = 0
      = 0xfffffffff810f4734
flags = 0xa0000001
[...]
```

Enabling tracing

vmlinux:

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
call ftrace caller
       %rbp
qoq
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
< cond resched>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
<vield>:
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
call ftrace regs caller
       %rbp
pop
[...]
```

<ftrace_pages>

```
= 0xfffffffff81087ad4
flags = 0
ip
      = 0xffffffff81087b14
flags = 0
      = 0xffffffff81087bd5
ip
flags = 0
      = 0xffffffff81087c41
iρ
flags = 0
      = 0xffffffff810a7aa0
ip
flags = 0
      = 0xffffffff810a7bd4
iρ
flags = 0
      = 0xfffffffff810a7d34
İΒ
flags = 0
      = 0xfffffffff810a7d7d
ip
flags = 0
ip
      = 0xfffffffff810f45f4
flags = 0x20000001
      = 0xfffffffff810f4635
in
flags = 0
      = 0xfffffffff810f4684
ip
flags = 0
      = 0xfffffffff810f4734
flags = 0xe0000001
[...]
```

Modifying code at runtime

- Not the same as at boot up
- SMP boxes must be careful
- Other CPUs may be executing that code
- x86 has non uniform machine instructions
- Instructions may cross cache boundaries

Modifying code at runtime

```
<schedule>:
       55
                                push
                                       %rbp
                                       %rsp,%rbp
       48 89 e5
                                mov
       Of 1f 44 00 00
                                nop
       5d
                                       %rbp
                                pop
       48 8b 04 25 80 d0 15
                                       0xffffffff8115d080,%rax
                                mov
       81
       48 8b 00
                                       (%rax),%rax
                                mov
                                       ffffffff810f40a0 <__schedule>
       e9 96 fa ff ff
                                jmpq
       66 Of 1f 44 00 00
                                       0x0(%rax,%rax,1)
                                nopw
```

```
<schedule>:
       55
                                push
                                       %rbp
       48 89 e5
                                mov
                                       %rsp,%rbp
       e8 37 2e 00 00
                                callq
                                       ffffffff810f7430 <ftrace caller>
       5d
                                       %rbp
                                pop
       48 8b 04 25 80 d0 15
                                       0xffffffff8115d080,%rax
                                mov
       81
       48 8b 00
                                       (%rax),%rax
                                mov
       e9 96 fa ff ff
                                       ffffffff810f40a0 < schedule>
                                jmpq
       66 Of 1f 44 00 00
                                       0x0(%rax, %rax, 1)
                                nopw
```

CPU 0

```
<schedule>:
    55
    48 89 e5
    0f 1f 44 00 00
    5d
    48 8b 04 25 80 d0 15
    81
    48 8b 00
    e9 96 fa ff ff
    66 0f 1f 44 00 00
```

CPU 1

CPU₀

```
<schedule>:
    55
    48 89 e5
    e8 37 2e 00 00
    5d
    48 8b 04 25 80 d0 15
    81
    48 8b 00
    e9 96 fa ff ff
    66 0f 1f 44 00 00
```

CPU 1

CPU₀

```
<schedule>:
    55
    48 89 e5
    e8 37 2e 00 00
    5d
    48 8b 04 25 80 d0 15
    81
    48 8b 00
    e9 96 fa ff ff
    66 0f 1f 44 00 00
```

CPU 1

Of 1f 2e 00??? Of 1f 2e 00 00

Of 1f 2e 00???

- BOOM!
- CRASH!
- GENERAL PROTECTION FAULT!
- REBOOT!

How to go from this

```
<schedule>:
       55
                                push
                                       %rbp
                                       %rsp,%rbp
       48 89 e5
                                mov
       Of 1f 44 00 00
                                nop
       5d
                                       %rbp
                                pop
       48 8b 04 25 80 d0 15
                                       0xffffffff8115d080,%rax
                                mov
       81
       48 8b 00
                                       (%rax),%rax
                                mov
       e9 96 fa ff ff
                                       ffffffff810f40a0 < schedule>
                                jmpq
       66 Of 1f 44 00 00
                                       0x0(%rax,%rax,1)
                                nopw
```

to this?

```
<schedule>:
                                       %rbp
       55
                                push
       48 89 e5
                                       %rsp,%rbp
                                mov
                               callq
       e8 37 2e 00 00
                                       ffffffff810f7430 <ftrace caller>
       5d
                                       %rbp
                                pop
       48 8b 04 25 80 d0 15
                               mov
                                       0xffffffff8115d080,%rax
       81
       48 8b 00
                                       (%rax),%rax
                                mov
                                      ffffffff810f40a0 <__schedule>
       e9 96 fa ff ff
                                jmpq
                                       0x0(%rax,%rax,1)
       66 Of 1f 44 00 00
                                nopw
```



```
<schedule>:
                                       %rbp
       55
                                push
       48 89 e5
                                       %rsp,%rbp
                                mov
       Of 1f 44 00 00
                                nop
       5d
                                       %rbp
                                pop
       48 8b 04 25 80 d0 15
                                       0xffffffff8115d080,%rax
                                mov
       81
       48 8b 00
                                       (%rax),%rax
                                mov
                                       ffffffff810f40a0 <__schedule>
       e9 96 fa ff ff
                                jmpq
                                       0x0(%rax,%rax,1)
       66 Of 1f 44 00 00
                                nopw
```

```
<schedule>:
                                       %rbp
       55
                                push
       48 89 e5
                                       %rsp,%rbp
                                mov
       cc 1f 44 00 00
                              <bp>nop
       5d
                                       %rbp
                                pop
       48 8b 04 25 80 d0 15
                                mov
                                       0xffffffff8115d080,%rax
       81
       48 8b 00
                                       (%rax),%rax
                                mov
                                       ffffffff810f40a0 <__schedule>
       e9 96 fa ff ff
                                jmpq
                                       0x0(%rax,%rax,1)
       66 Of 1f 44 00 00
                                nopw
```

```
<schedule>:
                                       %rbp
       55
                                push
                                       %rsp,%rbp
       48 89 e5
                                mov
       cc 37 2e 00 00
                              <bp>callq ffffffff810f7430 <ftrace_caller>
       5d
                                       %rbp
                                pop
       48 8b 04 25 80 d0 15
                                mov
                                       0xffffffff8115d080,%rax
       81
       48 8b 00
                                       (%rax),%rax
                                mov
       e9 96 fa ff ff
                                       ffffffff810f40a0 < schedule>
                                jmpq
       66 Of 1f 44 00 00
                                       0x0(%rax, %rax, 1)
                                nopw
```

```
<schedule>:
       55
                                push
                                       %rbp
       48 89 e5
                                       %rsp,%rbp
                                mov
       e8 37 2e 00 00
                                callq
                                       ffffffff810f7430 <ftrace caller>
       5d
                                       %rbp
                                pop
       48 8b 04 25 80 d0 15
                                       0xffffffff8115d080,%rax
                                mov
       81
       48 8b 00
                                       (%rax),%rax
                                mov
       e9 96 fa ff ff
                                       ffffffff810f40a0 < schedule>
                                jmpq
       66 Of 1f 44 00 00
                                       0x0(%rax,%rax,1)
                                nopw
```

Registering with Ftrace

- Call to register_ftrace_function()
- Requires an ftrace_ops descriptor
- Static ftrace_ops
 - function and function_graph
 - function probes (schedule:traceoff)
 - stack tracer
 - latency tracers
- Dynamic ftrace_ops
 - perf
 - kprobes

ftrace_ops

```
struct ftrace_ops {
   ftrace_func_t
                           func;
   struct ftrace_ops
                           *next;
   unsigned long
                           flags;
                           *disabled;
   int __percpu
                           *private;
   void
#ifdef CONFIG_DYNAMIC_FTRACE
                           *notrace_hash;
   struct ftrace_hash
                           *filter_hash;
   struct ftrace_hash
                           regex_lock;
   struct mutex
#endif
};
```

ftrace_ops.flags

- ENABLED
 - set by ftrace, when ops is recording
- DYNAMIC
 - set by ftrace when ops is dynamically allocated
- CONTROL
 - set by perf

ftrace_ops.flags

- SAVE_REGS
 - set by caller, to record regs
 - fails if saving regs is not supported
- SAVE_REGS_IF_SUPPORTED
 - set by caller, save regs if supported
 - doesn't fail register if not supported
- RECURSION_SAFE
 - If ftrace_ops.func handles recursion
 - Otherwise, ftrace will handle it

ftrace_ops.flags

- STUB
 - used by ftrace for stub functions
- INITIALIZED
 - used by ftrace when ftrace_ops is first used
- DELETED
 - ftrace_ops has been deleted
 - used by ftrace buffer instances

ftrace_ops hashes

- regex_lock
 - used to protect the hashes
- notrace_hash
 - what functions not to trace
 - empty means OK to trace all
- filter_hash
 - what functions to trace
 - empty means to trace all
- Functions in notrace_hash will not be traced even if they exist in filter_hash

ftrace_caller trampoline

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
call ftrace caller
qoq
       %rbp
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
< cond resched>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
pop
       %rbp
[...]
<vield>:
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
call ftrace regs caller
       %rbp
pop
[...]
```

```
ftrace_caller:
   save regs
   load args
   ftrace_call:
   call ftrace_stub
   restore regs
   ftrace_stub:
   retq
```

ftrace_caller trampoline

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
call ftrace caller
       %rbp
gog
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
<_cond resched>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
<vield>:
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
call ftrace regs caller
       %rbp
pop
[...]
```

```
ftrace_caller:
   save regs
   load args
   ftrace_call:
   call func_trace
   restore regs
   ftrace_stub:
   retq
```

```
void func_trace()
{
    /* trace */
}
```

ftrace_caller trampoline

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
call ftrace caller
       %rbp
gog
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
<_cond resched>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
<vield>:
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
call ftrace regs caller
pop
       %rbp
[...]
```

```
ftrace_caller:
   save regs
   load args
   ftrace_call:
   call func_trace
   restore regs
   ftrace_stub:
   retq
```

ftrace_ops.func

```
void func_trace()
{
    /* trace */
}
```

Multiple callbacks?

- Direct call works fine
- Multiple calls requires a list operation
- All functions being traced will call the list function

Multiple callbacks?

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
call ftrace caller
       %rbp
qoq
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
< cond resched>:
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
<vield>:
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
call ftrace_regs_caller
pop
       %rbp
[...]
```

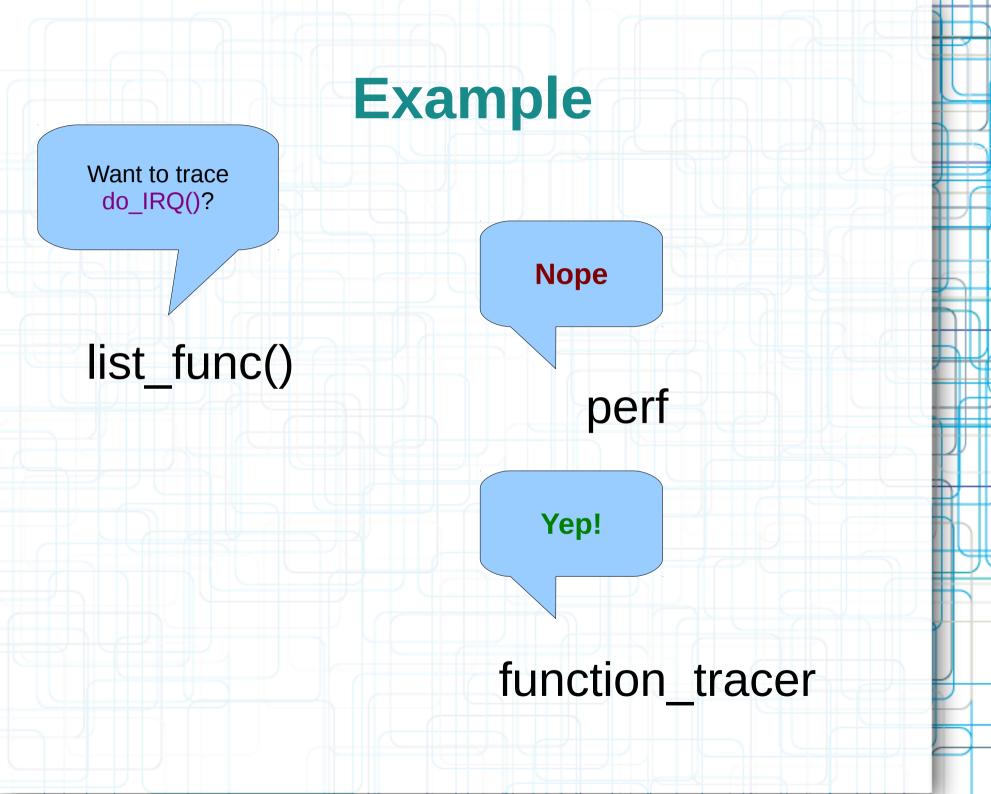
```
ftrace caller:
 save regs
 load args
ftrace call:
 call list func
 restore regs
                          void list_func()
ftrace_stub:
 retq
                               /* iterate */
```

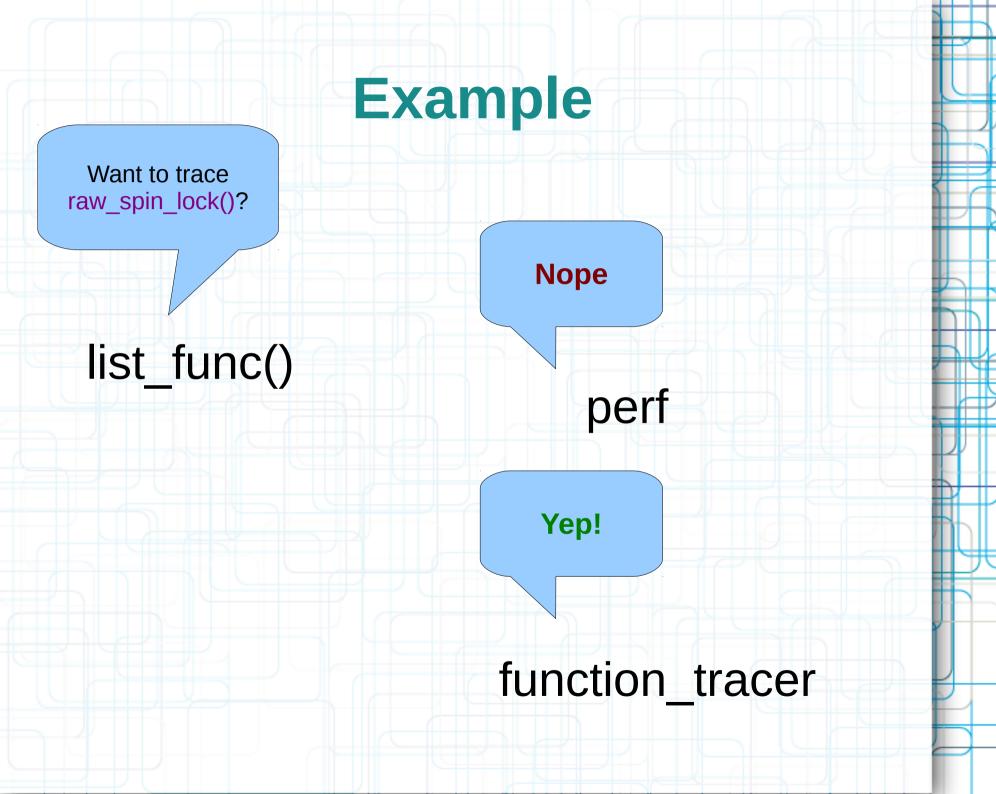
```
void func1_trace()
{
    /* trace */
}
```

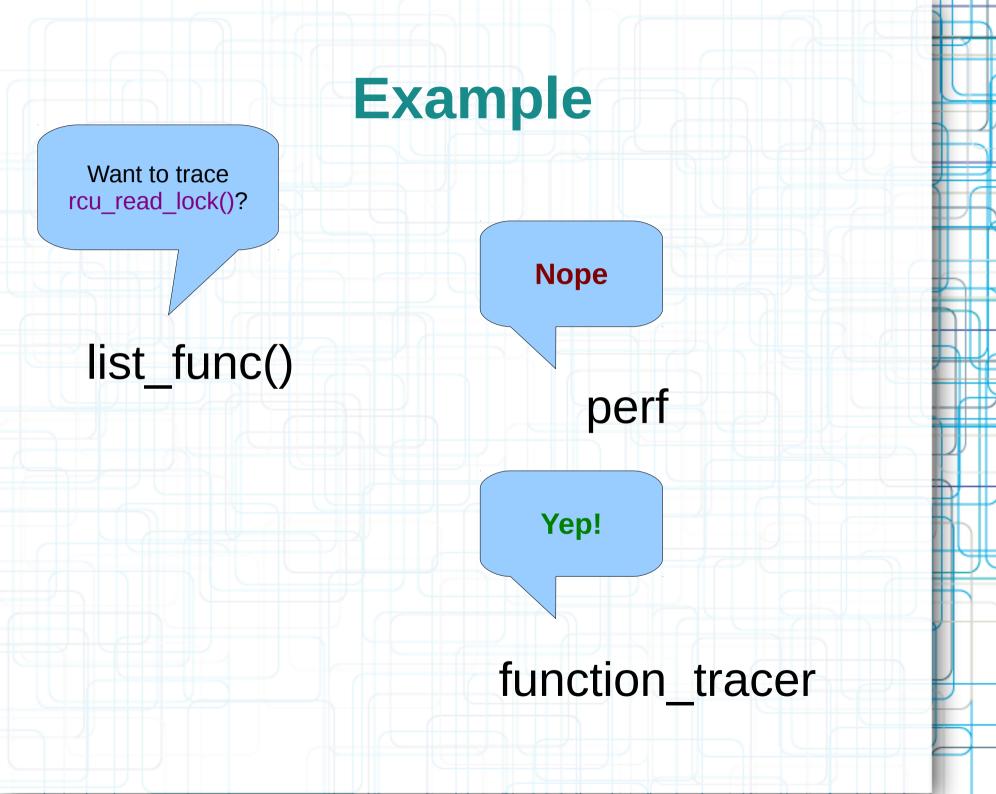
```
void func2_trace()
{
    /* trace */
}
```

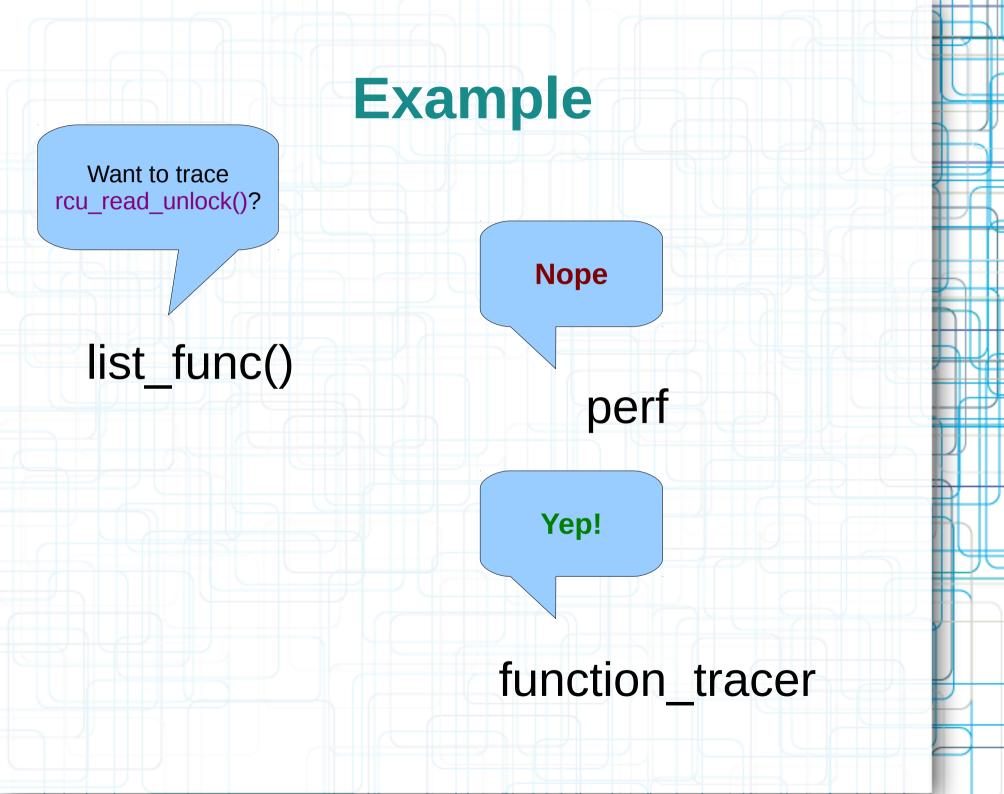
Example

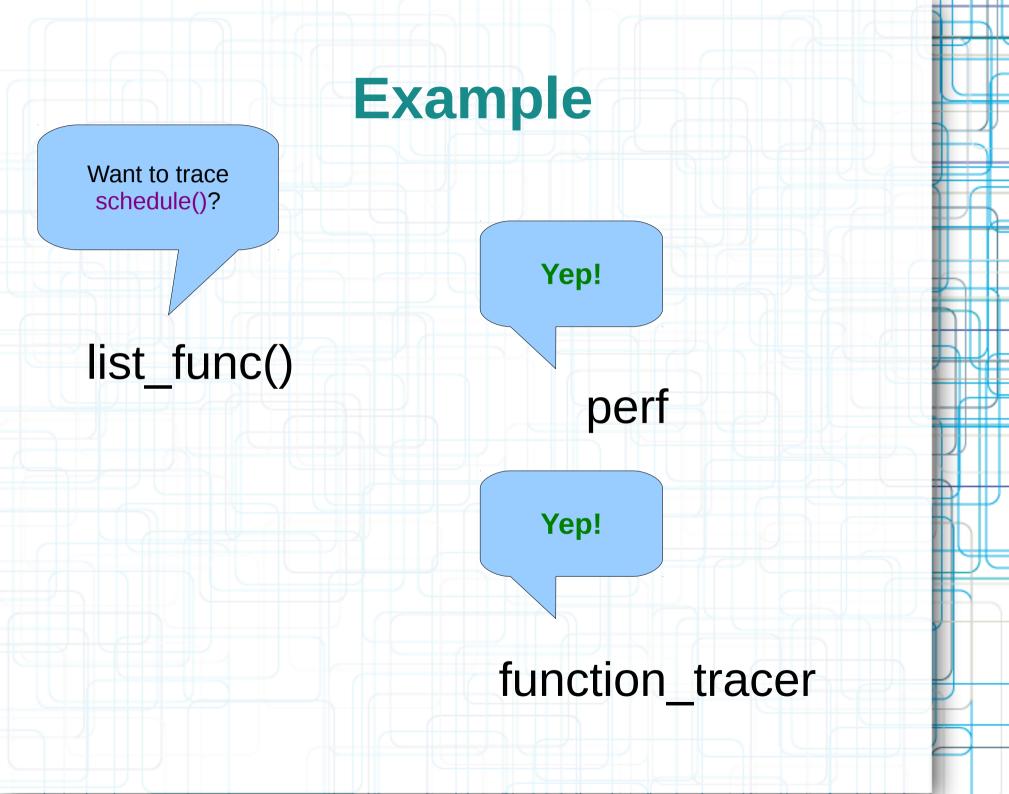
- Run function tracer on all functions
- Run perf on just the scheduler











Multiple callbacks?

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
call ftrace caller
       %rbp
gog
cpreempt_schedule_irq>:
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
call ftrace caller
       %rbp
pop
[...]
< cond resched>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
call ftrace caller
pop
       %rbp
[...]
<vield>:
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
call ftrace caller
pop
       %rbp
[...]
```

```
ftrace_caller:
   save regs
   load args
   ftrace_call:
   call list_func-
   restore regs
   ftrace_stub:
   retq
```

```
void function_trace()
{
    /* function
        tracing */
}
```

```
void list_func()
{
    /* iterate and
    check hash of ops */
}
```

```
void perf_func()
{
    /* perf
    profiling */
}
```

Multiple callbacks?

```
void list_func()
<schedule>:
                                   ftrace caller:
push
      %rbp
                                    save regs
                                                                        /* iterate and
      %rsp,%rbp
mov
                                    load args
                                                                      check hash of ops */
call ftrace caller
      %rbp
qoq
                                   ftrace call:
                                    call list func
cpreempt_schedule_irq>:
                                    restore regs
push
      %rbp
                                   ftrace stub:
      %rsp,%rbp
mov
push
      %rbx
                                    reta
call ftrace caller
      %rbp
gog
[...]
< cond resched>:
                                                void function_trace()
push
      %rbp
                                                                                   void perf_func()
      %rsp,%rbp
mov
                                                      /* function
push
      %rbx
                                                                                         /* perf
call ftrace caller
                                                           tracing */
                                                                                         profiling */
pop
      %rbp
[...]
<vield>:
      %rbp
push
                                 dynamic_trampoline:
      %rsp,%rbp
mov
                                  save regs
push
      %rbx
call ftrace caller
                                  load args
pop
      %rbp
                                  call function trace
[...]
                                  restore regs
                                  reta
```

Problems with Dynamic Trampolines

- When can you free them?
- When are they not in use?
- Would RCU help?

Dynamic trampolines

Task

dynamic_trampoline:

save regs
load args
call function_trace
restore regs
retq

Dynamic trampolines



Preempted!

dynamic_trampoline:
 save regs
 load args
 call function_trace
 restore regs
 retq

What about dynamic ftrace_ops

- Remember, there's a difference between dynamic and static ftrace_ops
- Ftrace detects ftrace_ops that are dymanic
- Always uses the list function
 - it disables preemption
 - and is static

Dynamic ftrace_ops

```
<schedule>:
push
       %rbp
       %rsp,%rbp
mov
call ftrace caller
       %rbp
gog
cpreempt_schedule_irq>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
< cond resched>:
push
       %rbp
       %rsp,%rbp
mov
push
       %rbx
nop
       %rbp
pop
[...]
<vield>:
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
call ftrace regs caller
pop
       %rbp
[...]
```

```
ftrace_caller:
   save regs
   load args
   ftrace_call:
   call list_func
   restore regs
   ftrace_stub:
   retq
```

```
void list_func()
{
  preempt_disable_notrace();
    /* iterate */
  preempt_enable_notrace();
}
```

```
void dynamic_ops_func()
{
    /* trace */
}
```

Knowing when to free

- If there was a way to know no more tasks were on the trampoline or function
- There will be a way in coming 3.18

call_rcu_tasks()

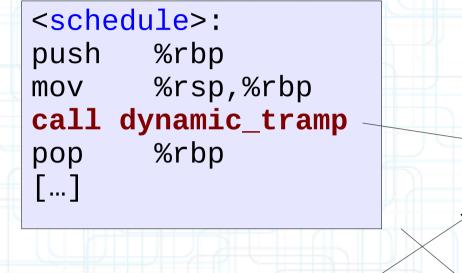
- Call a function after all tasks
 - have voluntarily scheduled
 - in userspace
 - are idle

```
<schedule>:
push %rbp
mov %rsp,%rbp
call dynamic_tramp
pop %rbp
[...]
```

Task

dynamic_trampoline:

save regs
load args
call function_trace
restore regs
retq



Task Preempted!

dynamic_trampoline:
 save regs
 load args
 call function_trace
 restore regs
 retq

```
<schedule>:
push %rbp
mov %rsp,%rbp
call ftrace_stub
pop %rbp
[...]
```

Task

Preempted!

dynamic_trampoline:

save regs
load args
call function_trace
restore regs
retq

```
<schedule>:
push %rbp
mov %rsp,%rbp
call ftrace_stub
pop %rbp
[...]
```

Task Preempted!

dynamic_trampoline:
 save regs
 load args
 call function_trace
 restore regs
 retq

call_rcu_task()

```
<schedule>:
push %rbp
mov %rsp,%rbp
call ftrace_stub
pop %rbp
[...]
```

Task

- · Voluntary schedule
- · In idle
- · In userspace

```
dynamic_trampoline:
   save regs
  load args
  call function_trace
  restore regs
  retq
```

call_rcu_task()

```
<schedule>:
push %rbp
mov %rsp,%rbp
call ftrace_stub
pop %rbp
[...]
```

Task

- · Voluntary schedule
- · In idle
- · In userspace

dynamic_trampoline:
 save regs
 load args
 call function_trace
 restore regs
 retq

- mcount can't record parameters
- New feature of gcc
 - starting with gcc 4.6.0
 - Added by Andi Kleen
 - for x86_64 only (for now)
- gcc -pg -mfentry

```
<schedule>:
       55
                                push
                                       %rbp
       48 89 e5
                                       %rsp,%rbp
                                mov
                                       ffffffff810f7430 <mcount>
       e8 37 2e 00 00
                                callq
       5d
                                       %rbp
                                pop
                                       0xffffffff8115d080,%rax
       48 8b 04 25 80 d0 15
                                mov
       81
       48 8b 00
                                       (%rax),%rax
                                mov
                                       ffffffff810f40a0 <__schedule>
       e9 96 fa ff ff
                                jmpq
                                       0x0(%rax,%rax,1)
       66 Of 1f 44 00 00
                                nopw
```

```
<posix_cpu_timer_set>:
                                        %rbp
       55
                                 push
       48 89 e5
                                        %rsp,%rbp
                                mov
                                        %r15
       41 57
                                 push
                                        %r14
       41 56
                                 push
                                        %r13
       41 55
                                 push
                                        %r12
       41 54
                                 push
       53
                                 push
                                        %rbx
       48 83 ec 30
                                 sub
                                        $0x30,%rsp
       e8 1a 81 0b 00
                                callq
                                        ffffffff810f7430 <mcount>
       48 8b 47 70
                                        0x70(%rdi),%rax
                                mov
       49 89 ff
                                        %rdi,%r15
                                mov
```

```
<posix_cpu_timer_set>:
                                        ffffffff810f0af0 <__fentry__>
       e8 eb 3c 0b 00
                                callq
       41 57
                                push
                                        %r15
                                push
                                        %r14
       41 56
       49 89 ff
                                        %rdi,%r15
                                mov
       41 55
                                push
                                        %r13
                                        %r12
       41 54
                                push
       49 89 d5
                                        %rdx,%r13
                                mov
       55
                                push
                                        %rbp
       53
                                push
                                        %rbx
```

```
<posix_cpu_timer_set>:
call ftrace_caller
push %r15
push %r14
mov %rdi,%r15
push %r13
push %r12
```

```
ftrace_caller:
    save regs
    load args
    ftrace_call:
    call func_trace
    restore regs
    ftrace_stub:
    retq
```

```
void func_trace()
{
    /* trace */
}
```

Live Kernel Patching!

```
<posix_cpu_timer_set>:
call ftrace_caller
push
       %r15
push
       %r14
       %rdi,%r15
mov
       %r13
push
       %r12
push
 <posix_cpu_timer_set>:
 nop
        %r15
 push
 push
        %r14
        %rdi,%r15
 mov
 push
        %r13
```

%r12

push

```
ftrace_caller:
    save regs
    load args
    ftrace_call:
    call func_trace
    restore regs
    ftrace_stub:
    retq
```

```
void lkp()
{
    /* change
    return reg */
}
```

vmlinux:

```
<schedule>:
nop
push
       %rbp
       %rsp,%rbp
mov
       %rbp
gog
cpreempt_schedule_irq>:
nop
       %rbp
push
       %rsp,%rbp
mov
       %rbx
push
       %rbp
pop
[...]
< cond resched>:
nop
       %rbp
push
       %rsp,%rbp
mov
       %rbx
push
       %rbp
pop
[...]
<yield>:
nop
push
       %rbp
mov
       %rsp,%rbp
       %rbx
push
       %rbp
pop
[...]
```

```
= 0xfffffffff81087ad4
iр
flags = 0
iρ
      = 0xfffffffff81087b14
flags = 0
      = 0xffffffff81087bd5
flags = 0
      = 0xfffffffff81087c41
ip
flags = 0
      = 0xfffffffff810a7aa0
ip
flags = 0
      = 0xffffffff810a7bd4
ip
flags = 0
      = 0xfffffffff810a7d34
ip
flags = 0
      = 0xfffffffff810a7d7d
ip
flags = 0
      = 0xfffffffff810f45f4
ip
flags = 0
      = 0xfffffffff810f4635
iρ
flags = 0
      = 0xfffffffff810f4684
ip
flags = 0
      = 0xfffffffff810f4734
ip
flags = 0
[...]
```

vmlinux:

```
<schedule>:
nop
push
       %rbp
       %rsp,%rbp
mov
       %rbp
gog
cpreempt_schedule_irq>:
nop
       %rbp
push
       %rsp,%rbp
mov
       %rbx
push
       %rbp
pop
[...]
< cond resched>:
nop
       %rbp
push
       %rsp,%rbp
mov
       %rbx
push
       %rbp
pop
[...]
<vield>:
nop
       %rbp
push
       %rsp,%rbp
mov
       %rbx
push
       %rbp
pop
[...]
```

```
<preempt_schedule_irq>:
nop
push %r15
push %r14
mov %rdi,%r15
push %r13
push %r12
```

```
= 0xfffffffff81087ad4
iр
flags = 0
iρ
      = 0xfffffffff81087b14
flags = 0
      = 0xffffffff81087bd5
flags = 0
      = 0xffffffff81087c41
ip
flags = 0
      = 0xfffffffff810a7aa0
ip
flags = 0
      = 0xffffffff810a7bd4
ip
flags = 0
      = 0xfffffffff810a7d34
ip
flags = 0
      = 0xfffffffff810a7d7d
ip
flags = 0
      = 0xfffffffff810f45f4
ip
flags = 0
      = 0xffffffff810f4635
iρ
flags = 0
      = 0xffffffff810f4684
ip
flags = 0
      = 0xfffffffff810f4734
ip
flags = 0
[...]
```

vmlinux:

```
<schedule>:
nop
push
       %rbp
       %rsp,%rbp
mov
       %rbp
gog
cpreempt_schedule_irq>:
nop
push
       %rbp
       %rsp,%rbp
mov
       %rbx
push
       %rbp
pop
[...]
< cond resched>:
nop
       %rbp
push
       %rsp,%rbp
mov
       %rbx
push
       %rbp
pop
[...]
<vield>:
nop
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
       %rbp
pop
[...]
```

```
<preempt_schedule_irq>:
nop
push %r15
push %r14
mov %rdi,%r15
push %r13
push %r12
```

```
= 0xfffffffff81087ad4
iр
flags = 0
iρ
      = 0xfffffffff81087b14
flags = 0
      = 0xffffffff81087bd5
flags = 0
      = 0xffffffffa0014466
ip
flags = 0
      = 0xfffffffff810a7aa0
ip
flags = 0
      = 0xffffffff810a7bd4
ip
flags = 0
      = 0xfffffffff810a7d34
ip
flags = 0
      = 0xfffffffff810a7d7d
ip
flags = 0
      = 0xfffffffff810f45f4
ip
flags = 0
      = 0xffffffff810f4635
ip
flags = 0
      = 0xffffffff810f4684
ip
flags = 0
      = 0xfffffffff810f4734
ip
flags = 0
[...]
```

vmlinux:

```
<schedule>:
nop
push
       %rbp
       %rsp,%rbp
mov
       %rbp
gog
cpreempt_schedule_irq>:
jmp preempt_sched2
push
       %rbp
       %rsp,%rbp
mov
       %rbx
push
       %rbp
pop
[...]
< cond resched>:
nop
       %rbp
push
       %rsp,%rbp
mov
       %rbx
push
       %rbp
pop
[...]
<vield>:
nop
       %rbp
push
       %rsp,%rbp
mov
push
       %rbx
       %rbp
pop
[...]
```

```
<preempt_schedule_irq>:
nop
push %r15
push %r14
mov %rdi,%r15
push %r13
push %r12
```

```
= 0xfffffffff81087ad4
iр
flags = 0
iρ
      = 0xfffffffff81087b14
flags = 0
      = 0xffffffff81087bd5
flags = 0
      = 0xffffffffa0014466
ip
flags = 0
      = 0xfffffffff810a7aa0
ip
flags = 0
      = 0xffffffff810a7bd4
ip
flags = 0
      = 0xfffffffff810a7d34
ip
flags = 0
      = 0xfffffffff810a7d7d
ip
flags = 0
      = 0xfffffffff810f45f4
ip
flags = 0
      = 0xffffffff810f4635
iρ
flags = 0
      = 0xffffffff810f4684
ip
flags = 0
      = 0xfffffffff810f4734
ip
flags = 0
[...]
```

Instead of this

```
<posix_cpu_timer_set>:
                              ftrace_caller:
call ftrace_caller
                                 save regs
push
       %r15
                                 load args
push
       %r14
                                ftrace_call:
       %rdi,%r15
mov
       %r13
push
       %r12
push
                                ftrace stub:
                                 retq
 <posix_cpu_timer_set>:
 nop
        %r15
 push
        %r14
 push
        %rdi,%r15
 mov
 push
        %r13
        %r12
 push
```

```
call func_trace
restore regs
trace_stub:
retq

void lkp()
{
    /* change
    return reg */
}
```

Have this!

```
<posix_cpu_timer_set>:
jmp posix_cpu2
push %r15
push %r14
mov %rdi,%r15
push %r13
push %r12
```

```
<posix_cpu_timer_set>:
call ftrace_caller
push %r15
push %r14
mov %rdi,%r15
push %r13
push %r12
```

ftrace_caller:
 save regs
 load args
 ftrace_call:
 call func_trace
 restore regs
 ftrace_stub:
 retq

