SAM3U-EK RevB Block Diagram ATMEL Cortex M3 Processor SAM3U (LQFP144) Reset, Debug Logic **Power Manage Audio DAC SD/MMC Card PSRAM Accelerometer Nand Flash Temperature sense** 2.8 Inch TFT-LCD **USB & UART User Interface (PIO PortA,B,C)** B ZXL 19-MAY-09 PP 20-JAN-10 A INIT EDIT ZXL 03-FEB-09 XXX XX-XXX-XX REV MODIF. DES. DATE VER. DATE ROUSSET SAM3U-EK RevB SCALE . 1/1 SHEET

1. SAM3U-EK Revision HISTORY

Rev: A

SCH: SAM3U-EK RevA Data: 2008/12

Note: Original Released

Rev: B

SCH: SAM3U-EK RevB Data: 2009/04

Note: Final Released

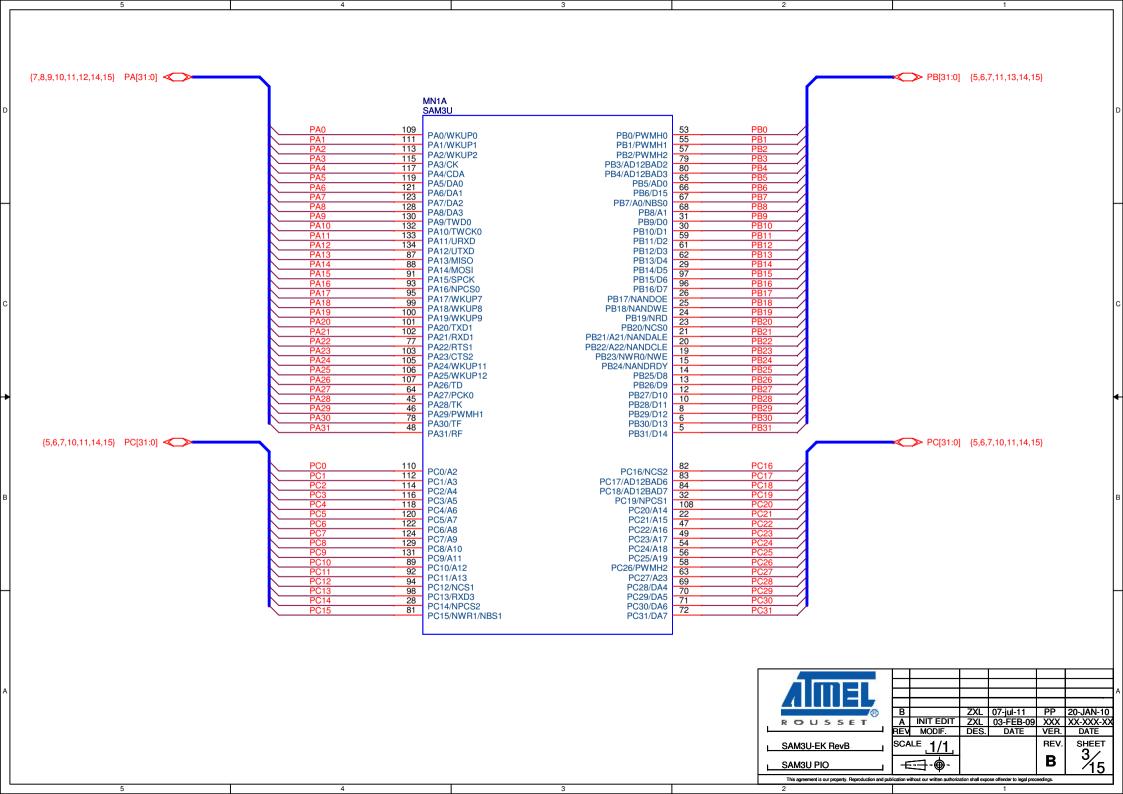
2. Explain of Schematics

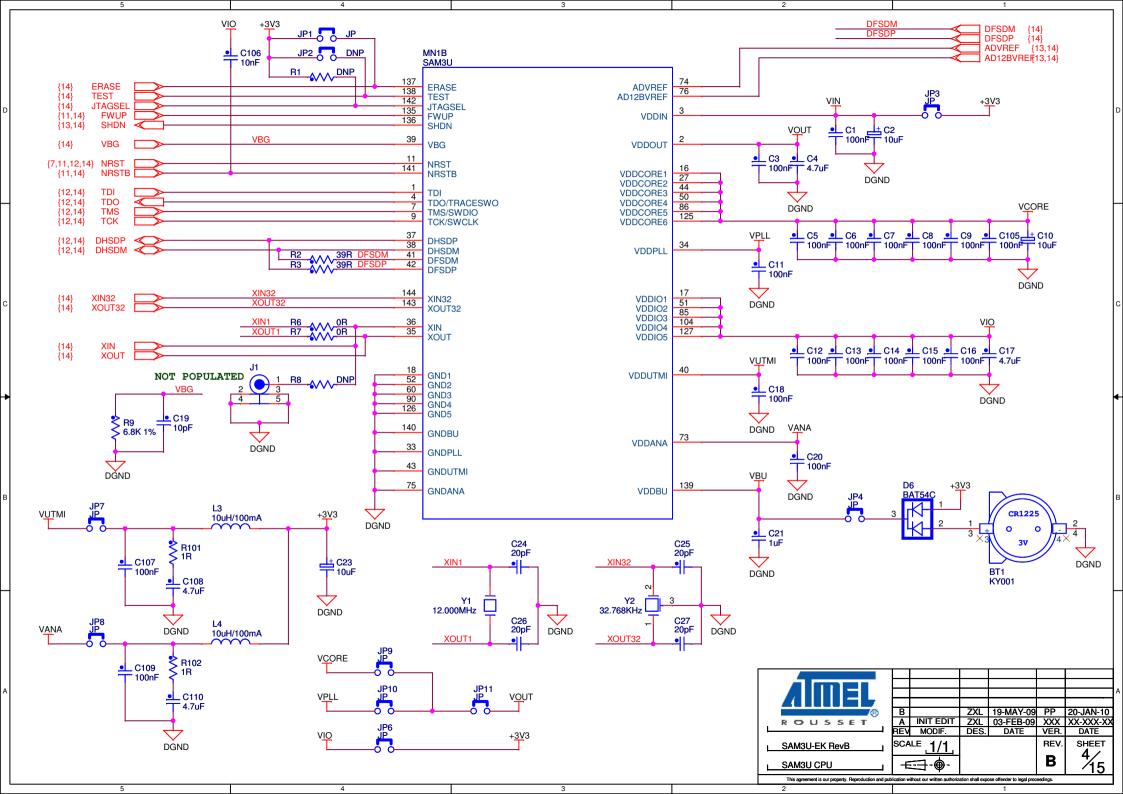
(1) Resistance Unit: "K" is

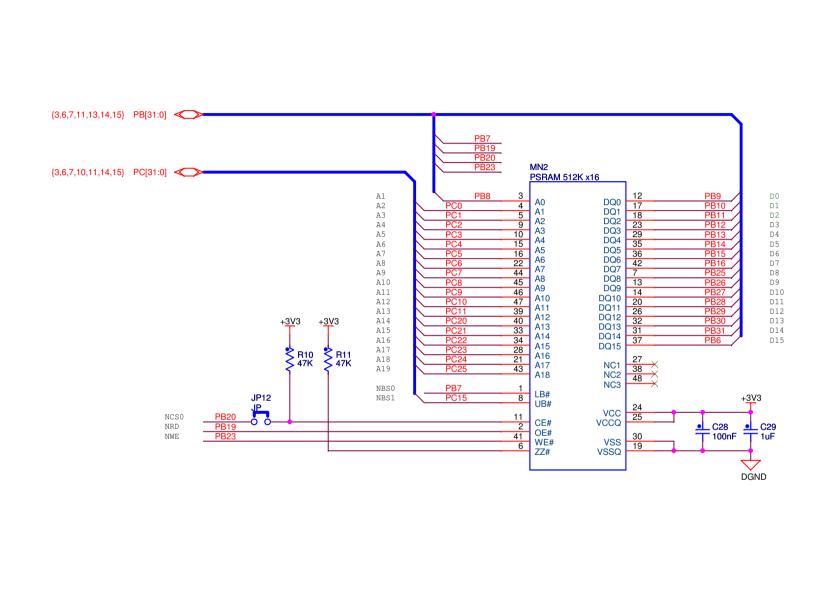
"K| , "

(2) "DNP" means the component is not populated by default

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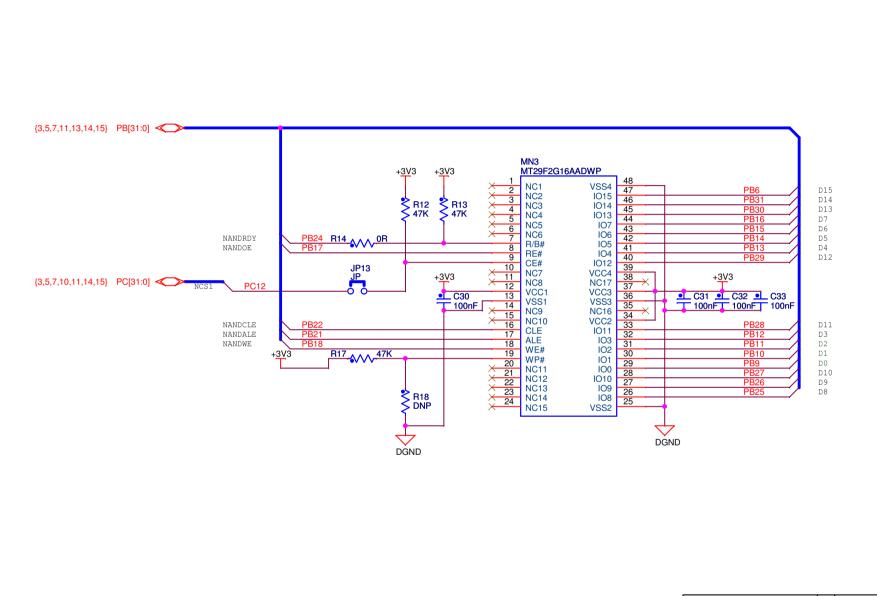




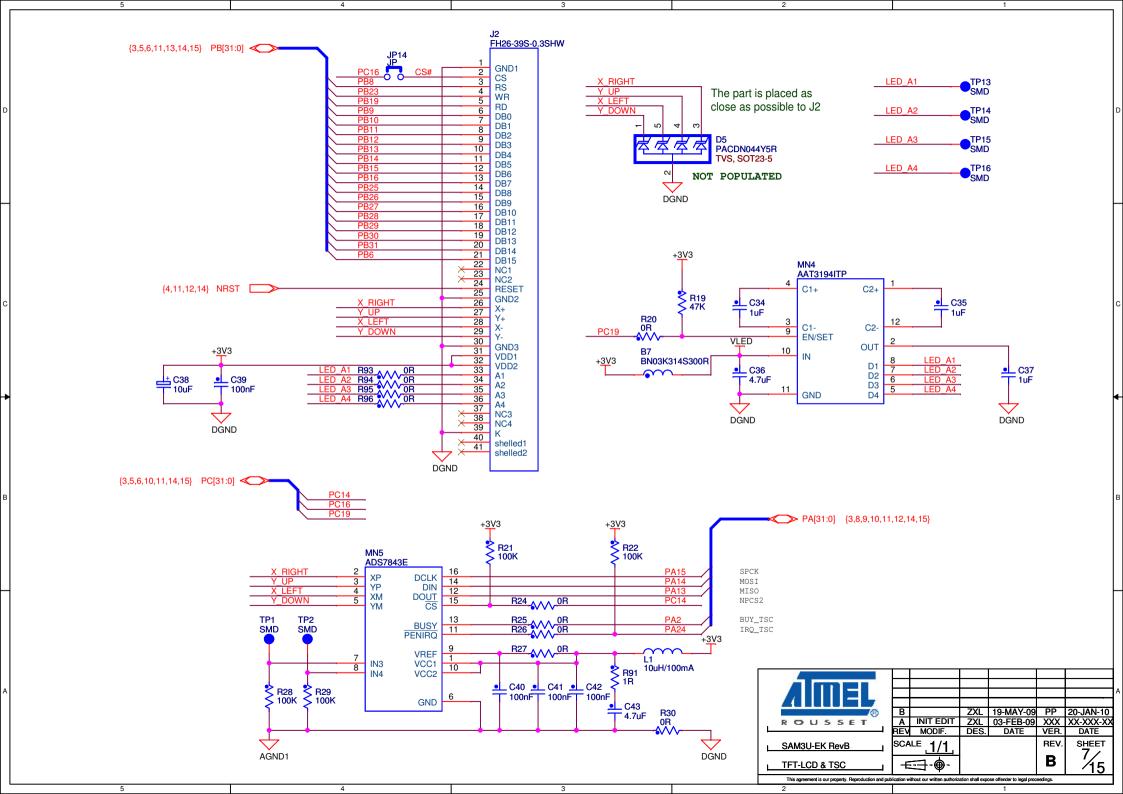


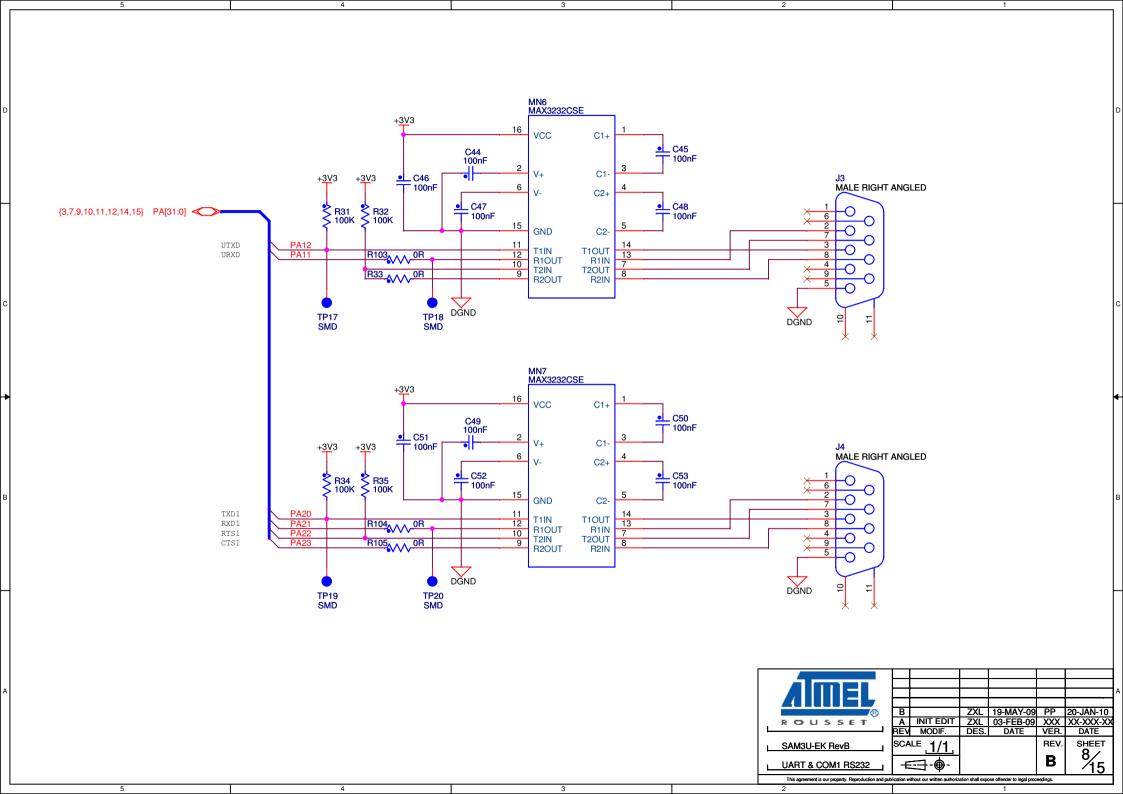
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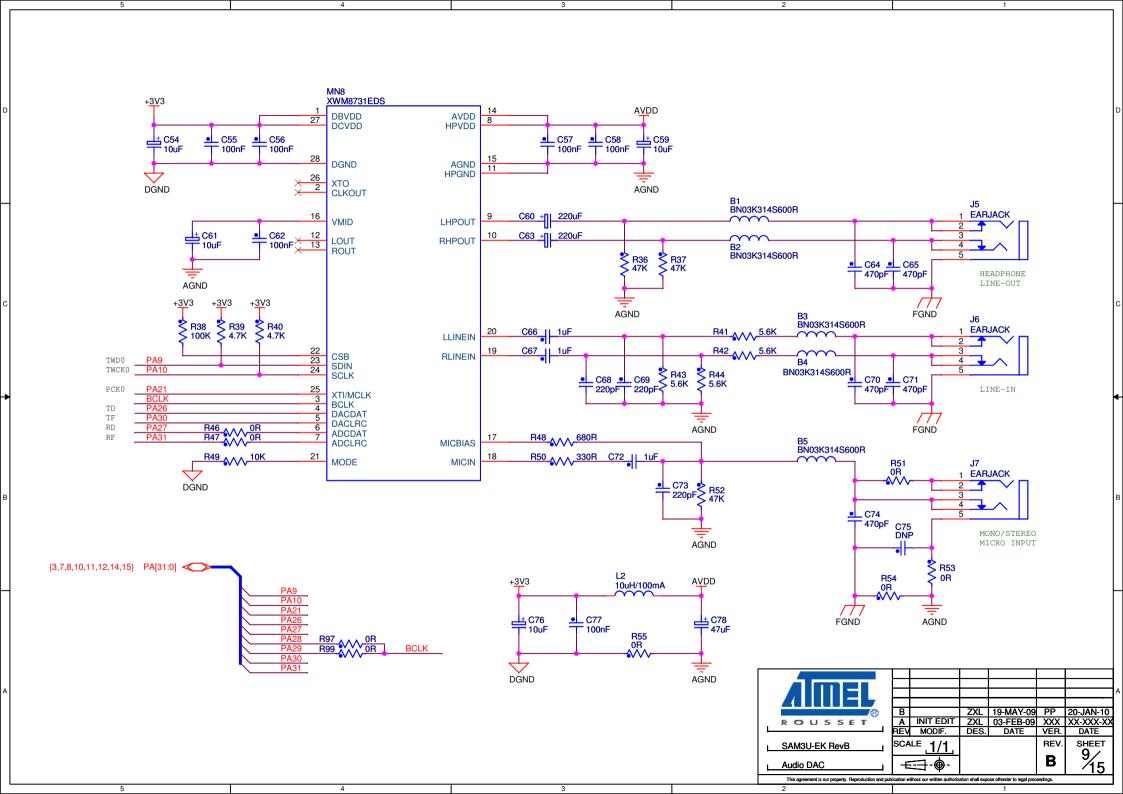
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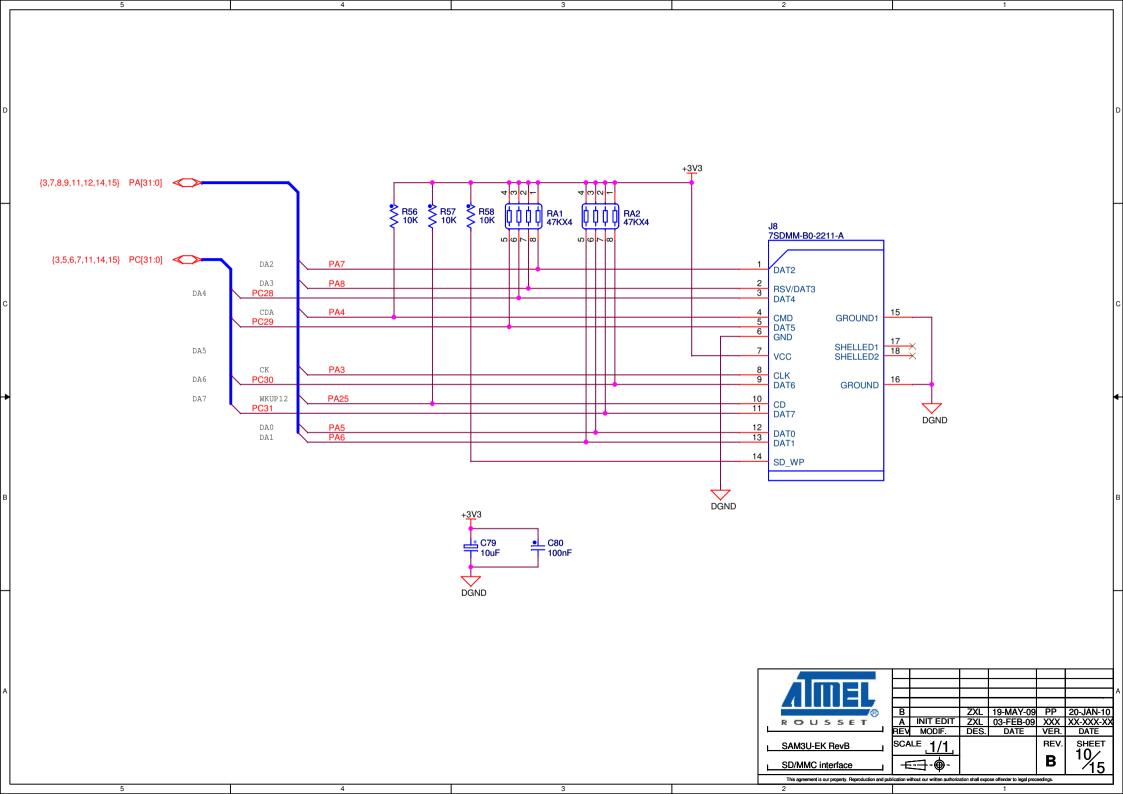


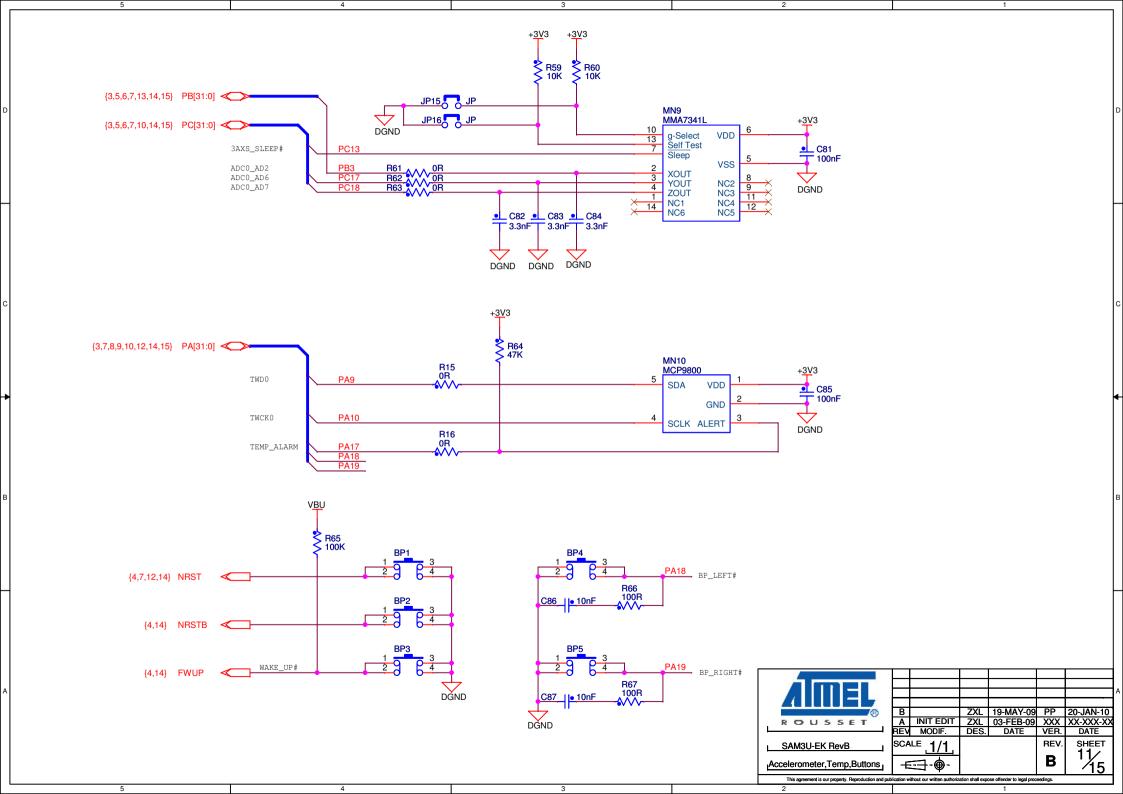
B ZXL 19-MAY-09 PP 20-JAN-10
A INIT EDIT ZXL 03-FEB-09 XXX XX-XXX-XX
REV MODIF. DES. DATE VER. DATE ROUSSET REV MODIF. SHEET 6/ 15 SCALE , 1/1 REV. SAM3U-EK RevB EBI Memory II NAND FLASH This agreement is our property. Reproduction and publication without our written authorization shall expose offender to legal proceedings

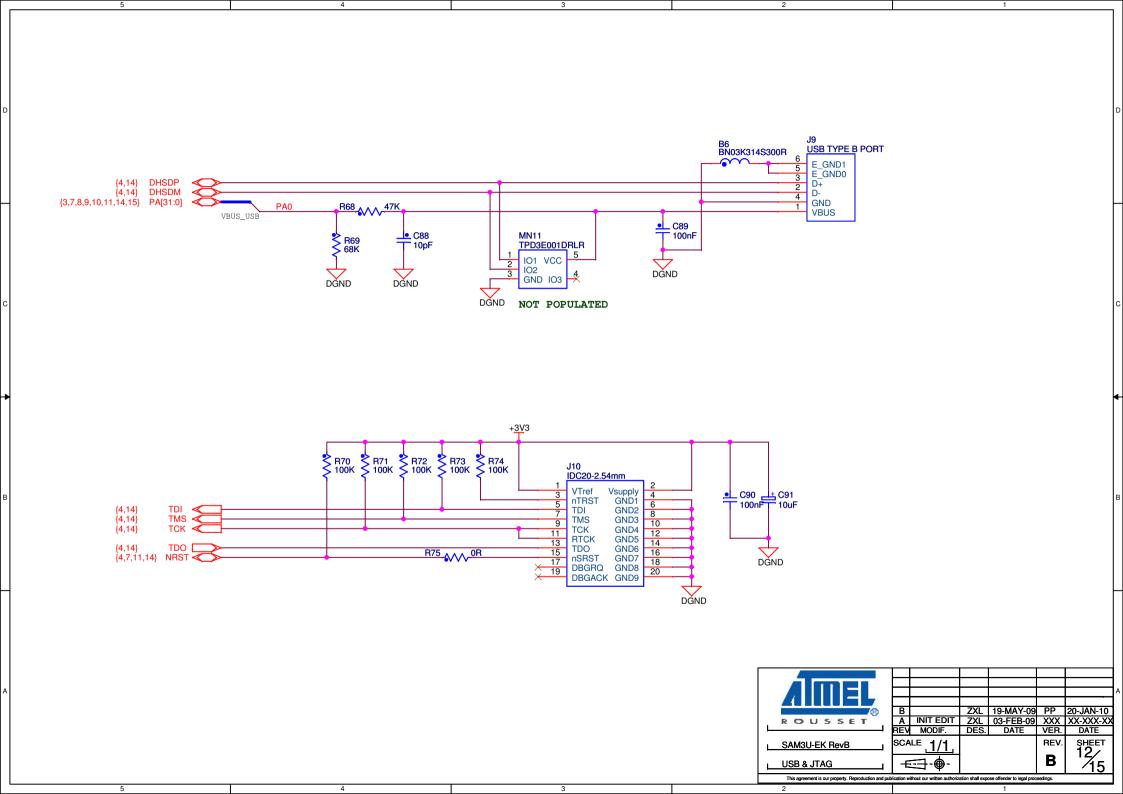


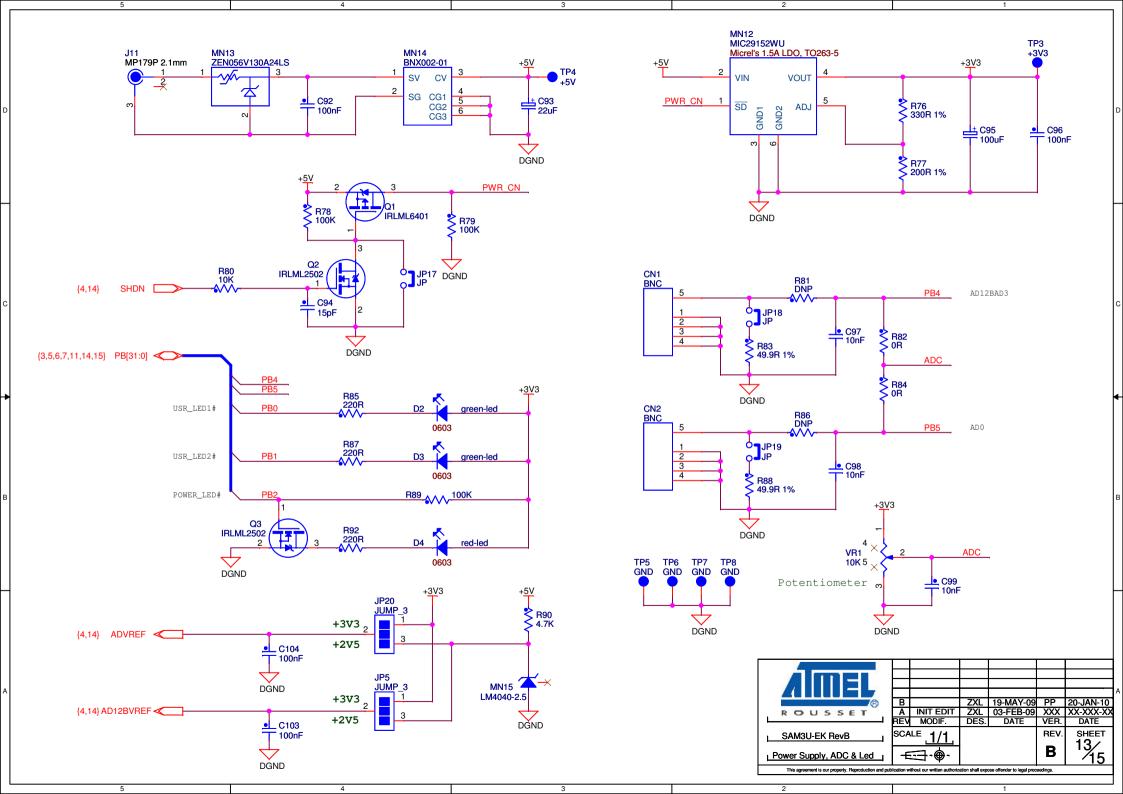


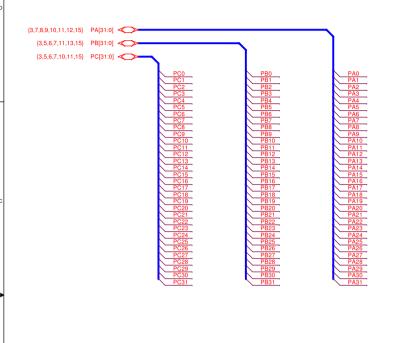


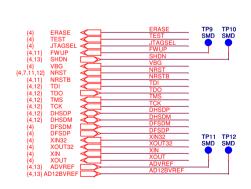


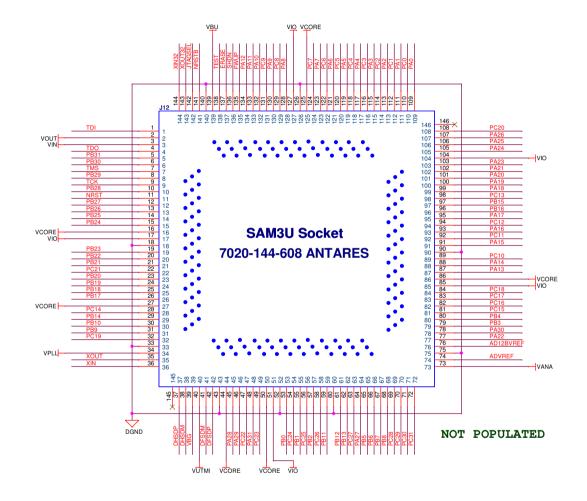












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