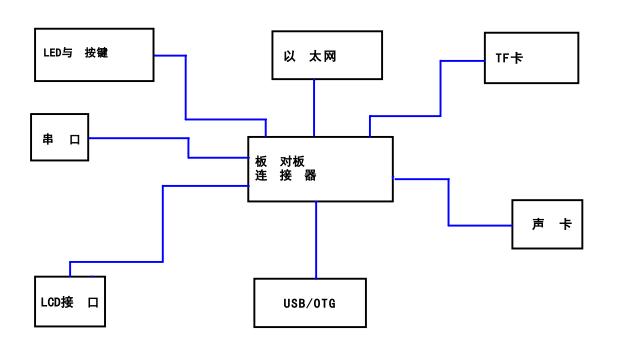
BLOCK DIAGRAM

REVISION HISTORY

Revision	Description	Date	Design	Checked
V00	REALSE VERSION	2018-08-14	frank	thisway_diy



Schematics Index:

P01: REVISION HISTORY

P02: CONNECTOR

P03: POWER

P04: LED KEY

P05: UART SD

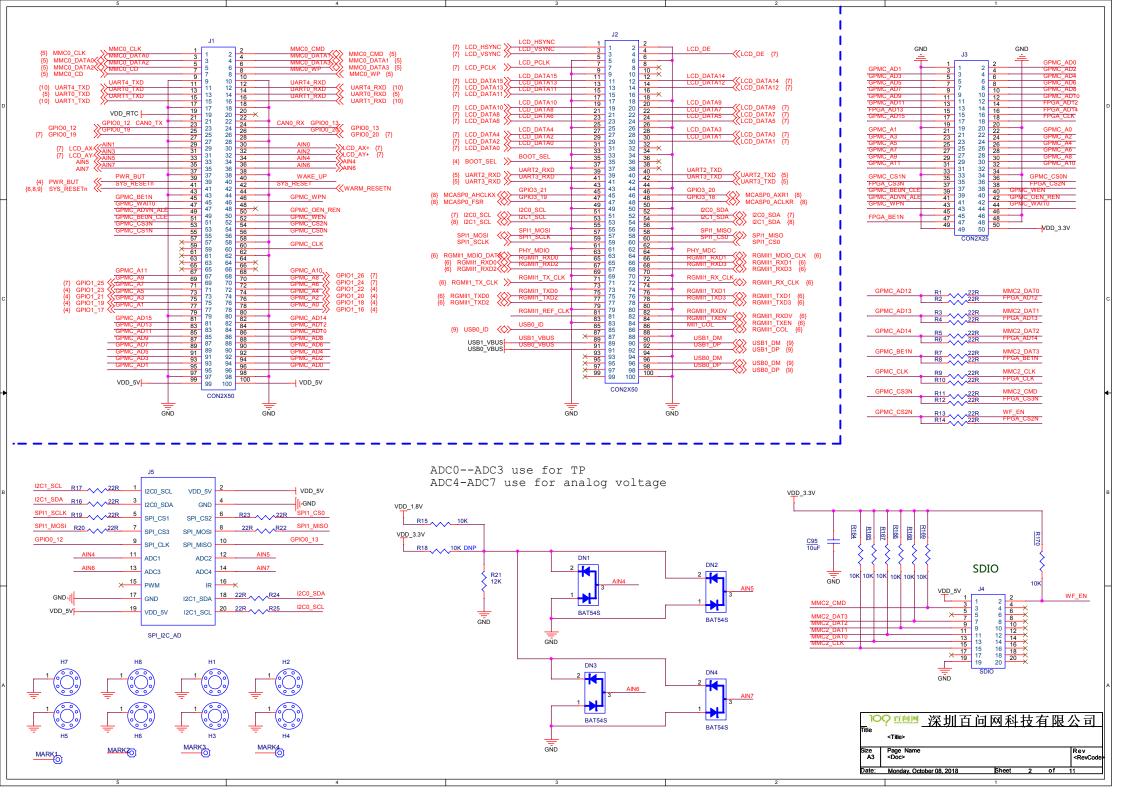
P06: NET

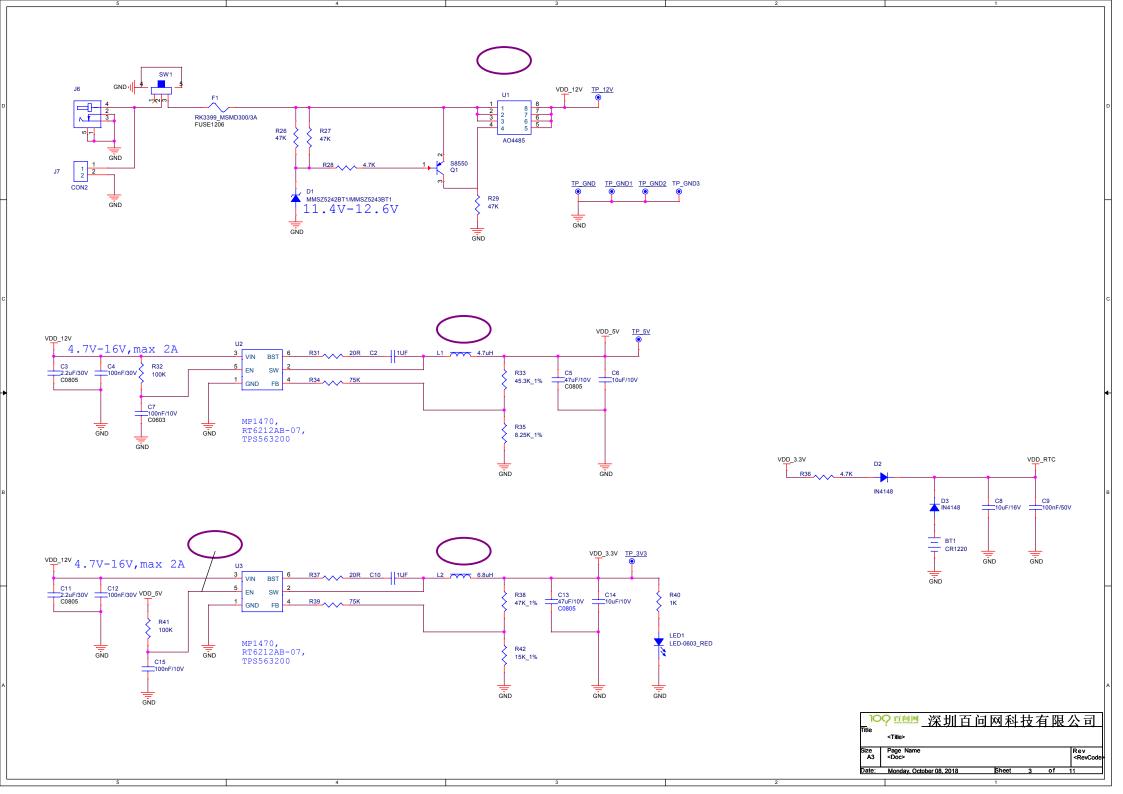
P07: LCD

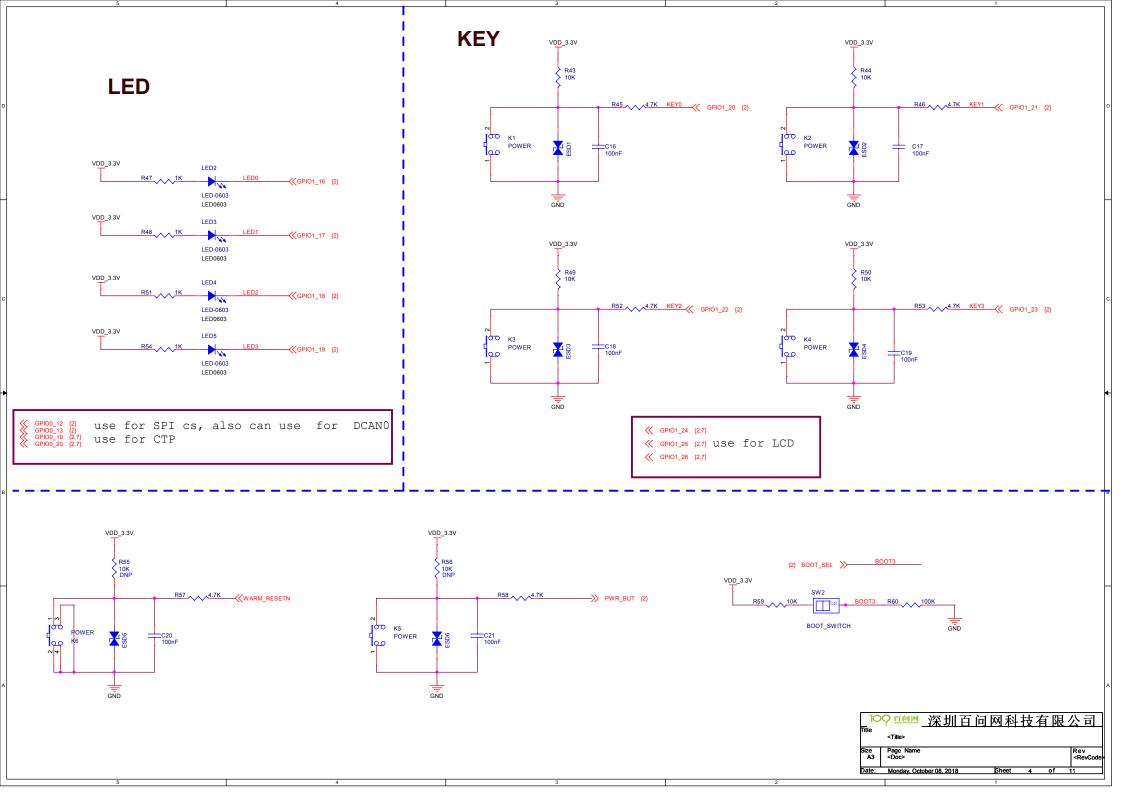
P08: AUDIO

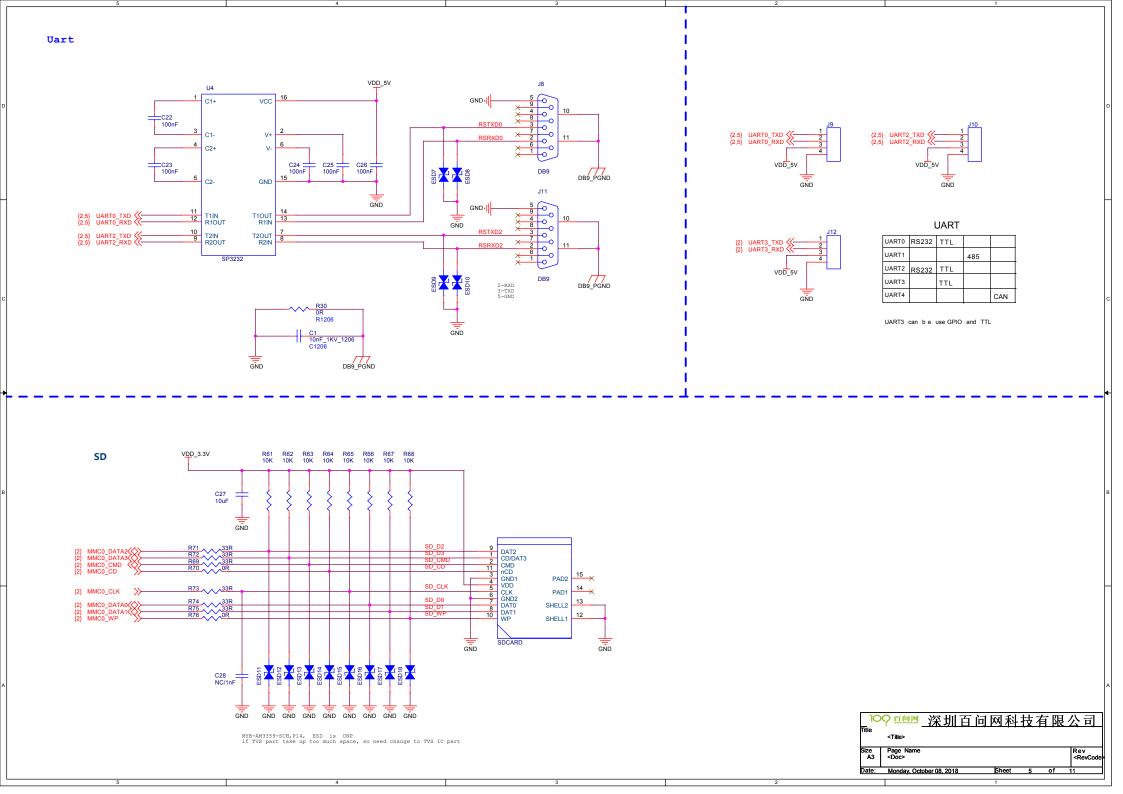
P09: USB

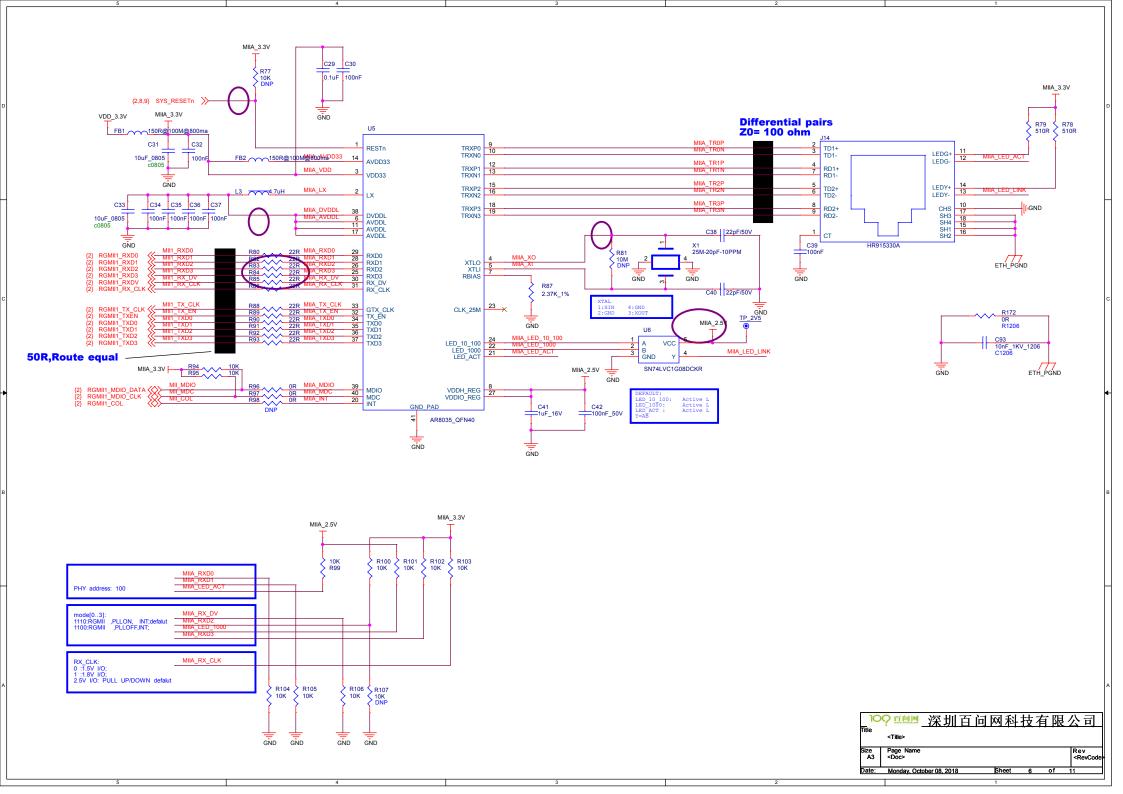
P10: CAN 485

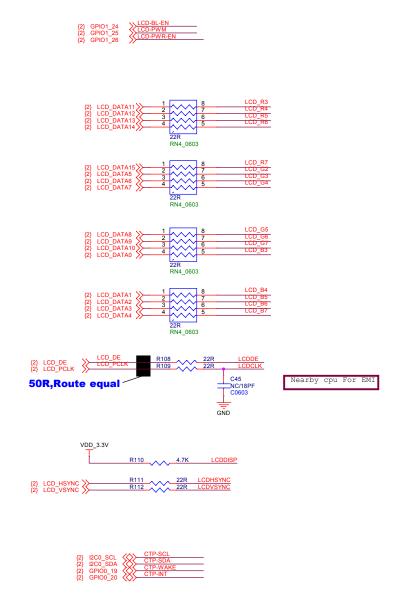




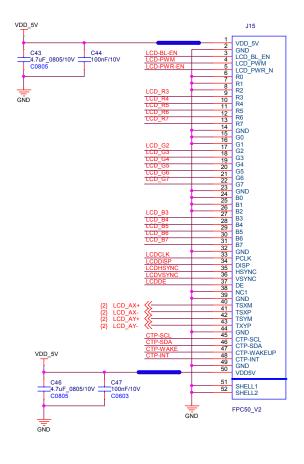








1) 飞 凌的L C D 接口 2) 底 板2.2的 原理图



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	<title></th><th></th></tr><tr><th>Size</th><th>Page Name</th><th>Rev</th></tr><tr><td>A3</td><td><Doc></td><td><RevCode</td></tr><tr><td>Date:</td><td>Monday, October 08, 2018 Sheet 7 o</td><td>f 11</td></tr></tbody></table></title>				

