BLOCK DIAGRAM

REVISION HISTORY

Revision	Description	Date	Design	Checked
V00	REALSE VERSION	2018-08-14	frank	thisway diy

Schematics Index:

P01: REVISION HISTORY

P02: CONNECTOR

P03: POWER

P04: LED KEY

P05: $UAR\overline{T}$ SD

P06: NET

P07: LCD

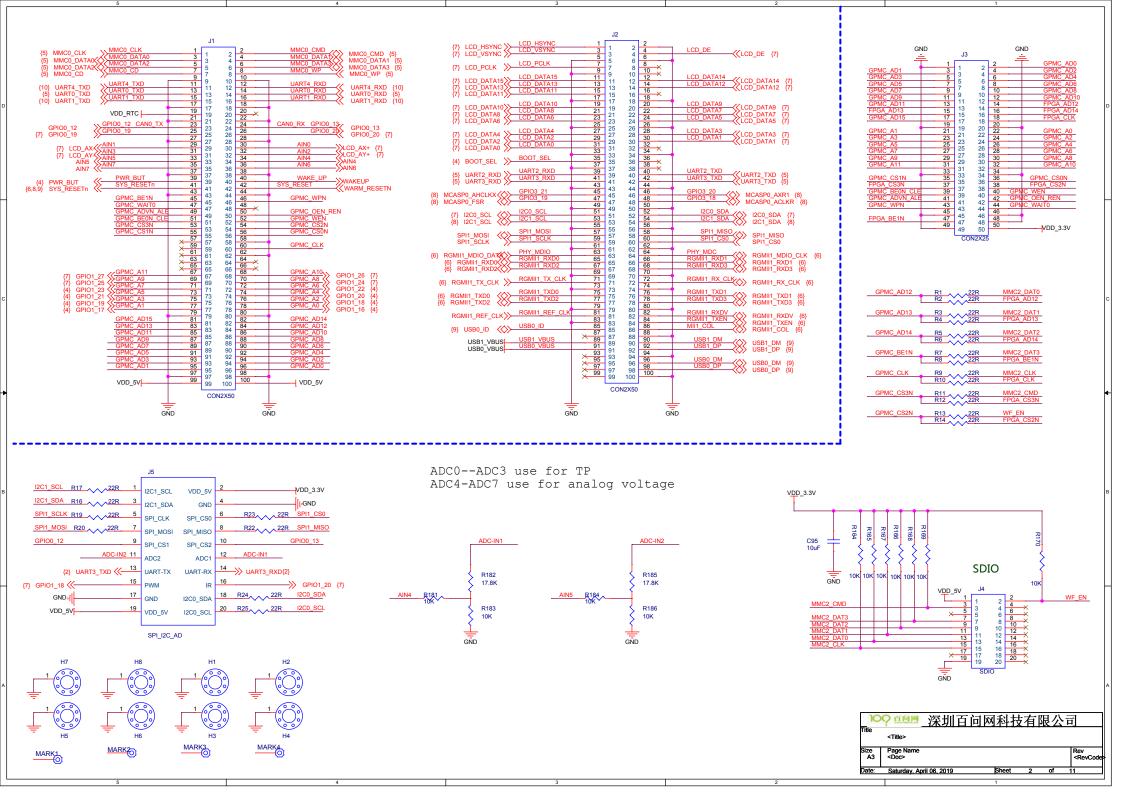
P08: AUDIO

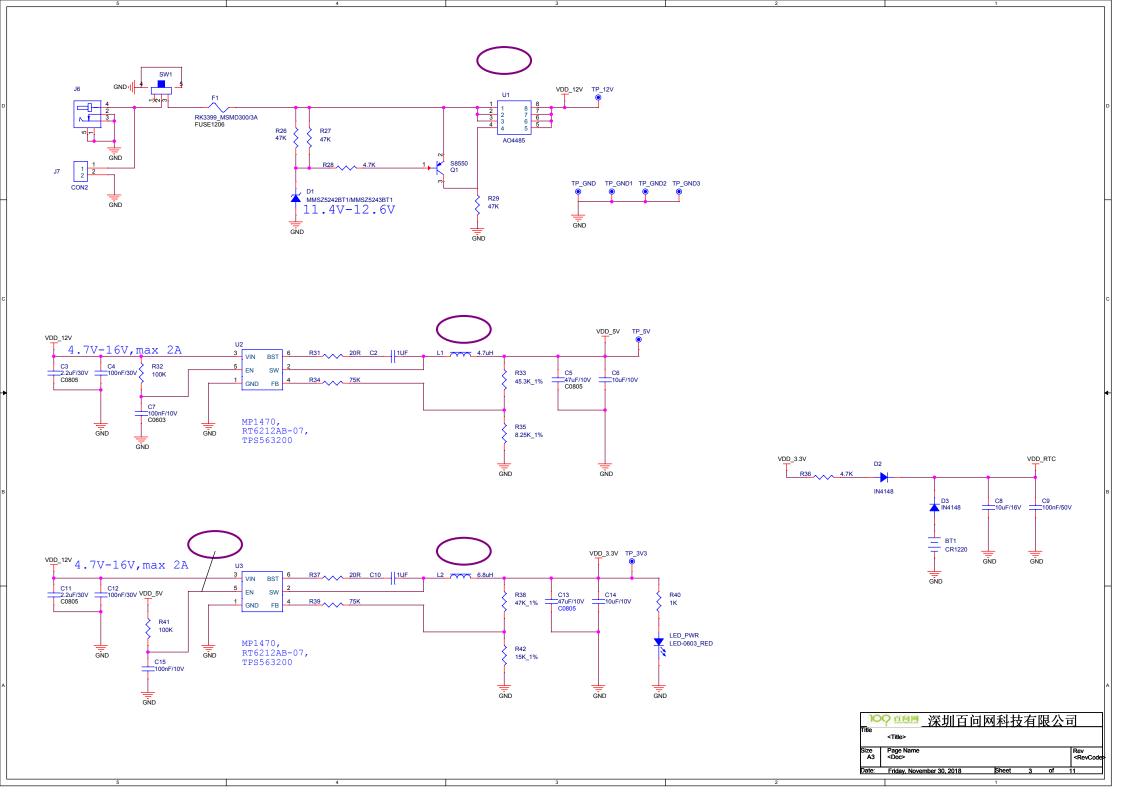
P09: USB

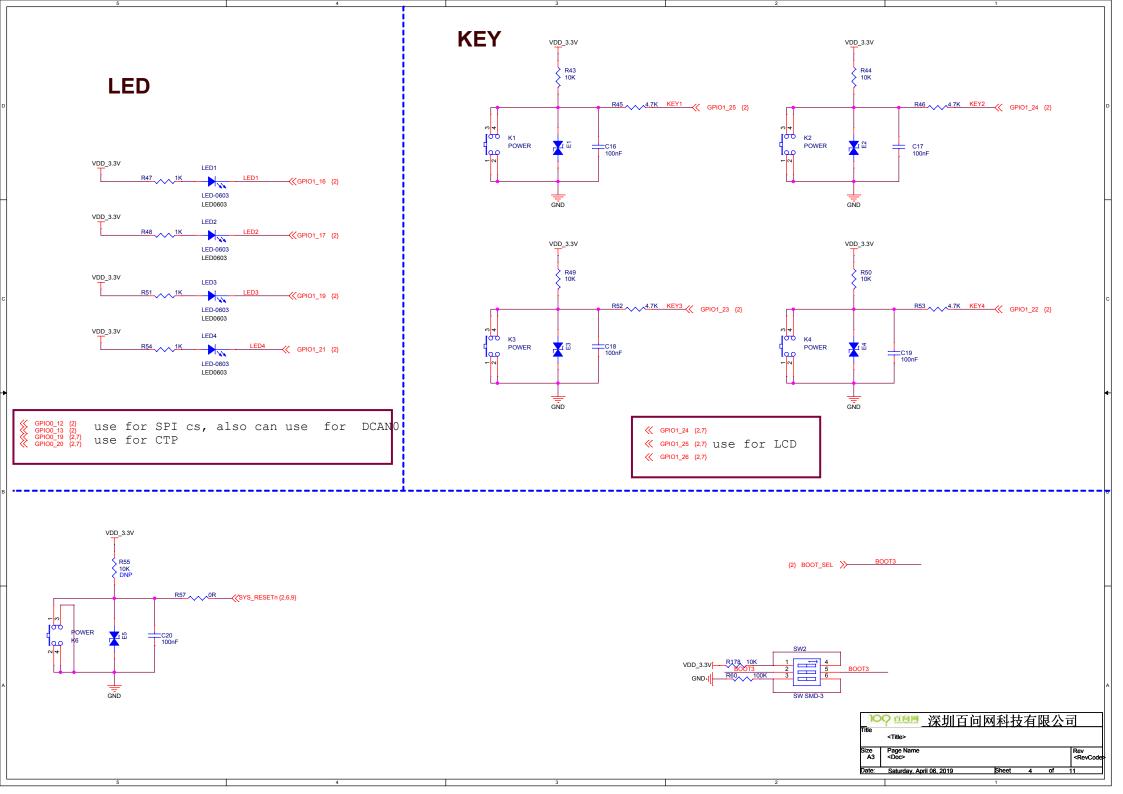
P10: CAN 485

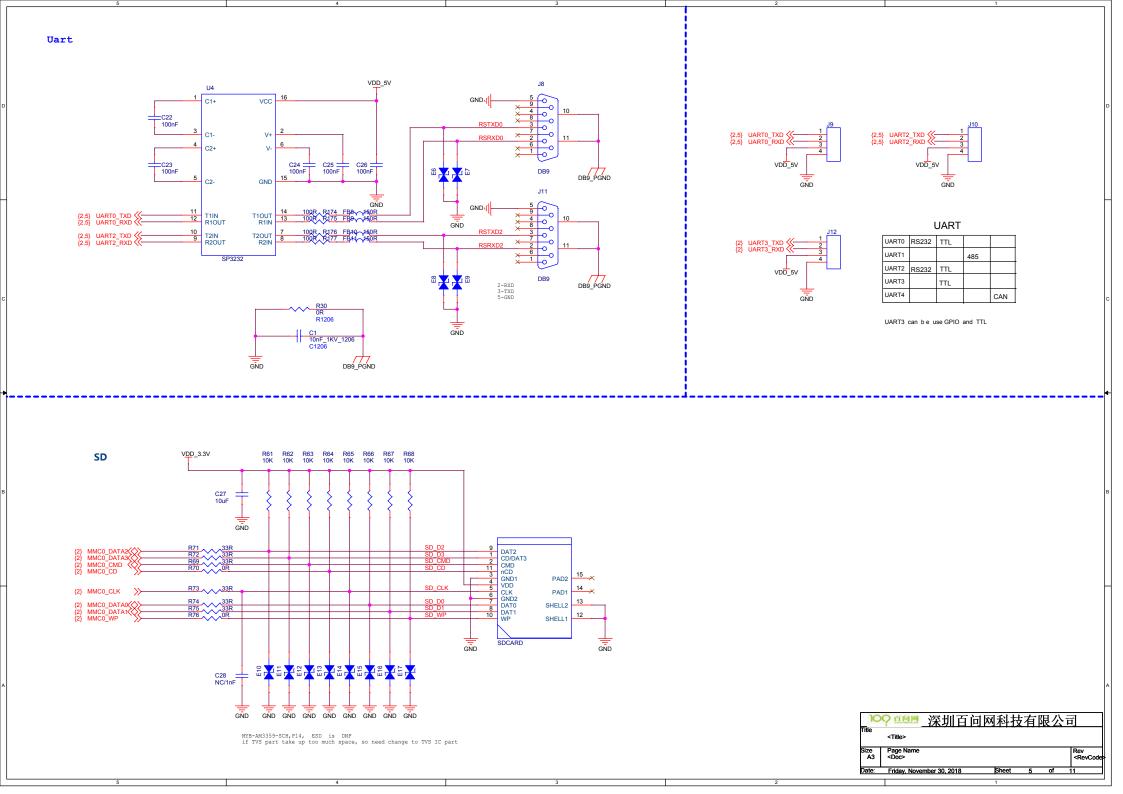
| Title | 深圳百间网科技有限公司 | Title | 不Title | 不Title

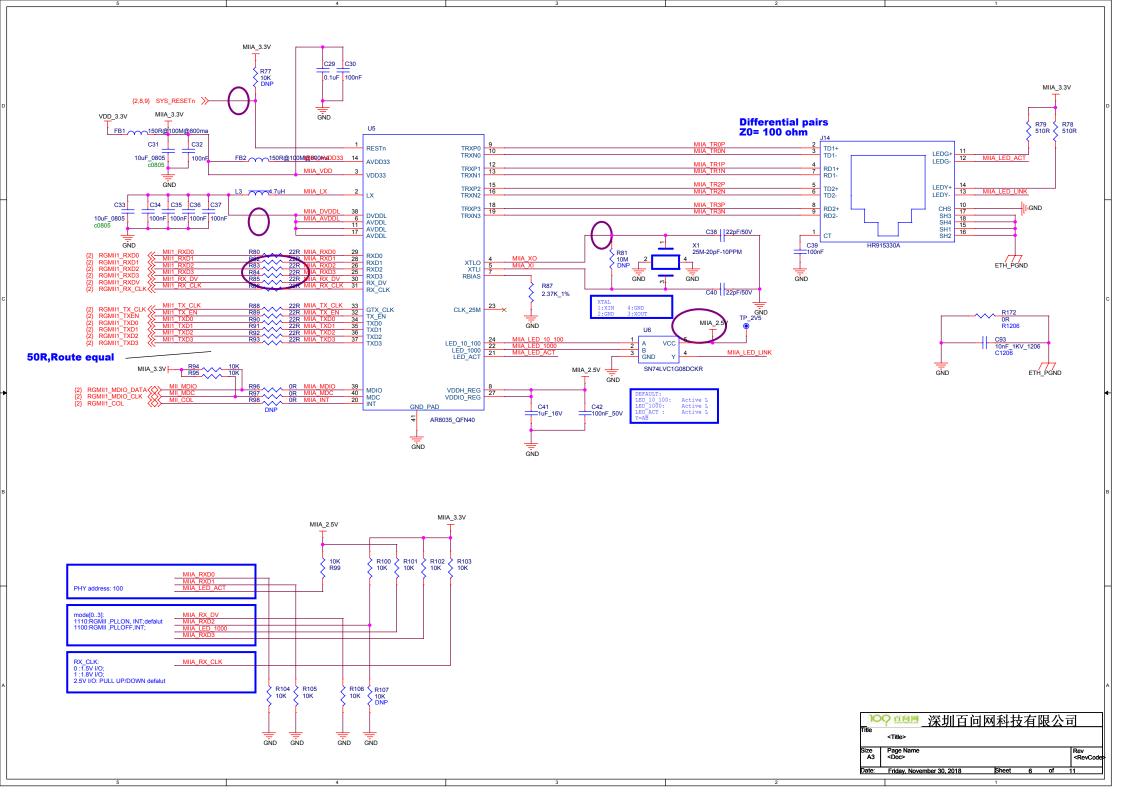






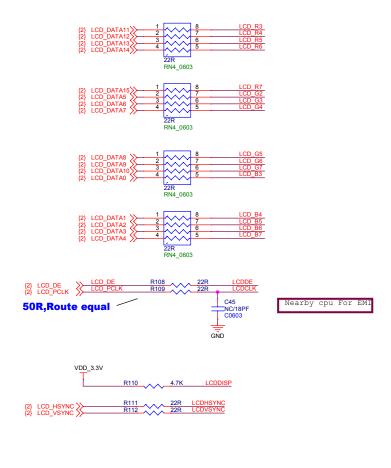


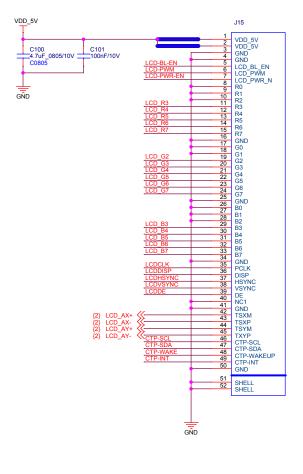






(2) 12C0_SCL CTP-SCL
(2) 12C0_SDA CTP-WAKE
(2) GPIC0_19
(2) GPIC0_20 CTP-INT





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