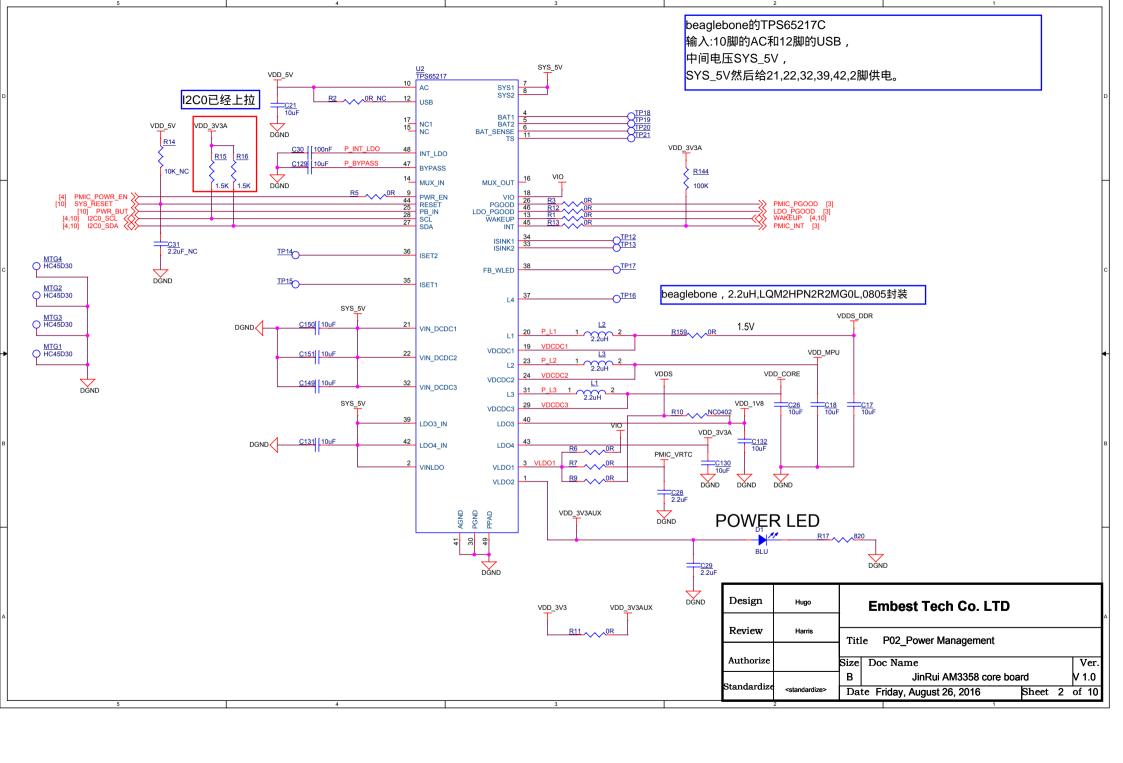
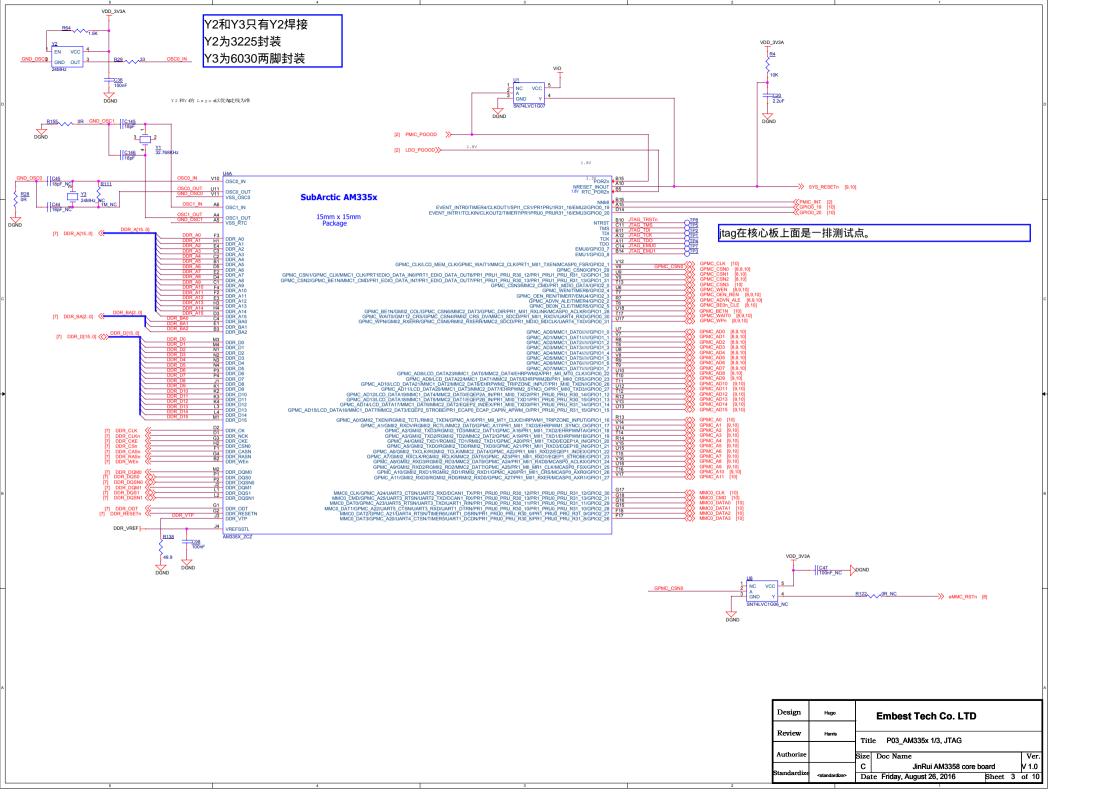
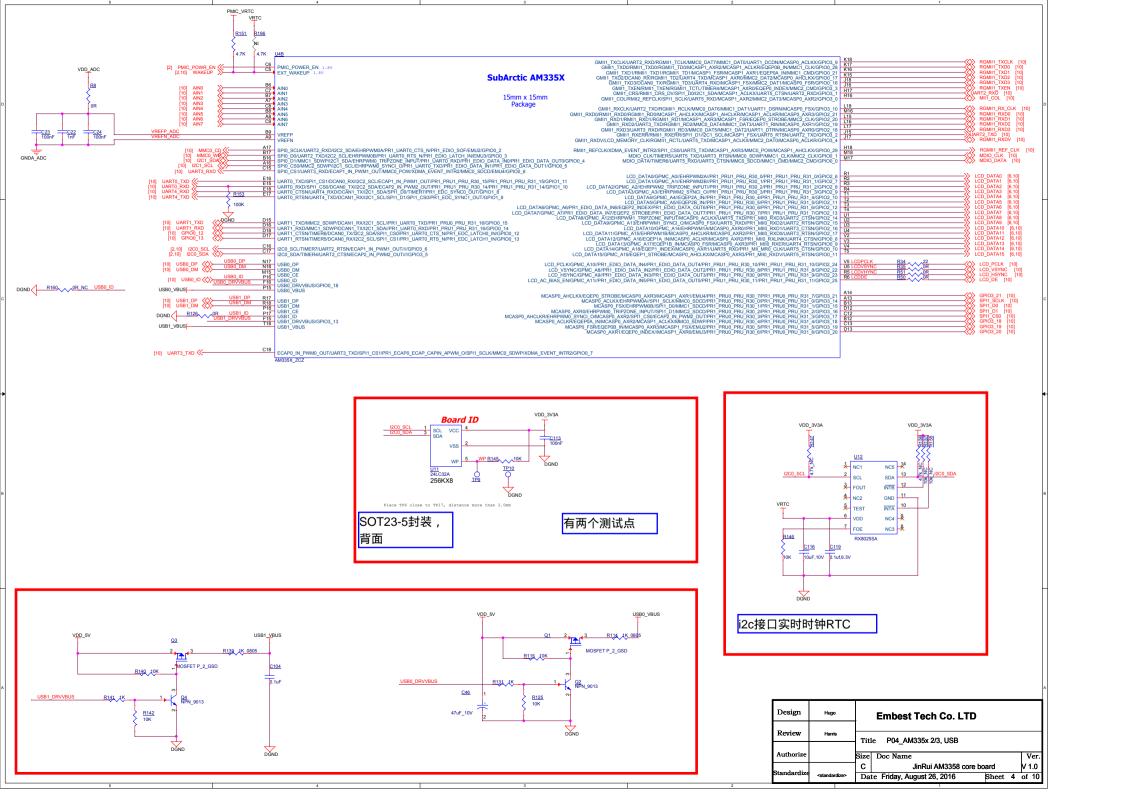
5	4		3
REV	Description	DATE	BY
V1.0 Initial production I	Release.	11/19/2012	Hugo
V1.0 SCH: A. Change Nor if a: B. Change def aut C. Change Y2 E m D. Change RTC p	sh 16bit~25bit address to GPMC_A1~GPMC_A10 boot configure from 011100001001001to 01001001 011101110 best PN from 1008001100010to 1008001100009 owerlogic		

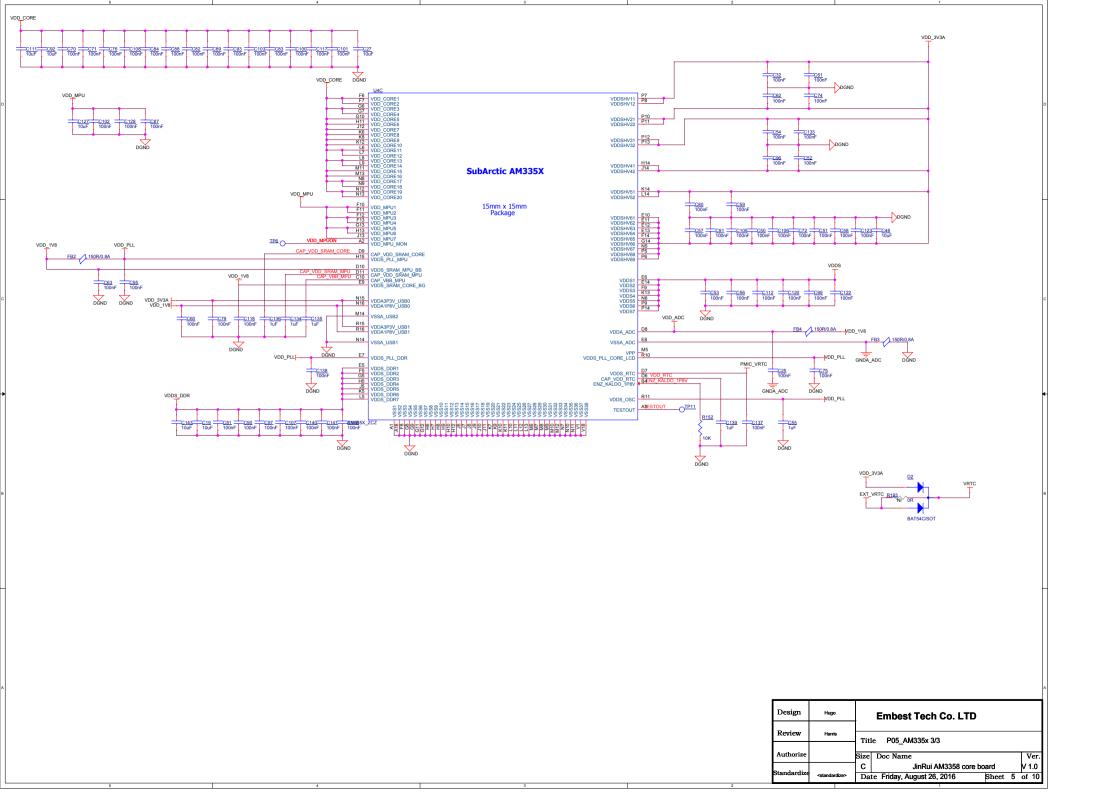
PAGE NO.	SCHEMATIC PAGE					
1	TITLE PAGE					
2	POWER MANAGEMENT					
3	PROCESSOR 1 OF 3, JTAG HEADER					
4	PROCESSOR 2 OF 3, UAB PORTS					
5	PROCESSOR 3 OF 3					
6	BOOT CONFIG					
7	DDR3 MEMORY					
8	eMMC FLASH					
9	NOR FLASH					
10	CONNECTOR					

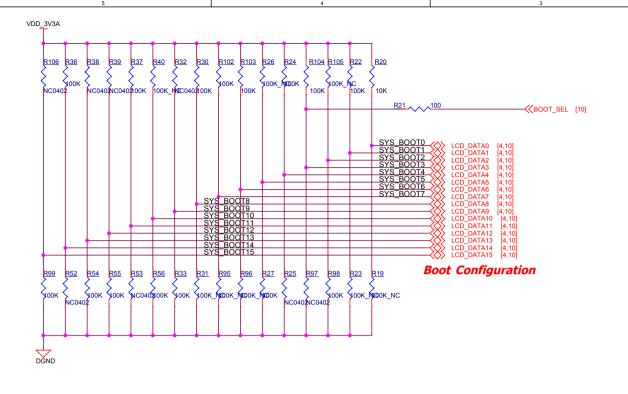
Design	Hugo	Embest Tech Co. LTD
Review	Harris	Title P01 Title Page
Authorize		Size Doc Name Ven
a		B JinRui AM3358 core board V 1.0
Standardize	<standardize></standardize>	Date Friday, August 26, 2016 Sheet 1 of 10











SYSBOOT[15:14] 00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	SYSBOOT[13:12] 00b (all other values reserved)	For XIP boot: 00b = non-muxed device 10b = muxed device x1b = reserved	Don't care for ROM code	SYSBOOT[8] 0 = 8-bit device 1 = 16-bit device device	Don't care for ROM code	o = CLKOUT1 disabled 1 = CLKOUT1 enabled	11011b	Boot Sequence			
								XIP w/ WAIT[1] (MUX2)[2]	UART0	SPI0	MMC0
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	For NAND boot: must be 00b	0 = ECC done by ROM 1 = ECC handled by NAND	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	10011b	NAND	NANDI2C	ммсо	UART0
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11100b	MMC1	MMC0	UART0	USB0

Des	sign	Hugo		Eı	mbest Tech Co.	LTD			
Rev	<i>r</i> iew	Harris	Title P06 Boot Config						
			1101	C	1 00_Book coming				
Aut	horize		Size	Do	oc Name				Ver.
			в		JinRui AM3358	core bo	ard		V 1.0
Stan	dardize	<standardize></standardize>	Date Friday, August 26, 2016 Sheet 6					6	of 10

