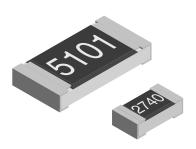


## **High Stability Thin Film Flat Chip Resistor**

≤ 0.05 % (1000 h rated power at 70 °C)



TNPW e3 Precision Thin Film Flat Chip Resistors are the perfect choice for most fields of modern electronics where highest reliability and stability is of major concern. Typical applications include automotive, telecommunication, industrial, medical equipment, precision test and measuring equipment.

### **FEATURES**

- High Temperature Exposure: 200 °C; 1000 h
- Superior moisture resistivity ≤ 0.25 % (85 °C; 56 days; 85 % RH)



- Low temperature coefficient and tight tolerances (± 0.1 %; ± 10 ppm/K)
- · Waste gas resistant

### **APPLICATIONS**

- Automotive
- Telecommunication
- Medical Equipment
- · Industrial Equipment
- Instrumentation
- Test and Measuring Equipment

STANDARD EL	ECTRICAL	SPECIFICA	TIONS				
	TNPW0402	TNPW0603	TNPW0805	TNPW1206	TNPW1210 <sup>1)</sup>	TNPW2010	TNPW2512 <sup>1)</sup>
Metric Size	RR 1005M	RR 1608M	RR 2012M	RR 3216M	RR 3225M	RR 5025M	RR 6332M
Resistance range	10 $\Omega$ to 100 k $\Omega$	10 $\Omega$ to 332 k $\Omega$	10 $\Omega$ to 1 M $\Omega$	10 $\Omega$ to 2 M $\Omega$	10 $\Omega$ to 3.01 M $\Omega$	10 $\Omega$ to 4.99 M $\Omega$	10 Ω to 8.87 MΩ
Resistance tolerance			± 1	%; ± 0.5 %; ± 0.	1 %		
Temperature Coefficent		± 50 ppm/K; ± 25	5 ppm/K; ± 15 pp	m/K; ± 10 ppm/k		± 50 ppm/K;	± 25 ppm/K
Climatic category (LCT/UCT/days)	55/125/56	55/125/56	55/125/56	55/125/56	55/125/56	55/125/56	55/125/56
Rated dissipation, $P_{70}^{\ 2)}$	0.063 W	0.1 W	0.125 W	0.25 W	0.33 W	0.4 W	0.5 W
Operating voltage, <i>U</i> <sub>max</sub> AC/DC	50 V	75 V	150 V	200 V	200 V	300 V	300 V
Maximum permissible film temperature	155 °C	155 °C	155 °C	155 °C	155 °C	155 °C	155 °C
Thermal resistance <sup>3)</sup>	870 K/W	550 K/W	440 K/W	220 K/W	170 K/W	140 K/W	110 K/W
Max. resistance change at $P_{70}$ ; $ \Delta R/R $	10 $\Omega$ to 100 k $\Omega$	10 $\Omega$ to 332 k $\Omega$	10 $\Omega$ to 1 M $\Omega$	10 $\Omega$ to 2 M $\Omega$	10 $\Omega$ to 3.01 M $\Omega$	10 $\Omega$ to 4.99 M $\Omega$	10 Ω to 8.87 MΩ
1000 h	≤ 0.05 %	≤ 0.05 %	≤ 0.05 %	≤ 0.05 %	≤ 0.05 %	≤ 0.05 %	≤ 0.05 %
8000 h	≤ 0.10 %	≤ 0.10 %	≤ 0.10 %	≤ 0.10 %	≤ 0.10 %	≤ 0.10 %	≤ 0.10 %
225 000 h	≤ 0.30 %	≤ 0.30 %	≤ 0.30 %	≤ 0.30 %	≤ 0.30 %	≤ 0.30 %	≤ 0.30 %
Insulation voltage:							
U <sub>ins</sub> 1 min	75 V	100 V	200 V	300 V	300 V	300 V	300 V
continuous	75 V	75 V	75 V	75 V	75 V	75 V	75 V
Failure rate	≤ 0.3 x 10 <sup>-9</sup> /h	≤ 0.3 x 10 <sup>-9</sup> /h	≤ 0.3 x 10 <sup>-9</sup> /h	≤ 0.3 x 10 <sup>-9</sup> /h	≤ 0.3 x 10 <sup>-9</sup> /h	≤ 0.3 x 10 <sup>-9</sup> /h	≤ 0.3 x 10 <sup>-9</sup> /h
Weight/1000 pcs.	0.65 g	2 g	5.5 g	10 g	16 g	28 g	39 g

<sup>1)</sup> Size not specified in EN 140401-801

 $<sup>^{2)}</sup>$  Rated voltage  $\sqrt{P\times R}$ . The power dissipation on the resistor generates a temperature rise against the local ambient, depending on the heat flow support of the printed-circuit board (thermal resistance). The rated dissipation applies only if the permitted film temperature is not exceeded

<sup>3)</sup> Measuring conditions in accordance with EN 140401-801

<sup>•</sup> TNPW 0402 without marking

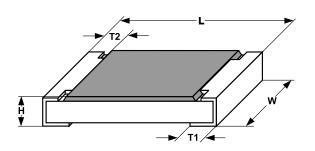
<sup>•</sup> Extended values, tighter tolerances and temperature coefficient available on request



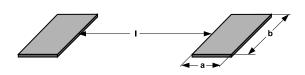
# High Stability Thin Film Flat Chip Resistor $\leq$ 0.05 % (1000 h rated power at 70 °C)

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### **DIMENSIONS**



S	IZE		DIMENSION	IS in millimete	rs	
INCH	METRIC	L	W	Н	T1	T2
0402	1005	$1.0 \pm 0.05$	$0.5 \pm 0.05$	$0.35 \pm 0.05$	0.2 ±	0.10
0603	1608	1.6 ± 0.10	$0.85 \pm 0.10$	$0.45 \pm 0.10$	0.3 ±	0.20
0805	2012	$2.0 \pm 0.15$	1.25 ± 0.15	$0.45 \pm 0.10$	0.4 ±	0.20
1206	3216	$3.2 \pm 0.15$	1.6 ± 0.15	$0.55 \pm 0.10$	0.5 ±	0.25
1210	3225	$3.2 \pm 0.15$	2.45 ± 0.15	$0.60 \pm 0.15$	0.5 ±	0.25
2010	5025	$5.0 \pm 0.15$	2.5 ± 0.15	$0.60 \pm 0.15$	0.6 ±	0.25
2512	6332	$6.3 \pm 0.20$	3.1 ± 0.15	$0.60 \pm 0.15$	0.6 ±	0.25



SOLDER PAD DIMENSIONS in millimeters								
S	SIZE	REFLO	W SOLI	DERING	WAV	E SOLDE	RING	
INCH	METRIC	а	b	ı	а	b	I	
0402	1005	0.4	0.6	0.5	1	1	-	
0603	1608	0.5	0.9	1.0	0.9	0.9	1.0	
0805	2012	0.7	1.3	1.2	0.9	1.3	1.3	
1206	3216	0.9	1.7	2.0	1.1	1.7	2.3	
1210	3225	0.9	2.5	2.0	1.1	2.5	2.3	
2010	5025	1.0	2.5	3.9	1.2	2.5	3.9	
2512	6332	1.0	3.2	5.2	1.2	3.2	5.2	

TYPE	TCR	TOLERANCE	RESISTANCE VALUE	E-SERIES	
		±1%	10R - 100K	24 - 96	
	± 50 ppm/K	± 0.5 %	10R - 100K	24 - 192	
		± 0.1 %	47R - 100K	24 - 192	
TNPW0402		± 1 %	10R - 100K	24 - 96	
1NPWU4U2	± 25 ppm/K	± 0.5 %	10R - 100K		
		± 0.1 %		24 - 192	
	± 15 ppm/K	± 0.1 %	47R - 100K	24 - 192	
	± 10 ppm/K	± 0.1 %			
		± 1 %	10R - 332K	24 - 96	
	± 50 ppm/K	± 0.5 %	10R - 332K	24 - 192	
		± 0.1 %	10h - 332k	24 - 192	
TNPW0603		± 1 %	10R - 332K	24 - 96	
INPWUOUS	± 25 ppm/K	± 0.5 %	10R - 332K	24 - 192	
		± 0.1 %	10h - 332k		
	± 15 ppm/K	± 0.1 %	47R - 332K		
	± 10 ppm/K	± 0.1 %	4/H - 332K		
		±1%	10R - 1M0	24 - 96	
	± 50 ppm/K	± 0.5 %	10R - 1M0	24 - 192	
		± 0.1 %	TOR - TMO		
TNDWOOD		± 1 %	10R - 1M0	24 - 96	
TNPW0805	± 25 ppm/K	± 0.5 %	100, 100		
		± 0.1 %	10R - 1M0	24 - 192	
	± 15 ppm/K	± 0.1 %	475 4140	24 - 192	
	± 10 ppm/K	± 0.1 %	47R - 1M0		

# High Stability Thin Film Flat Chip Resistor ≤ 0.05 % (1000 h rated power at 70 °C)



TYPE	TCR	TOLERANCE	RESISTANCE VALUE	E-SERIES	
		± 1 %	10R - 2M0	24 - 96	
	± 50 ppm/K	± 0.5 %	10R - 2M0	24 - 192	
		± 0.1 %		_	
TNPW1206		± 1 %	10R - 2M0	24 - 96	
1141 441200	± 25 ppm/K	± 0.5 %	10R - 2M0		
		± 0.1 %	1011 21110	24 - 192	
	± 15 ppm/K	± 0.1 %	47R - 2M0	24 102	
	± 10 ppm/K	± 0.1 %	-		
		± 1 %	10R - 3M01	24 - 96	
	± 50 ppm/K	± 0.5 %	10R - 3M01	24 - 192	
		± 0.1 %	47R - 2M13		
TNPW1210	± 25 ppm/K	± 1 %	10R - 3M01	24 - 96	
1141 441210		± 0.5 %	10R - 3M01		
		± 0.1 %		24 - 192	
	± 15 ppm/K	± 0.1 %	47R - 2M13		
	± 10 ppm/K	± 0.1 %			
		± 1 %	10R - 4M99	24 - 96	
	± 50 ppm/K	± 0.5 %	10R - 4M99	24 - 192	
TNPW2010		± 0.1 %	47R - 1M0		
		± 1 %	10R - 4M99	24 - 96	
	± 25 ppm/K	± 0.5 %	10R - 4M99	24 - 192	
		± 0.1 %	47R - 1M0	_	
		± 1 %	10R - 8M87	24 - 96	
	± 50 ppm/K	± 0.5 %	10R - 8M87	24 - 192	
TNPW2512		± 0.1 %	47R - 1M0		
		± 1 %	10R - 8M87	24 - 96	
	± 25 ppm/K	± 0.5 %	10R - 8M87	24 - 192	
		± 0.1 %	47R - 1M0	0_	

#### PART NUMBER AND PRODUCT DESCRIPTION Products can be ordered using either the Product Description or the Part Number. For ordering TNPW with SnPb contacts please refer to latest edition of data sheet TNPW lead bearing. PART NUMBER: (LEAD (Pb)-FREE) TNPW12061K32DEEA Ρ W K 2 Т 6 3 D Ε Ε Α TOLERANCE PACKING1) MODEL **VALUE TCR SPECIAL** $H = \pm 50 \text{ ppm/K}$ $E = \pm 25 \text{ ppm/K}$ $X = \pm 15 \text{ ppm/K}$ EA EC TNPW0402 R = Decimal $B = \pm 0.1 \%$ up to 2 digits **TNPW0603** K = Thousand $D = \pm 0.5 \%$ Blank = standard **TNPW0805** ED M = Million $F = \pm 1.0 \%$ TNPW1206 (4 digits) $Y = \pm 10 \text{ ppm/K}$ EF TNPW1210 EG **TNPW2010** ΕN **TNPW2512** PRODUCT DESCRIPTION: TNPW1206 1K32 0.5 % T-9 ET1 e3 **TNPW1206** 1K32 0.5 % T-9 ET1 е3 RESISTANCE LEAD (Pb)-FREE MODEL **TOLERANCE** TCR PACKING1) VALUE $\Omega$ TNPW0402 Examples: ± 0.1 % $T-2 = \pm 50 \text{ ppm/K}$ e3 = Pure Tin ET1 **TNPW0603** $T-9 = \pm 25 \text{ ppm/K}$ $54R1 = 54.1 \Omega$ $\pm$ 0.5 % Termination Finish ET6 **TNPW0805** $1K32 = 1320 \Omega$ $\pm$ 1.0 % $T-10 = \pm 15 ppm/K$ ET7 TNPW1206 $T-13 = \pm 10 \text{ ppm/K}$ E02 TNPW1210 E67 TNPW2010 F52 TNPW2512 E75

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Document Number: 28758

Revision: 14-Jul-06

<sup>1)</sup> Please refer to PACKING table



## High Stability Thin Film Flat Chip Resistor ≤ 0.05 % (1000 h rated power at 70 °C)

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PACKAG	PACKAGING								
MODEL	TAPE WIDTH [mm]	PITCH [mm]	REEL DIAMETER [mm/inch]	PIECES PER REEL	PACKING CODE for PRODUCT DESCRIPTION	PACKING CODE for PART NUMBER	TYPE OF CARRIER TAPE		
TNPW 0402	8	2	180/7	10 000	ET7	ED	Paper		
TNPW 0603 TNPW 0805 TNPW 1206 TNPW 1210	8	4	180/7	1000	E52 <sup>1</sup>	EN <sup>1</sup>	Paper		
TNPW 0603 TNPW 0805 TNPW 1206 TNPW 1210	8	4	180/7	5000	ET1	EA	Paper		
TNPW 0603 TNPW 0805 TNPW 1206 TNPW 1210	8	4	330/13	20 000	ET6	EC	Paper		
TNPW 2010	12	4	180/7	1000	E75	EY	Blister		
1141 44 2010	12	+	100/7	4000	E02	EF	Blister		
TNPW 2512	12	4	180/7	1000	E75	EY	Blister		
1141 44 5215	12	7	100/1	2000	E67	EG	Blister		

<sup>1)</sup> E52/EN only for precision resistors with tolerance ± 0.1 % and temperature coefficient ≤ ± 25 ppm/k

### **DESCRIPTION**

Production is strictly controlled and follows an extensive set of instructions established for reproducibility. A homogeneous film of metal alloy is deposited on a super high grade (96 % Al<sub>2</sub>0<sub>3</sub>) ceramic substrate and conditioned to achieve the desired temperature coefficient. Specially designed inner contacts are deposited on both sides. A special laser is used to achieve the target value by smoothly fine trimming the resistive layer without damaging the ceramics. A further conditioning is applied in order to stabilize the trimming result. The resistor elements are covered by a protective coating designed for electrical, mechanical and climatic protection. The terminations receive a final pure tin on nickel plating. The result of the determined production is verified by an extensive testing procedure on 100 % of the individual chip resistors. Only accepted products are laid directly into the tape in accordance with EN 60286-3.

#### **ASSEMBLY**

The resistors are suitable for processing on automatic SMD assembly systems. They are suitable for automatic soldering using wave, reflow or vapour phase as shown in **IEC 61760-1**. The encapsulation is resistant to all cleaning solvents commonly used in the electronics industry, including alcohols, esters and aqueous solutions. The resistors are RoHS compliant, the pure tin plating provides compatibility with lead (Pb)-free and lead-containing soldering processes. The immunity of the plating against tin whisker growth has been proven under extensive testing.

All products comply with the GADSL<sup>1)</sup> and the CEFIC-EECA-EICTA<sup>2)</sup> list of legal restrictions on hazardous substances. This includes full compliance with the following directives:

- 2000/53/EC End of Vehicle life Directive (ELV) and Annex II (ELV II)
- 2002/95/EC Restriction of the use of Hazardous Substances Directive (RoHS)
- 2002/96/EC Waste Electrical and Electronic Equipment Directive (WEEE)

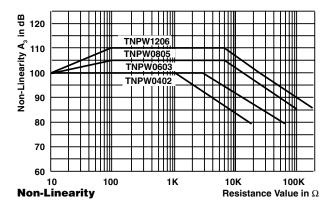
Solderability is specified for 2 years after production or re-qualification. The permitted storage time is 20 years.

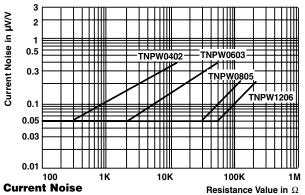
<sup>1)</sup> Global Automotive Declarable Substance List, see www.gadsl.org

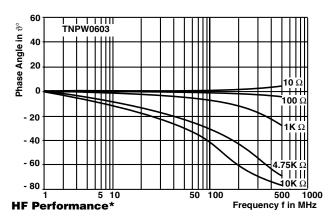
<sup>&</sup>lt;sup>2)</sup> CEFIC (European Chemical Industry Council), EECA (European Electronic Component Manufacturers Association), EICTA (European trade organisation representing the information and communications technology and consumer electronics), see <a href="https://www.eicta.org"><u>www.eicta.org</u></a> -> issue -> environment policy -> chemicals -> chemicals for electronics

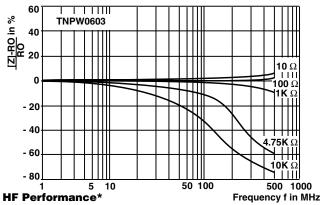
# High Stability Thin Film Flat Chip Resistor ≤ 0.05 % (1000 h rated power at 70 °C)

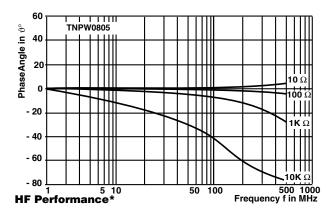


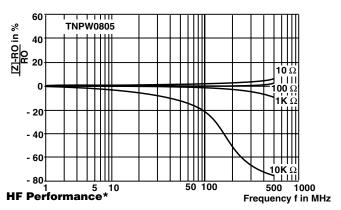












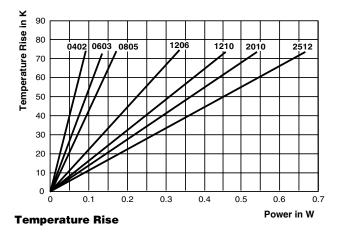
<sup>\*</sup> Typical figures. HF-characteristic also depends on termination and circuit design.

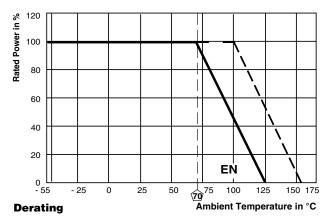


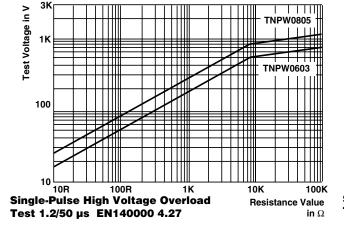


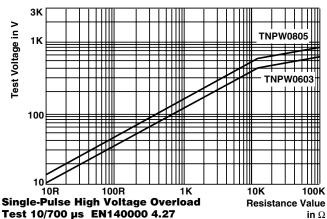
High Stability Thin Film Flat Chip Resistor  $\leq$  0.05 % (1000 h rated power at 70 °C)

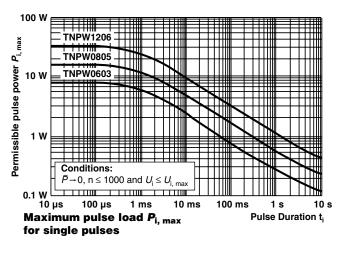


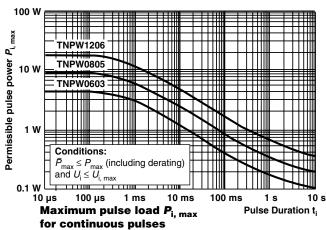










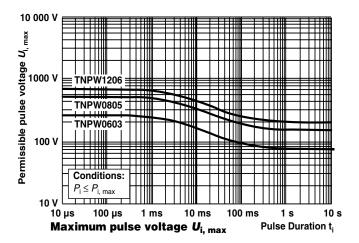


Document Number: 28758 Revision: 14-Jul-06

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# High Stability Thin Film Flat Chip Resistor ≤ 0.05 % (1000 h rated power at 70 °C)





### **TEST AND REQUIREMENTS**

All tests are carried out in accordance with the following specifications:

EN 60115-1, Generic specification (includes tests)

EN 140 400, Sectional specification (includes schedule for qualification approval)

EN 140 401-801, Detail specification (includes schedule for conformance inspection)

The following table contains only the most important tests. For the full test schedule refer to the documents listed above. The testing also covers most of the requirements specified by EIA/IS-703 and JIS-C-5202. The tests are carried out in accordance with IEC 60068 and under standard atmospheric conditions in accordance with IEC 60068-1, 5.3. Climatic category LCT/UCT/56 (rated temperature range: Lower

Category Temperature, Upper Category Temperature; damp heat, long term, 56 days) is valid. Unless otherwise specified the following values apply:

Temperature: 15 °C to 35 °C Relative humidity: 45 % to 75 %

Air pressure: 86 kPa to 106 kPa (860 mbar to 1 060 mbar).

The components are mounted for testing on boards in accordance with EN 60115-1, 4.31 unless otherwise specified. The parameters stated in the Test Procedures and Requirements table are based on the required tests and permitted limits of EN 140 401-801. However, some additional tests and a number of improvements against those minimum requirements have been included.

TEST PR	TEST PROCEDURES AND REQUIREMENTS							
EN 60115-1 CLAUSE	IEC 60068-2 TEST METHOD	TEST	PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE				
			stability for product types:					
			TNPW0402					
			TNPW0603					
			TNPW0805					
			TNPW1206					
			TNPW1210					
			TNPW2010					
			TNPW2512					
4.5	-	resistance		± 1 %; ± 0.5 %; ± 0.1 %				
4.8.4.2	-	temperature coefficient	at 20/- 55/20 °C ± and 20/125/20 °C	± 50 ppm/K; ± 25 ppm/K; ± 15 ppm/K; ± 10 ppm/K				
4.25.1	-	endurance at 70 °C	$U = \sqrt{P_{70} \times R}$ or $U = U_{\text{max}}$ ; whichever is the less severe; 1.5 h on; 0.5 h off;					
			70 °C; 1000 h	$\pm (0.05 \% R + 0.01 \Omega)$				
			70 °C; 8000 h	$\pm (0.1 \% R + 0.02 \Omega)$				

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Revision: 14-Jul-06



# High Stability Thin Film Flat Chip Resistor ≤ 0.05 % (1000 h rated power at 70 °C)

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EN 60115-1	IEC 60068-2	TEOT	PROCEDURE	REQUIREMENTS
CLAUSE	TEST METHOD	TEST	PROCEDURE	PERMISSIBLE CHANGE
			stability for product types:	
			TNPW0402	
			TNPW0603	
			TNPW0805	
			TNPW1206	
			TNPW1210	
			TNPW2010	
			TNPW2512	
4.25.3	-	endurance at	125 °C; 1000 h	$\pm (0.05 \% R + 0.01 \Omega)$
		upper category	155 °C; 1000 h 200 °C, 1000 h <sup>1)</sup>	$\pm (0.1 \% R + 0.02 \Omega)$ $\pm (0.25 \% R + 0.05 \Omega)$
4.24	78 (Cab)	damp heat,	(40 ± 2) °C; 56 days;	$\pm (0.1 \% R + 0.01 \Omega)$
		steady state	(93 ± 3) % RH	
4.23		climatic sequence:		
4.23.2	2 (Ba)	dry heat	UCT; 16 h	
4.23.3	30 (Db)	damp	55 °C; 24 h; > 90 % RH; 1	
4.23.4	1 (Aa)	cold	LCT; 2 h	
4.23.5	13 (M)	low air	8.5 kPa; 2 h; 25 ± 10 °C	
4.23.6	30 (Db)	damp heat,	55 °C; 5 days;	
4.23.0	30 (Db)	cyclic	> 95 to 100 % RH; 5 cycles	
4.23.7	-	d.c. load	$U = \sqrt{P_{70} \times R} \le U_{\text{max}}; \text{ 1 min}$	
0.,		0.01.000	LCT = - 55 °C	
			UCT = 125 °C	$\pm (0.1 \% R + 0.02 \Omega)$
-	1 (Aa)	cold	- 55 °C; 2 h	$\pm (0.05 \% R + 0.01 \Omega)$
4.19	14 (Na)	rapid change	30 minutes at LCT and	± (0.1 % R + 0.01 Ω)
		of	30 minutes at UCT; LCT = - 55 °C; UCT = 125 °C;	
		temperature	1000 cycles	
4.13	-	short time	$U = 2.5 \text{ x } \sqrt{P_{70} \times R} \text{ or }$	± (0.05 % R + 0.01 Ω)
		overload	$U = 2 \times U_{\text{max}}$ ; whichever is	
			the less severe; 5 s	
4.27	-	single pulse	severity no. 4:	$\pm (0.5 \% R + 0.05 \Omega)$
		high voltage overload	$U = 10 \text{ x } \sqrt{P_{70} \times R} \text{ or } U = 2 \text{ x } U_{\text{max}}; \text{ whichever is}$	no visible damage
		01011000	the less severe;	
			10 pulses 10 μs/700 μs	
4.37	-	periodic	$U = \sqrt{15 \times P_{70} \times R} \text{ or}$	$\pm (0.5 \% R + 0.05 \Omega)$
		electric overload	$U = 2 \times U_{\text{max}}$ ; whichever is the less severe:	no visible damage
			0.1 s on; 2.5 s off;	
			1000 cycles	
4.22	6 (Fc)	vibration	endurance by sweeping;	$\pm (0.05 \% R + 0.01 \Omega)$
			10 to 2000 Hz; no resonance;	no visible damage
			amplitude ≤ 1.5 mm or	
			$\leq$ 200 m/s <sup>2</sup> ; 6 h	

<sup>1)</sup> Using advanced temperature level may require special considerations towards the choise of circuit board and solder material

# High Stability Thin Film Flat Chip Resistor ≤ 0.05 % (1000 h rated power at 70 °C)



TEST PR	TEST PROCEDURES AND REQUIREMENTS							
EN 60115-1 CLAUSE	IEC 60068-2 TEST METHOD	TEST	PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE				
			stability for product types:					
			TNPW0402					
			TNPW0603					
			TNPW0805					
			TNPW1206					
			TNPW1210					
			TNPW2010					
			TNPW2512					
4.17.2	58 (Td)	solderability	solder bath method; SnPb40; non-activated flux $(215 \pm 3)$ °C; $(3 \pm 0.3)$ s	good tinning (≥ 95 % covered);				
			solder bath method; SnAg3Cu0,5 or SnAg3,5; non-activated flux (235 ± 3) °C; (2 ± 0.2) s	no visible damage				
4.18.2	58 (Td)	resistance to soldering heat	solder bath method; (260 $\pm$ 5) °C; (10 $\pm$ 1) s	± (0.02 % R + 0.01 Ω)				
4.29	45 (XA)	component solvent resistance	isopropyl alcohol + 50 °C; method 2	no visible damage				
4.32	21 (Ue <sub>3</sub> )	shear	RR 1005M and RR 1608M;	no visible damage				
			RR 2012M and RR 3216M;					
4.33	21 (Ue <sub>1</sub> )	substrate bending	depth 2 mm, 3 times	$\pm  (0.05 \%  R + 0.01  \Omega)$ no visible damage, no open circuit in bent position				
4.7	-	voltage proof	$U_{\rm rms} = U_{\rm ins}; 60 \pm 5 \text{ s}$	no flashover or breakdown				
4.35	-	flammability	IEC 60695-11-5, needle flame test; 10 s	no burning after 30 s				
	-	damp heat	(85 ± 5) °C; 56 days (85 ± 5) % RH	± (0.25 R + 0.05 Ω)				

### **APPLICABLE SPECIFICATIONS**

- CECC40000/40400
- EN140400
- EN 140401-801
- EN 60115-1
- IEC 60286-3

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## **Legal Disclaimer Notice**



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