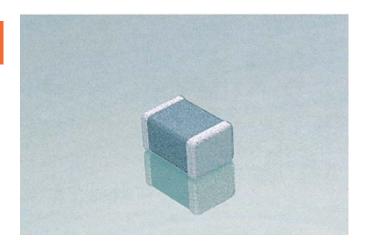
COG (NP0) Dielectric

General Specifications

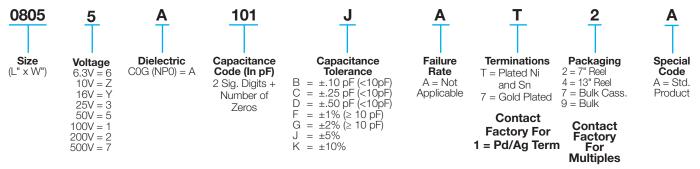




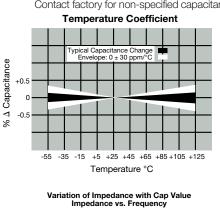
COG (NPO) is the most popular formulation of the "temperature-compensating," EIA Class I ceramic materials. Modern COG (NPO) formulations contain neodymium, samarium and other rare earth oxides.

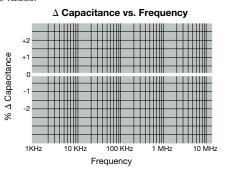
COG (NP0) ceramics offer one of the most stable capacitor dielectrics available. Capacitance change with temperature is 0 $\pm 30 ppm/^{\circ}C$ which is less than $\pm 0.3\%$ ΔC from -55°C to +125°C. Capacitance drift or hysteresis for COG (NP0) ceramics is negligible at less than $\pm 0.05\%$ versus up to $\pm 2\%$ for films. Typical capacitance change with life is less than $\pm 0.1\%$ for COG (NP0), one-fifth that shown by most other dielectrics. COG (NP0) formulations show no aging characteristics.

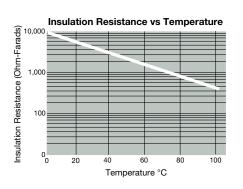
PART NUMBER (see page 2 for complete part number explanation)

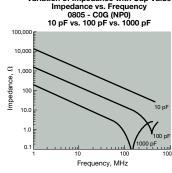


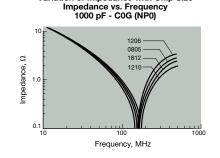
NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.



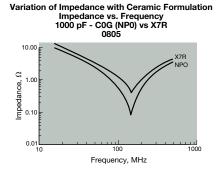








Variation of Impedance with Chip Size





C0G (NP0) Dielectric



Specifications and Test Methods

Parame	ter/Test	NP0 Specification Limits	Measuring Conditions							
Operating Temp		-55°C to +125°C	Temperature Cycle Chamber							
Capacitance		Within specified tolerance	Freq.: 1.0 MHz ± 10% for cap ≤ 1000 pF							
Q		<30 pF: Q≥ 400+20 x Cap Value	$1.0 \text{ kHz} \pm 10\% \text{ for cap} > 1000 \text{ pF}$							
<u> </u>		≥30 pF: Q≥ 1000	Voltage: 1.0Vrms ± .2V							
Insulation Resistance		100,000M Ω or 1000M Ω - μF,	Charge device with rated voltage for							
modiation resistance		whichever is less	60 ± 5 secs @ room temp/humidity							
Dielectric Strength		No breakdown or visual defects	Charge device with 300% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max) Note: Charge device with 150% of rated voltage for 500V devices.							
	Appearance	No defects	Deflection							
Resistance to Flexure Stresses	Capacitance Variation	±5% or ±.5 pF, whichever is greater	Test Time: 30 seconds 7 1mm/sec 90 mm							
	Q	Meets Initial Values (As Above)								
	Insulation Resistance	≥ Initial Value x 0.3								
Solderability		≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic solder at 230 \pm 5°C for 5.0 \pm 0.5 seconds							
Resistance to Solder Heat	Appearance	No defects, <25% leaching of either end terminal								
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Dip device in eutectic solder at 260°C for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.							
	Q	Meets Initial Values (As Above)								
	Insulation Resistance	Meets Initial Values (As Above)		9						
	Dielectric Strength	Meets Initial Values (As Above)								
Thermal Shock	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes						
	Capacitance Variation	\leq ±2.5% or ±.25 pF, whichever is greater	Step 2: Room Temp	≤ 3 minutes						
	Q	Meets Initial Values (As Above)	Step 3: +125°C ± 2°	30 ± 3 minutes						
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes						
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles and measure after 24 hours at room temperature							
	Appearance	No visual defects								
Load Life	Capacitance Variation	≤ ±3.0% or ± .3 pF, whichever is greater	Charge device with twice rated voltage in							
	Q (C=Nominal Cap)	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C	test chamber set for 1000 hou	ırs (+48, -0).						
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from test chamber and stabilize at room temperature for 24 hours before measuring.							
	Dielectric Strength	Meets Initial Values (As Above)								
Load Humidity	Appearance	No visual defects								
	Capacitance Variation	≤ ±5.0% or ± .5 pF, whichever is greater	Store in a test chamber set at 85°C ± 2°C/85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied. Remove from chamber and stabilize at room temperature for 24 ± 2 hours							
	Q	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C								
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)								
	Dielectric Strength	Meets Initial Values (As Above)	before measuring.							



C0G (NP0) Dielectric





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SIZE	01005	0201	0402	0402 0603		0805					1206							
Soldering	Reflow Only	Reflow Only	Reflow/Wave		Reflow/Wave			Reflow/Wave					Reflow/Wave					
Packaging mm	All Paper 0.40 ± 0.02	All Paper 0.60 ± 0.03	All Paper All Paper 1.00 ± 0.10 1.60 ± 0.15			Paper/Embossed 2,01 ± 0,20				Paper/Embossed 3,20 ± 0,20								
(L) Length (in.)	(0.016 ± 0.0008)	(0.024 ± 0.001)	(0.040 ± 0.00)	(0.040 ± 0.004) (0.063 ± 0.006)			(0.079 ± 0.008)				(0.126 ± 0.008)							
(W) Width mm (in.)	0.20 ± 0.02 (0.008 ± 0.0008)	0.30 ± 0.03 (0.011 ± 0.001)	0.50 ± 0.10 (0.020 ± 0.00)	14)	(0.032 ± 0.006)			1.25 ± 0.20 (0.049 ± 0.008)				1.60 ± 0.20 (0.063 ± 0.008)						
(t) Terminal mm	0.10 ± 0.04 (0.004 ± 0.016)	0.15 ± 0.05 (0.006 ± 0.002)	0.25 ± 0.15 (0.010 ± 0.00)		0.35 ± 0.15 (0.014 ± 0.006)			0.50 ± 0.25 (0.020 ± 0.010)				0.50 ± 0.25 (0.020 ± 0.010)						
WVDC	16	25 50	16 25	50 16		50	100	16	25	50	100	200	16	25	50	100	200	500
Cap 0.5 (pF) 1.0	В	A A		C G		G G	G G	J	J	J	J J	J	J	J	J	J	J	J
1.2 1.5	B B	A A	C C	C G		G	G G	J J	J J	J	J J	J J	J	J J	J	J	J J	J J
1.8	В	A A	C C	C G	G G	G	G	J	J	J	J	J	J	J	J	J	J	J
2.2 2.7	B B	A A		C G		G	G G	J J	J	J	J J	J	J	J	J	J	J J	J
3.3	B B	A A	C C	C G		G G	G G	J	J	J	J	J	J	J	J	J	J	J
3.9 4.7	В	A A	СС	C G	G G	G	G	J J	J J	J	J J	J	J	J J	J	J	J J	J
5.6 6.8	B B	A A	C C	C G		G	G G	J J	J J	J	J J	J J	J	J J	J	J	J J	J
8.2	В	A A	C C	C G	G G	G	G	J	J	J	J	J	J	Ĵ	J	J	J	J
10 12	B B	A A	C C	C G		G G	G G	J J	J	J	J J	J	J	J	J	J	J J	J
<u>15</u>	B B	A A	C C	C G	G G	G	G G	J	J	J	J	J	J	J	J	J	J	J
22	В	A A	CC	C G	G G	G	G	J	J	Ĵ	J	J	J	J	Ĵ	J	J	J
33		A A	C C	C G		G	G G	J	J	J	J J	J	J	J	J	J	J	J
39		А	C C	C G	G G	G	G	J	J	J	J	J	J	J	J	J	J	J
<u>47</u> 56		A	C C	C G		G	G G	J	J	J	J J	J	J	J J	J	J	J	J
68 82		A A		C G		G G	G G	J J	J J	J	J J	J J	J	J J	J	J	J J	J J
100		A	C C	C G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
120 150				C G		G	G G	J J	J J	J	J J	J J	J	J J	J	J	J J	J J
180			C C	C G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
220 270				C G		G G	G G	J J	J	J	J J	J M	J	J J	J	J	J J	M M
330 390			CCC	C G		G G	G	J	J J	J	J J	M M	J	J J	J	J	J J	M M
470			c c	C G	G G	G		J	J	J	J	М	J	J	J	J	J	М
560 680				G		G		J J	J J	J	J J	М	J	J J	J	J	J J	M P
820 1000				G		G G		J J	J J	J	J J		J	J J	J	J	M Q	
1200				G	ı G	G		J	J	J	J		J	J	J	J	Q	
1500 1800								J	J	J			J	J	J M	M M	Q	
2200								J	J	N			J	J	М	Р		
2700 3300							\dashv	J	J	N			J	J	M	P P		
3900 4700								J J	J J				J	J J	M M	P P		
5600			 					U	U				J	J	M	-		
6800 8200		. >	-W-										M M	M M				
Cap 0.010 (μF) 0.012		✓ L											М	М				
0.015				٢'														
0.018 0.022																		
0.027		ĺ	⁻t∣															
0.033 0.039				1														
0.047	-																	
0.082																		
0.1 WVDC	25	50 16	25 50	16 25	5 50	100	16	25	50	100	200	16	25	50	100	200	500	
SIZE	01005	0201	0402			603				0805					12			
Letter	A B	C		G	J	K	M		N	P		Q	Х		Υ	Z		
	0.22 .013) (0.009					1.02	1.27		1.40 .055)	1.52 (0.060		1.78 .070)	2.29 (0.090		2.54 1.100)	2.79))	
	, , ,	PAPER					EMBOSSED				,		,	,				



C0G (NP0) Dielectric



Capacitance Range

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