Multi-layer ceramic chip capacitors

MCH31 (3216 (1206) size, chip capacitor)

●Features

1) Miniature, high capacitance
2) Achieved high capacitance by thin and multi layer technology
3) Lead-free plating terminal
4) No polarity

●Quick Reference

The design and specifications are subject to change without prior notice. Please check the most recent technical specifications prior to placing orders or using the product. For more detail information regarding packaging style code, please check product designation.

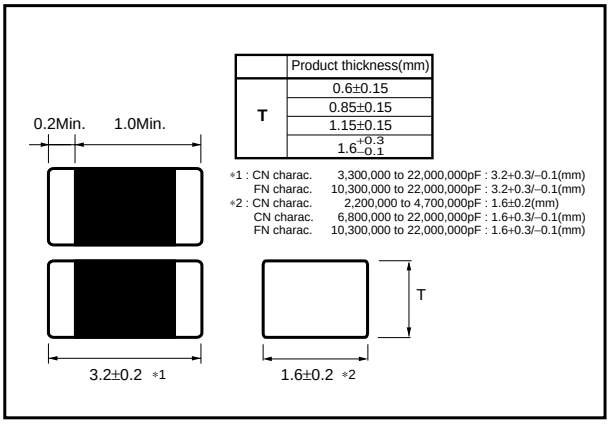
●Thermal compensation

Part No.	Size code	Temperature characteristics code	Temperature characteristics (ppm/°C)	Operating temp. range (°C)	Rated voltage (V)	Capacitance (pF)	Capacitance tolerance	Thickness (mm)
MCH31	3216 (1206)	A (AN)	0±30 (CG) (C0G)	−55 to +125	50	4,700 to 6,800 (E12 Series)	J(±5%)	0.6 ± 0.15
						8,200 to 10,000 (E12 Series)		0.85 ± 0.15
						15,000 to 22,000 (E12 Series)		1.15 ± 0.15
						33,000 (E12 Series)		1.6 ± 0.2

●High dielectric constant

Part No.	Size code	Temperature characteristics code	Temperature characteristics (ppm/°C)	Operating temp. range (°C)	Rated voltage (V)	Capacitance (pF)	Capacitance tolerance	Thickness (mm)
MCH31	3216 (1206)	CN	±10% (B)	−25 to +85	50	220 to 68,000 (E6 Series)	K(±10%)	0.6 ± 0.15
						100,000 (E6 Series)		0.85 ± 0.15
						150,000 to 330,000 (E6 Series)		1.15 ± 0.15
						470,000 (E6 Series)		1.6 $^{+0.3}_{-0.1}$
					25	680,000 (E6 Series)		0.85 ± 0.15
			±15% (R) (X7R)	−55 to +125	50	220 to 68,000 (E6 Series)		0.6 ± 0.15
						100,000 (E6 Series)		0.85 ± 0.15
						150,000 to 330,000 (E6 Series)		1.15 ± 0.15
						470,000 (E6 Series)		1.6 $^{+0.3}_{-0.1}$
					25	680,000 (E6 Series)		0.85 ± 0.15
			±15% (X5R)	−55 to +85	16	1,000,000 (E6 Series)		1.6 $^{+0.3}_{-0.1}$
						1,500,000 to 4,700,000 (E6 Series)		
					10	6,800,000 to 10,000,000 (E6 Series)		
					6.3	22,000,000 (E3 Series)		
		FN	+30% , −80% (F)	−25 to +85	50	1,000,000 (E3 Series)	Z(+80% , −20%)	0.85 ± 0.15
						2,200,000 (E3 Series)		1.15 ± 0.15
					25	4,700,000 (E3 Series)		
					16	10,000,000 (E3 Series)		1.6 $^{+0.3}_{-0.1}$
					10	22,000,000 (E3 Series)		
					6.3	47,000,000 (E3 Series)		
			+22% , −82% (Y5V)	−30 to +85	50	1,000,000 (E3 Series)		0.85 ± 0.15
						2,200,000 (E3 Series)		1.15 ± 0.15
					25	4,700,000 (E3 Series)		
					16	10,000,000 (E3 Series)		1.6 $^{+0.3}_{-0.1}$
					10	22,000,000 (E3 Series)		
					6.3	47,000,000 (E3 Series)		

●**External dimensions** (Unit : mm)



	Product thickness(mm)
T	0.6±0.15
	0.85±0.15
	1.15±0.15
	$1.6^{+0.3}_{-0.1}$

∗1 : CN charac. 3,300,000 to 22,000,000pF : 3.2+0.3/−0.1(mm)
FN charac. 10,300,000 to 22,000,000pF : 3.2+0.3/−0.1(mm)
∗2 : CN charac. 2,200,000 to 4,700,000pF : 1.6±0.2(mm)
CN charac. 6,800,000 to 22,000,000pF : 1.6+0.3/−0.1(mm)
FN charac. 10,300,000 to 22,000,000pF : 1.6+0.3/−0.1(mm)

●**Product designation**

Code	Product thickness	Packing specification	Reel	Basic ordering unit (pcs.)
K	**0.6mm**	Paper tape (width 8mm, pitch 4mm)	ϕ180mm (7in.)	4,000
K	**0.85mm**	Paper tape (width 8mm, pitch 4mm)	ϕ180mm (7in.)	4,000
P	**1.15mm**	Plustic tape (width 8mm, pitch 4mm)	ϕ180mm (7in.)	2,000
P	**1.60mm**	Plustic tape (width 8mm, pitch 4mm)	ϕ180mm (7in.)	1,000

Reel (ϕ180mm) : compatible with EIAJ ET-7200A

Part No. Packing Style

M	C	H	3	1	3	F	N	1	0	6	Z	K

Rated voltage

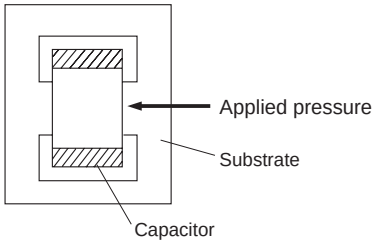
Code	Voltage
8	6.3V
4	10V
3	16V
2	25V
5	50V

Temperature characteristic code
: Refer to quick reference table.

Nominal capacitance	Capacitance tolerance	
	Code	Tolerance
3-digit designation according to IEC	**J**	±5% (11pF or more)
	K	±10%
	Z	+80%, −20%

●Performance and test method

<table>
<tr><th>No.</th><th>Items</th><th colspan="2">Performance</th><th>Test Method
(As per JIS C 5101-1, JIS C 5101-10)</th></tr>
<tr><td>1</td><td>Appearance and dimensions</td><td colspan="2">No marked defects shall be allowed for appearance.
Dimensions shall be as specified the clause 4.</td><td>As per 4.4 of JIS C 5101-1.
As per 4.5 of JIS C 5101-10
Using a Magnifier.</td></tr>
<tr><td>2</td><td>Withstanding voltage</td><td colspan="2">No dielectrical breakdown or other damage shall be allowed.</td><td>As per 4.6 of JIS C 5101-1.
As per 4.6.4 of JIS C 5101-10
Voltage shall be applied as per Table1.
Table 1
<table><tr><th>Charac-teristic</th><th>Voltage</th></tr><tr><td>A, AN</td><td>300% Rated voltage</td></tr><tr><td>CN</td><td rowspan="2">250% Rated voltage</td></tr><tr><td>FN</td></tr></table>Voltage shall be applied for 1 to 5s with 50mA charging and discharging current.</td></tr>
<tr><td>3</td><td>Insulation resistance</td><td colspan="2">Not less than 10000MΩ or 500MΩ・μF, whichever is less.
(For products with rated voltage less than 16V, it is not less than 10000MΩ or 100MΩ・μF, whichever is less.)</td><td>As per 4.5 of JIS C 5101-1.
As per 4.6.3 of JIS C 5101-10
Measurements shall be made after 60+/–5s period of the rated voltage applied.</td></tr>
<tr><td>4</td><td>Capacitance</td><td colspan="2">Capacitance shall be within specified tolerance range.</td><td>As per 4.7 of JIS C 5101-1.
As per 4.6.1 of JIS C 5101-10
Measurements shall be made under the conditions specified in Table 2.
Table 2
<table><tr><th>Charac-teristic</th><th colspan="2">Frequency・Voltage</th></tr><tr><td rowspan="2">A, AN</td><td>≤ 1000pF</td><td>> 1000pF</td></tr><tr><td>1+/–0.1MHz
1+/–0.1Vrms.</td><td>1+/–0.1kHz
1+/–0.1Vrms.</td></tr><tr><td>CN</td><td>≤ 10μF</td><td>> 10μF</td></tr><tr><td>FN</td><td>1+/–0.1KHz
1+/–0.1Vrms.</td><td>120+/–24Hz
0.5+/–0.2Vrms.</td></tr></table></td></tr>
<tr><td rowspan="3">5</td><td rowspan="3">Dielectric loss tangent</td><td>A, A N</td><td>tan δ ≤ 0.1%</td><td rowspan="3">As per 4.8 of JIS C 5101-1.
As per 4.6.2 of JIS C 5101-10
Measurements shall be made under the conditions specified in Table 2.</td></tr>
<tr><td>C N</td><td>Rated voltage≥25V
tan δ ≤ 3.0%
Rated voltage=16,10V
tan δ ≤ 5.0%
Rated voltage=6.3V
tan δ ≤ 10.0%</td></tr>
<tr><td>F N</td><td>Rated voltage=50V
tan δ ≤ 5.0%
Rated voltage=25V
tan δ ≤ 7.5%
Rated voltage=16V
tan δ ≤ 10.0%
Rated voltage=10V,6.3V
tan δ ≤ 12.5%</td></tr>
</table>

<table>
<tr><th>No.</th><th colspan="2">Items</th><th colspan="3">Performance</th><th>Test Method
(As per JIS C 5101-1, JIS C 5101-10)</th></tr>
<tr><td>6</td><td colspan="2">Temperature characteristic</td><td>A, A N</td><td colspan="2">0+/–30ppm / °C
(–55°C to +125°C)</td><td>As per 4.24 of JIS C 5101-1.
As per 4.7 of JIS C 5101-10
Temperature coefficient shall be calculated at 20°C and 85°C.</td></tr>
<tr><td></td><td colspan="2"></td><td rowspan="3">C N</td><td>X7R・R</td><td>+/–15%
(–55°C to +125°C)</td><td rowspan="3">As per 4.24 of JIS C 5101-1.
As per 4.7 of JIS C 5101-10
If required, measurements shall be made at a given temperature.</td></tr>
<tr><td></td><td colspan="2"></td><td>B</td><td>+/–10%
(–25°C to +85°C)</td></tr>
<tr><td></td><td colspan="2"></td><td>X5R</td><td>+/–15%
(–55°C to +85°C)</td></tr>
<tr><td></td><td colspan="2"></td><td rowspan="2">F N</td><td colspan="2">+30%, –80%
(–25°C to +85°C)</td><td rowspan="2"></td></tr>
<tr><td></td><td colspan="2"></td><td colspan="2">+22%, –82%
(–30°C to +85°C)</td></tr>
<tr><td>7</td><td colspan="2">Solderability</td><td colspan="3">More than 3/4 of each end termination shall be covered with new solder.</td><td>As per 4.15.2 of JIS C 5101-1.
As per 4.11 of JIS C 5101-10
The solder specified in JIS Z 3282 H63A shall be used. Ans the flux containing 25% rosin and ethanol solution shall be used.
The specimens shall be immersed into the solder at 235+/–5°C for 2+/–0.5s
So that both end terminations are completely under solder.</td></tr>
<tr><td>8</td><td rowspan="6">Resistance to soldering heat</td><td>Appearance</td><td colspan="3">Without mechanical damage.</td><td rowspan="6">As per 4.14 of JIS C 5101-1.
As per 4.10 of JIS C 5101-10
The solder specified in JIS Z 3282. H63A shall be used.
The specimens shall be immersed into the solder at 260+/–5°C for 5+/–0.5s so that both end terminations are completely under the solder.
Pre-heating at 150+/–10°C for 1 to 2min
Initial measurements prior to test shall be performed after the thermal
Pre-conditioning specified in Remarks (1).
Final measurements shall be made after the specimens have been left at room temperature as per Table3.
Table3
<table><tr><th>Charac-teristic</th><th>Time</th></tr><tr><td>A, AN</td><td>24+/–2 h</td></tr><tr><td>CN, FN</td><td>48+/–4 h</td></tr></table></td></tr>
<tr><td></td><td rowspan="3">Change rate from initial value</td><td>A, A N</td><td>Within +/–2.5%</td></tr>
<tr><td></td><td>C N</td><td>Within +/–7.5%</td></tr>
<tr><td></td><td>F N</td><td>Within +/–20%</td></tr>
<tr><td></td><td>Dielectric loss tangent</td><td colspan="3">Within specified initial value.</td></tr>
<tr><td></td><td>Insulation resistance</td><td colspan="3">Within specified initial value.</td></tr>
<tr><td></td><td>Withstanding voltage</td><td colspan="3">No defects shall be allowed.</td></tr>
<tr><td>9</td><td colspan="2">End termination adherence</td><td colspan="3">Without peeling or sign of peeling shall be allowed on the end terminations.</td><td>As per 4.13 of JIS C 5101-1.
As per 4.8 ofJIS C 5101-10
A 5N weight for 10+/–1s shall be applied to the soldered specimens as shown by the arrow mark in the below sketch.
 
</td></tr>
</table>

<table>
<tr><th>No.</th><th colspan="2">Items</th><th colspan="3">Performance</th><th>Test Method
(As per JIS C 5101-1, JIS C 5101-10)</th></tr>
<tr><td>10</td><td>Bending strength</td><td>Appearance</td><td colspan="3">Without mechanical damage.</td><td>As per 4.35 of JIS C 5101-1.
As per 4.9 of JIS C 5101-10
Glass epoxy board with soldered specimens shall be bent till 1mm by 1.0mm/s.</td></tr>
<tr><td rowspan="5">11</td><td rowspan="5">Vibration</td><td>Appearance</td><td colspan="3">Without mechanical damage.</td><td rowspan="5">As per 4.17 of JIS C 5101-1.
The specimens shall be soldered on the specified test jig.
Initial measurements shall be made after the thermal pre-conditioning specified in Remarks(1).
Final measurements shall be made after the specimens have been left at room temperature as per Table3.
[Condition]
Directions : 2h each X, Y and Z directions
Total : 6h
Frequency range : 10 to 55 to 10Hz(1min)
Applitude : 1.5mm
(shall not exceed acceleration196m/s^2)
Table3
<table><tr><th>Charac-teristic</th><th>Time</th></tr><tr><td>A, AN</td><td>24+/–2 h</td></tr><tr><td>CN, FN</td><td>48+/–4 h</td></tr></table></td></tr>
<tr><td rowspan="3">Change rate from initial value</td><td>A, A N</td><td>Capacitance shall be within specified tolerance range.</td></tr>
<tr><td>C N</td><td>Within +/–7.5%</td></tr>
<tr><td>F N</td><td>Within +/–20%</td></tr>
<tr><td>Dielectric loss tangent</td><td colspan="2">Within specified initial value.</td></tr>
<tr><td rowspan="8">12</td><td rowspan="8">Temperature cycling</td><td>Appearance</td><td colspan="3">Without mechanical damage.</td><td rowspan="8">As per 4.16 of JIS C 5101-1
As per 4.12 of JIS C 5101-10
The specimens shall be soldered on the test jig shown in Remarks.
Temperature cycle : 100cycles Initial measurements prior to test shall be performed after the thermal per-conditioning specified in Remarks (1).
Final measurements shall be made after the specimens have been left at room temperature as per Table3.
Test condition
<table><tr><th>Step</th><th>Temp. (°C)</th><th>Time (min)</th></tr><tr><td>1</td><td>Min operating temp.</td><td>30+/–3</td></tr><tr><td>2</td><td>Room temp.</td><td>≤ 3</td></tr><tr><td>3</td><td>Max operating temp.</td><td>30+/–3</td></tr><tr><td>4</td><td>Room temp.</td><td>≤ 3</td></tr></table>
Table3
<table><tr><th>Charac-teristic</th><th>Time</th></tr><tr><td>A, AN</td><td>24+/–2 h</td></tr><tr><td>CN, FN</td><td>48+/–4 h</td></tr></table></td></tr>
<tr><td rowspan="4">Change rate from initial value</td><td colspan="2">A, A N</td><td>Within +/–2.5%</td></tr>
<tr><td rowspan="2">C N</td><td>Rated vcoltage 25V,16V,10V</td><td>Within +/–7.5%</td></tr>
<tr><td>Rated vcoltage 6.3V</td><td>Within +/–15%</td></tr>
<tr><td colspan="2">F N</td><td>Within +/–20%</td></tr>
<tr><td>Dielectric loss tangent</td><td colspan="3">Within specified initial value.</td></tr>
<tr><td>Insulation resistance</td><td colspan="3">Within specified initial value.</td></tr>
<tr><td>Withstanding voltage</td><td colspan="3">No defects shall be allowed.</td></tr>
</table>

<table>
<tr><th>No.</th><th colspan="2">Items</th><th colspan="3">Performance</th><th>Test Method
(As per JIS C 5101-1, JIS C 5101-10)</th></tr>
<tr><td rowspan="8">13</td><td rowspan="8">Humidity (Steady)</td><td>Appearance</td><td colspan="3">Without mechanical damage.</td><td rowspan="8">As per 4.22 of JIS C 5101-1
JIS C 5101-10
Test temperature : 60+/–2°C
Relative humidity : 90 to 95%
Test time : 500 +24/–0 h
Initial measurements prior to test shall be made after the voltage pre-conditioning specified in Remarks (2).
Final measurements have been left at room temperature as per Table3.

Table3
<table><tr><th>Charac-teristic</th><th>Time</th></tr><tr><td>A, AN</td><td>24+/–2 h</td></tr><tr><td>CN, FN</td><td>48+/–4 h</td></tr></table></td></tr>
<tr><td rowspan="4">Change rate from initial value</td><td colspan="2">A, A N</td><td>Within +/–5.0%</td></tr>
<tr><td rowspan="2">C N</td><td>Rated voltaeg 25V,16V,10V</td><td>Within +/–12.5%</td></tr>
<tr><td>Rated voltaeg 6.3V</td><td>Within +/–25.0%</td></tr>
<tr><td colspan="2">F N</td><td>Within +/–30%</td></tr>
<tr><td rowspan="3">Dielectric tangent</td><td colspan="2">A, A N</td><td>tan δ ≤ 0.3%</td></tr>
<tr><td colspan="2">C N</td><td>Less than 200% of initial spec.</td></tr>
<tr><td colspan="2">F N</td><td>Less than 150% of initial spec.</td></tr>
<tr><td>Insulation resistance</td><td colspan="3">Not less than 1000MΩ or 50MΩ・μF, whichever is less.
(For products with rated voltage less than 16V, it is not less than 1000MΩ or 10MΩ・μF, whichever is less.)</td></tr>
<tr><td rowspan="8">14</td><td rowspan="8">Humidity life test</td><td>Appearance</td><td colspan="3">Without mechanical damage.</td><td rowspan="8">As per 4.22 of JIS C 5101-1
As per 4.14 of JIS C 5101-10
Test temperature : 60+/–2°C
Relative humidity : 90 to 95%
Voltage : Rated voltage
Test time : 500 +24/–0 h
Initial measurements prior to test shall be made after the voltage pre-conditioning specified in Remarks (2).
Final measurements shall be made after the specimens have been left at room temperature as per Table3.

Table3
<table><tr><th>Charac-teristic</th><th>Time</th></tr><tr><td>A, AN</td><td>24+/–2 h</td></tr><tr><td>CN, FN</td><td>48+/–4 h</td></tr></table></td></tr>
<tr><td rowspan="4">Change rate from initial value</td><td colspan="2">A, A N</td><td>Within +/–7.5%</td></tr>
<tr><td rowspan="2">C N</td><td>Rated voltaeg 25V,16V,10V</td><td>Within +/–12.5%</td></tr>
<tr><td>Rated voltaeg 6.3V</td><td>Within +/–25.0%</td></tr>
<tr><td colspan="2">F N</td><td>Within +/–30%</td></tr>
<tr><td rowspan="3">Dielectric loss tangent</td><td colspan="2">A, A N</td><td>tan δ ≤ 0.5%</td></tr>
<tr><td colspan="2">C N</td><td>Less than 200% of initial spec.</td></tr>
<tr><td colspan="2">F N</td><td>Less than 150% of initial spec.</td></tr>
<tr><td>Insulation resistance</td><td colspan="3">Not less than 500MΩ or 25MΩ・μF, whichever is less.
(For products with rated voltage less than 16V, it is not less than 500mΩ or 5MΩ・μF, whichever is less.)</td></tr>
</table>

<table>
<tr><th>No.</th><th colspan="2">Items</th><th colspan="2">Performance</th><th>Test Method
(As per JIS C 5101-1, JIS C 5101-10)</th></tr>
<tr><td rowspan="8">15</td><td rowspan="8">Heat life test</td><td>Appearance</td><td colspan="2">Without mechanical damage.</td><td rowspan="8">As per 4.23 of JIS C 5101-1.
As per 4.15 of JIS C 5101-10
<table><tr><th></th><th>Test temperature(°C)</th><th>Voltage</th><th>Test time (h)</th></tr><tr><td>A, AN</td><td>125</td><td>200% Rated voltage</td><td>1000 +48/–0</td></tr><tr><td rowspan="3">CN</td><td>85 (B・X5R)</td><td>200% Rated voltage</td><td>1000 +48/–0</td></tr><tr><td>125 (B・X5R)</td><td>200% Rated voltage</td><td>1000 +48/–0</td></tr><tr><td>FN</td><td>85</td><td>200% Rated voltage</td><td>1000 +48/–0</td></tr></table>
Initial measurements prior to test shall be made after the voltage pre-conditioning specified in Remarks (2).
Final measurements shall be made after the specimens have been left at room temperature
Table3
<table><tr><th>Charac-teristic</th><th>Time</th></tr><tr><td>A, AN</td><td>24+/–2 h</td></tr><tr><td>CN, FN</td><td>48+/–4 h</td></tr></table></td></tr>
<tr><td rowspan="4">Change rate from initial value</td><td colspan="2">A, A N</td><td>Within +/–3.0%</td></tr>
<tr><td rowspan="2">C N</td><td>Rated voltage 25V,16V,10V</td><td>Within +/–15%</td></tr>
<tr><td>Rated voltage 6.3V</td><td>Within +/–25%</td></tr>
<tr><td colspan="2">F N</td><td>Within +/–30%</td></tr>
<tr><td rowspan="3">Dielectric loss tangent</td><td colspan="2">A, A N</td><td>tan δ ≤ 0.5%</td></tr>
<tr><td colspan="2">C N</td><td>Less than 200% of initial spec.</td></tr>
<tr><td colspan="2">F N</td><td>Less than 150% of initial spec.</td></tr>
<tr><td>Insulation resistance</td><td colspan="2">Not less than 1000MΩ or 50MΩ・μF, whichever is less.
(For products with rated voltage less than 16V, it is not less than 1000mΩ or 10MΩ・μF, whichever is less.)</td></tr>
</table>

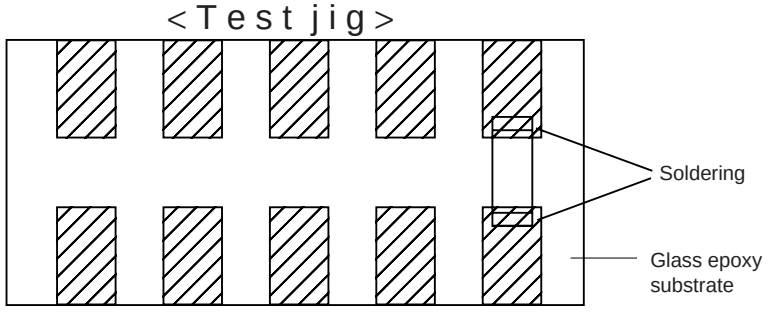
[Remarks]

Pre-conditioning

If specified in test method of as per 3(Performance and test merhod), capacitors of CN, FN characteristics shall be pre-conditionded as follows.

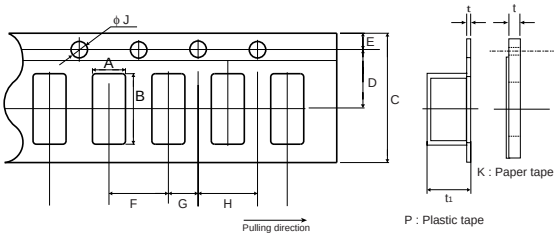
(1) Thermal pre-conditioning
Prior to initial measurements, specimens shall be conditioned at a temperature of 150 0/–10°C for a period of 1hr., and shall be allowed to stabilize at room temperature for 48+/–4h

(2) Voltage pre-conditioning
Prior to initial measurements, voltage specified as a test condition shall be applied to specimens for a period of 1hr., and the specimens shall be allowed to stabilize at room temperature for 48+/–4h



●Packaging specifications

Taping dimensions



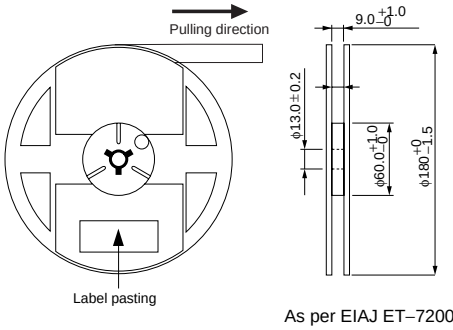
Symbol	C	D	E	F	G	H	J	t	t	t1
								Paper	Plastic	
Dimensions	8.0 +/ −0.3	3.5 +/ −0.05	1.75 +/ −0.1	4.0 +/ −0.1	2.0 +/ −0.05	4.0 +/ −0.1	ϕ1.5 +0.1/ −0	1.1 MAX.	0.3 MAX.	2.5 MAX.

(Unit : mm)

Style \ Symbol	A	B
MCH31	1.9+/–0.2	3.5+/–0.2

(Unit : mm)

Reel dimensions



As per EIAJ ET−7200A

(Unit : mm)

(1) The quantity for one reel is as bellows.

Kind of reel	Series	Paper tape		Plastic tape	
		Quantity	Symbol	Quantity	Symbol
ϕ180 reel	MCH31	4,000 pcs.	K	2,000 pcs.	P

(2) When the tape is pulled out towards the operator with the cover tape facing upward, the feeding holes shall be found on the right portion of the tape.
(3) Specification of beginning and ending of the tape are as follows.
Ending(reel's center) : Approx. Over 160mm (no chips)
Beginning(reel's round) : Approx. Over 160mm (no chips)
: Approx. 240mm (cover tape only)
(4) No juncture of tape shall be allowed.
(5) The share strength of tape shall be more than 5N at the break down strength.
(6) The peel strength of the cover tape shall be 0.1 to 0.7(N) when the cover tape are peeled 0 to 15° degree from the surface.
(7) The number of missing components shall not exceed 0.1% of the total number of components (marked number) or one whichever is the larger, and no consecutive missing exceeding two is allowed.
(8) The reels made from resin shall be used, as per EIAJ ET-7200A.

●Marking

No marking shall be performed on the chip.

Trademark, parts number, quantity, lot No. , and country of origin shall be labeled on each reel.

●Numbering system for LOT No.

Example 04 (1) 01 (2) A0001 (3) F (4)

(1) The end of the Christian Era < two digits> of production finish.
(2) Week in completing part of production finish.
(3) Manufacture continuity number.
(4) The symbol of manufacturing plant.

●Label expression

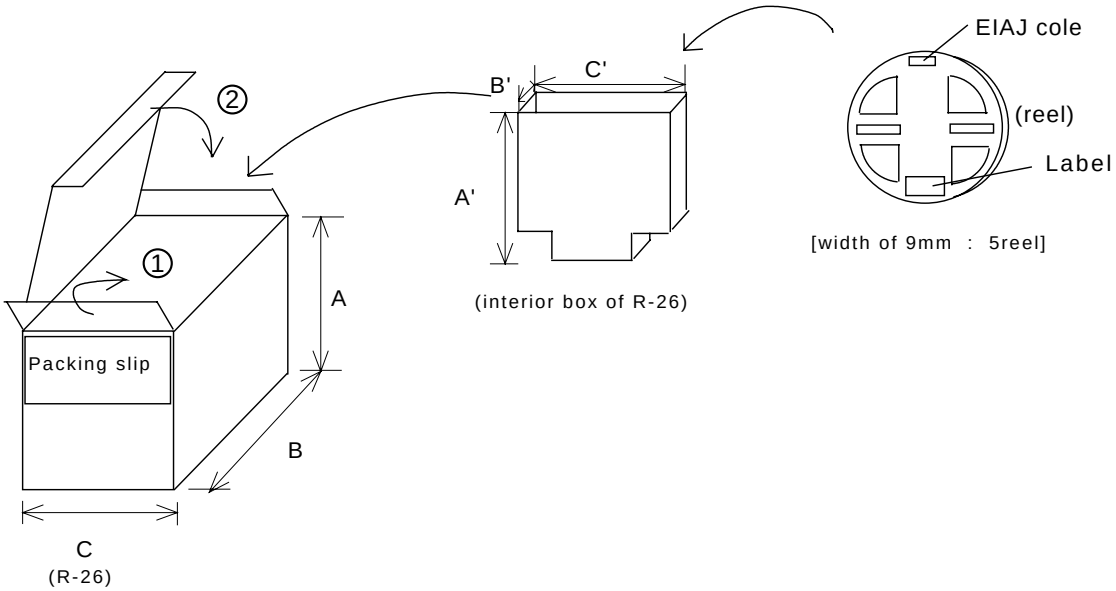
The Figure below is label expression

< Label Example > Part Number : MCH315A103JK

① Part Number
② Division cord
③ Quantity
④ Lot No.
⑤ The Country of origin
⑥ Inspector
⑦ QR code
⑧ Trademark

●Packing method

1) ϕ180mm Reel



< Packaging unit >

Symbol	K
Quantity of reel in interior box	5
Quantity of reel in box of R-26	20

Dimensions	Packaging	
	R-26	interior box of R-26
A (A')	195	185
B (B')	255	60
C (C')	190	185

(Unit : mm)

< Appearance >

Carton

< Accumulation >

You must do accumulation by ten boxes

< Packaging slip >

1. Customer
2. Parts number
3. Quantity
4. Box quantity
5. Trade mark

Notes

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