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226780-509

ELEC6234 - Embedded Processors

Lecturer: <>
Assignment: FPGA processor synthesis
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Receipt of paper copy not yet acknowledged.

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Submission Log

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             Name: Maxwell Fuller

          User id: mf5g14

             Date: Wed May  2 22:18 2018

All their own work: yes

    Submitted From: 81.100.247.215

      Logged on as: mf5g14

       Web browser: Mozilla/5.0 (Windows NT 6.3; Win64; x64) AppleWebKit/537.36 (KHTML, like Gecko) Chrome/65.0.3325.181 Safari/537.36


report.pdf: 747989 bytes from report.pdf, MD5 checksum fb01e28791d1a8fe5f622d6c18b7d5a0

code.zip: 11192 bytes from for_submission.zip, MD5 checksum 0ac4a362cb32fda6bdf6aeb334ea9ca7


Student's Plagiarism Statement

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This is all my own work and all sources have been correctly acknowledged.


Student's Comments

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None
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