TLA Inference/equivalence rules 1. Inv Next =) Inv' my DHext => DINV State action action predicate predicate All are temporal predicates. DF3F HF(Si) > F(Si) XFCSi,Si+1) > FCS1,S2) holds both when Fis State predicate or action predicate. F provable by proposition logic

Examples:
True, p3 pvq, x+x=x+x

Ut

5, -52 +53 + 54 -... Faga DF = DG Q Q Q Q 4 F(Si) 29 & G(Si) State predicate 4 F (Si, Si+1) 2) & G (Si, Si+1) action predict D(F^G) = OF ~ OG S- S2- S3- S4- S5- FFFE 9 9 9 9 Both F and G hold every state/action ● 6. D(F'G) ≠ DF V OG $S_1 \rightarrow S_2 \rightarrow S_3 \rightarrow S_4 \rightarrow \dots$ FAFF

D(F'G) holds. But DF'DG does not!

DF3 DFVG since F3 FVG

and rule 4

To prove TP Spec => TC Spec. Behaviors accepted by TP Spec are If suffices to show TPhit = TCMit TPNext => TCNext However, TCNext does not have actions for TM that TPNext has Such TPNext actions are a no-op TCNext TC is just "stattering"

Hence, Show TPINITY TCINIT TP Next =) TC Next V ym State= vmClate 2) [TCNext] mstate. Domo: manual proof / TCSpec is checked as property by TLC. Every action (edge) Initial State Satisfies

To Init

copair of states)

Satisff

Grand Amstate. How does TLC check TPSpec 2) TCSpec? what's next? calways Safety properties Dr Bad Hvings Bad things hever happen i,e, (- Bad thing) always happens Liveness properties Good things eventually happen eventually happen

Therefore > Safety = r & Bad Hung 9+ B not the case that bod thing eventually happens Similarly, Liveness = r - acod thing 9+ Ps not the Case that good thing never hopens Process X can be En ready queue & or performing some 10 operation 20 CPU can be busy with some proc. b or empty -b

D(-b^-r=310)

of CPU is empty and X is not

ready, men X is doing 10.

Process X does not suffer starration.

when It B Pn ready queue, it will evently

leave if

\(\tag{\gamma} \

V ~ Err C leads to Process X does not get permanently blocked doby 10.

- $\lozenge \square$ io $= \square \diamondsuit r$ io

It is not the case that 9t is always the case process eventually gets that the process is permanently stuck with 10 eventually not doing 10. $\lozenge \square P$ Eventually always p $S_1 \rightarrow S_2 \rightarrow S_3 \rightarrow S_4 \rightarrow ...$

 $\begin{array}{c}
\langle \Box F^{\prime} C \rangle = \langle \Box F ^{\prime} \rangle \langle \Box C \rangle \\
S_{1} \rightarrow S_{2} \rightarrow S_{3} \rightarrow S_{4} \rightarrow \dots \\
F C C F C
\end{array}$ $\begin{array}{c}
\langle \Box F^{\prime} C \rangle = \langle \Box F \rangle \langle \Box C \rangle \\
\downarrow F F^{\prime} C \rangle \\
\downarrow F C C F F \dots$

of course,

FVG holds

ODF 3 OD(FVG) since F=> FVG

