





Note:

- RL is connected to pin 79 of the FDD controller connector, but FDD controler doesn't use it. Most likely for testing, can be assumed always high (pull-up).
- ZO is a trace on the bottom layer between 2 pads, again, most likely for testing.
- 8 MHz oscillator is only assembled on computers with the FDD controller.

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Sheet: /clock/ File: clock.kicad_sch

Title: Elwro 800 Junior Rev C — clock generators

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