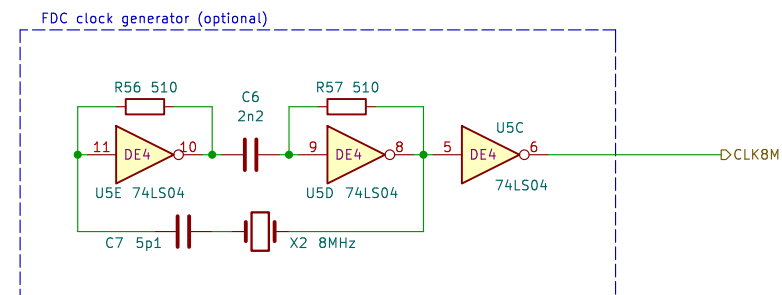
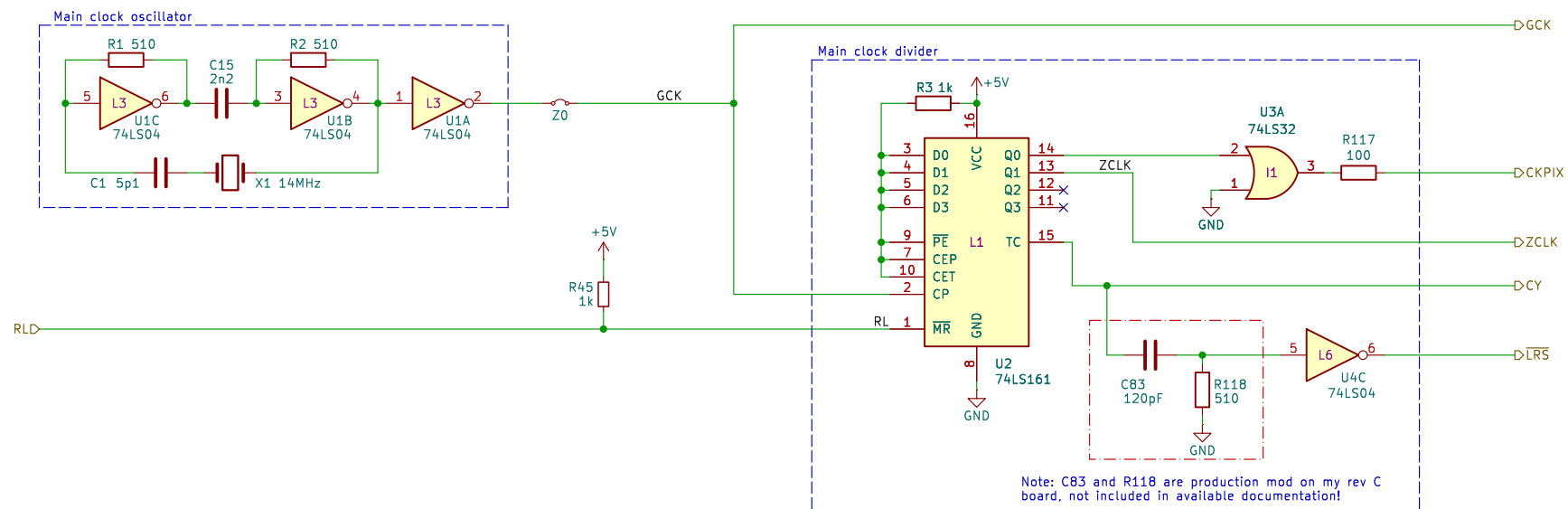


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Sheet: /
File: e800j_rev_c.kicad_sch

Title: Elwro 800 Junior Rev C

Size: A3	Date:	Rev:
KiCad E.D.A. 9.0.1		Id: 1/15



Note:

- RL is connected to pin 79 of the FDD controller connector, but FDD controller doesn't use it. Most likely for testing, can be assumed always high (pull-up).
- Z0 is a trace on the bottom layer between 2 pads, again, most likely for testing.
- 8 MHz oscillator is only assembled on computers with the FDD controller.

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Sheet: /clock/

File: clock.kicad_sch

Title: Elwro 800 Junior Rev C – clock generators

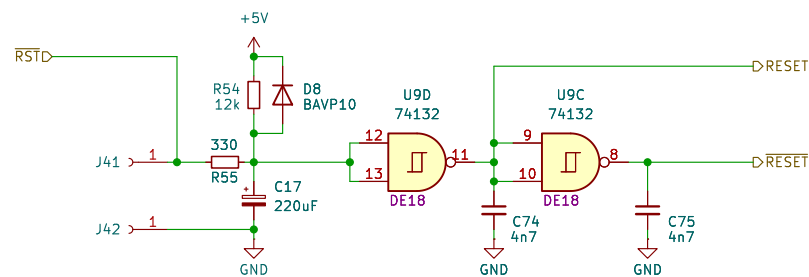
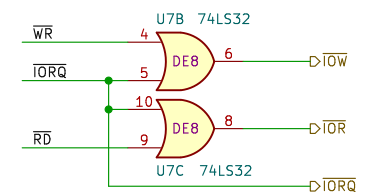
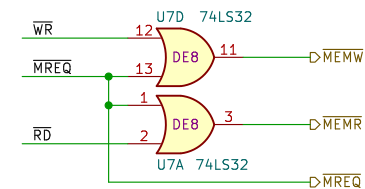
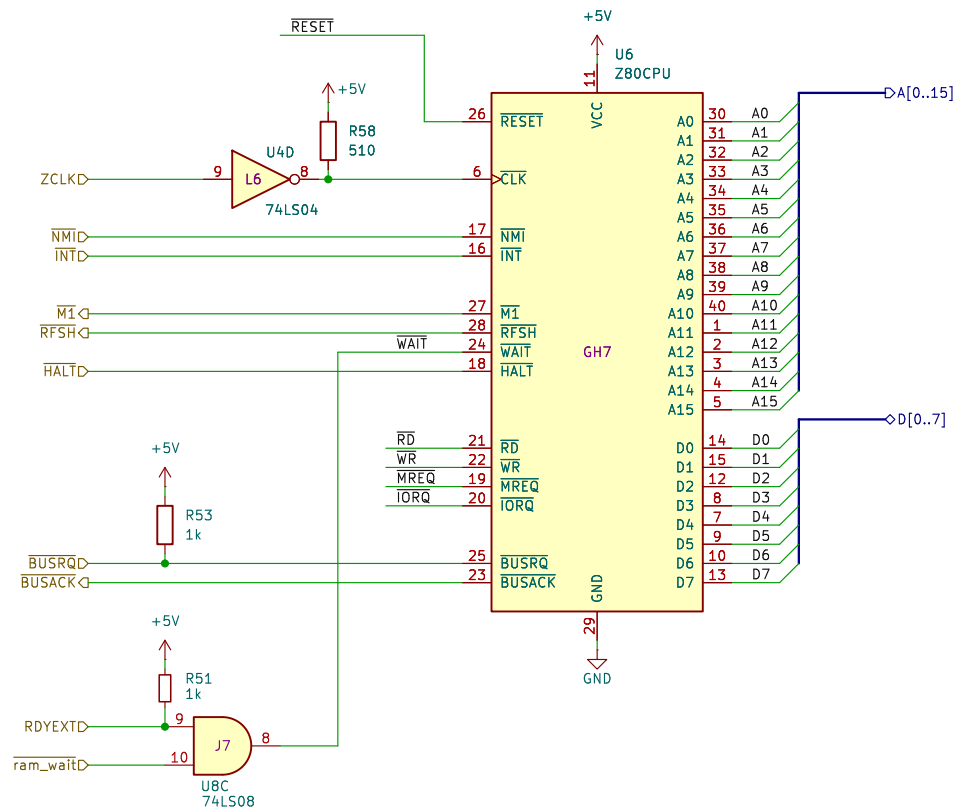
Size: A4

Date:

SIZE: A1	
KiCad E.D.A. 9.0.1	

Rev:

Id: 2/15



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Sheet: /cpu/
File: cpu.kicad_sch

Title: Elwro 800 Junior Rev C – CPU

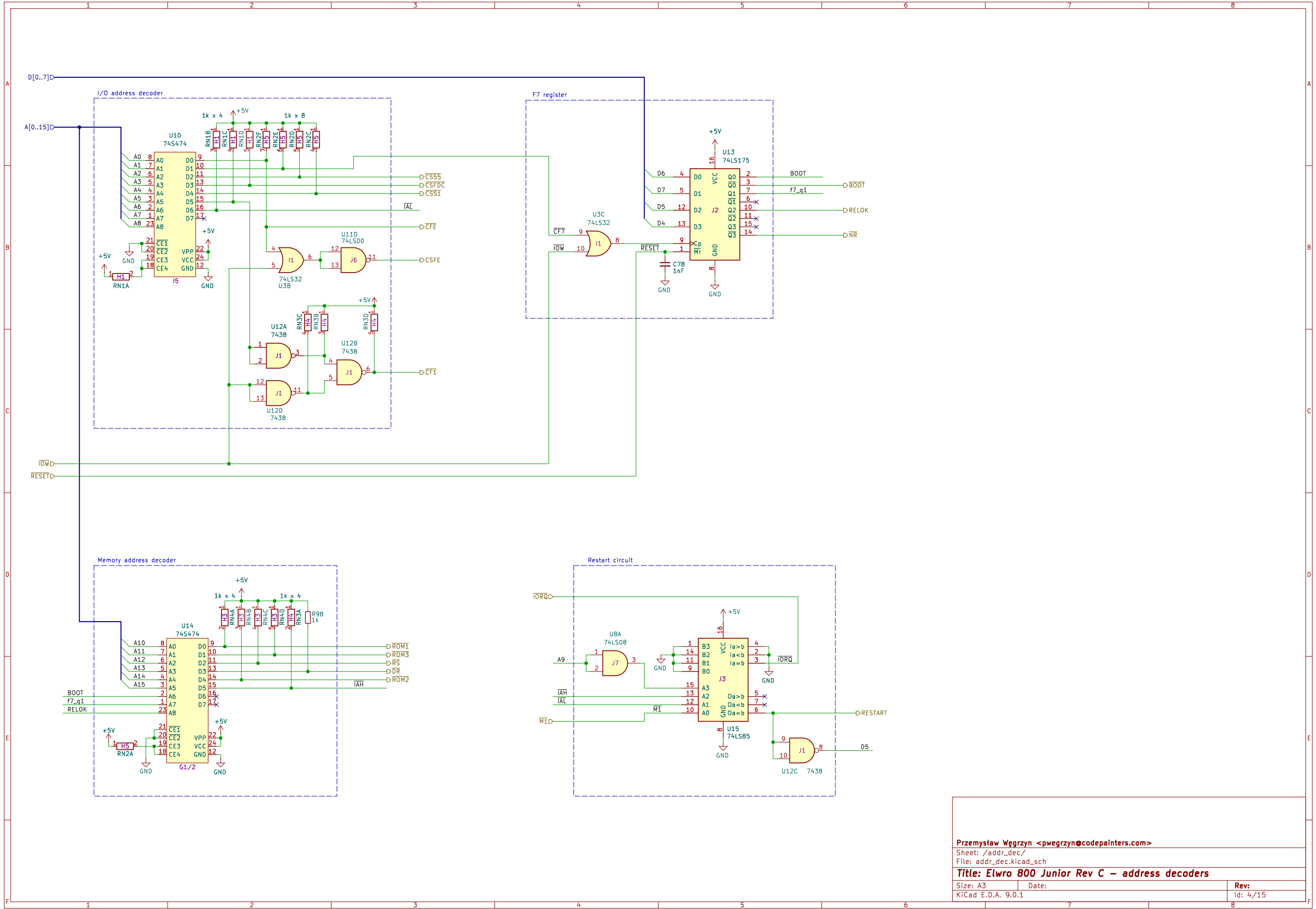
Size: A4

Date:

Rev:

KiCad E.D.A. 9.0.1

Id: 3/15

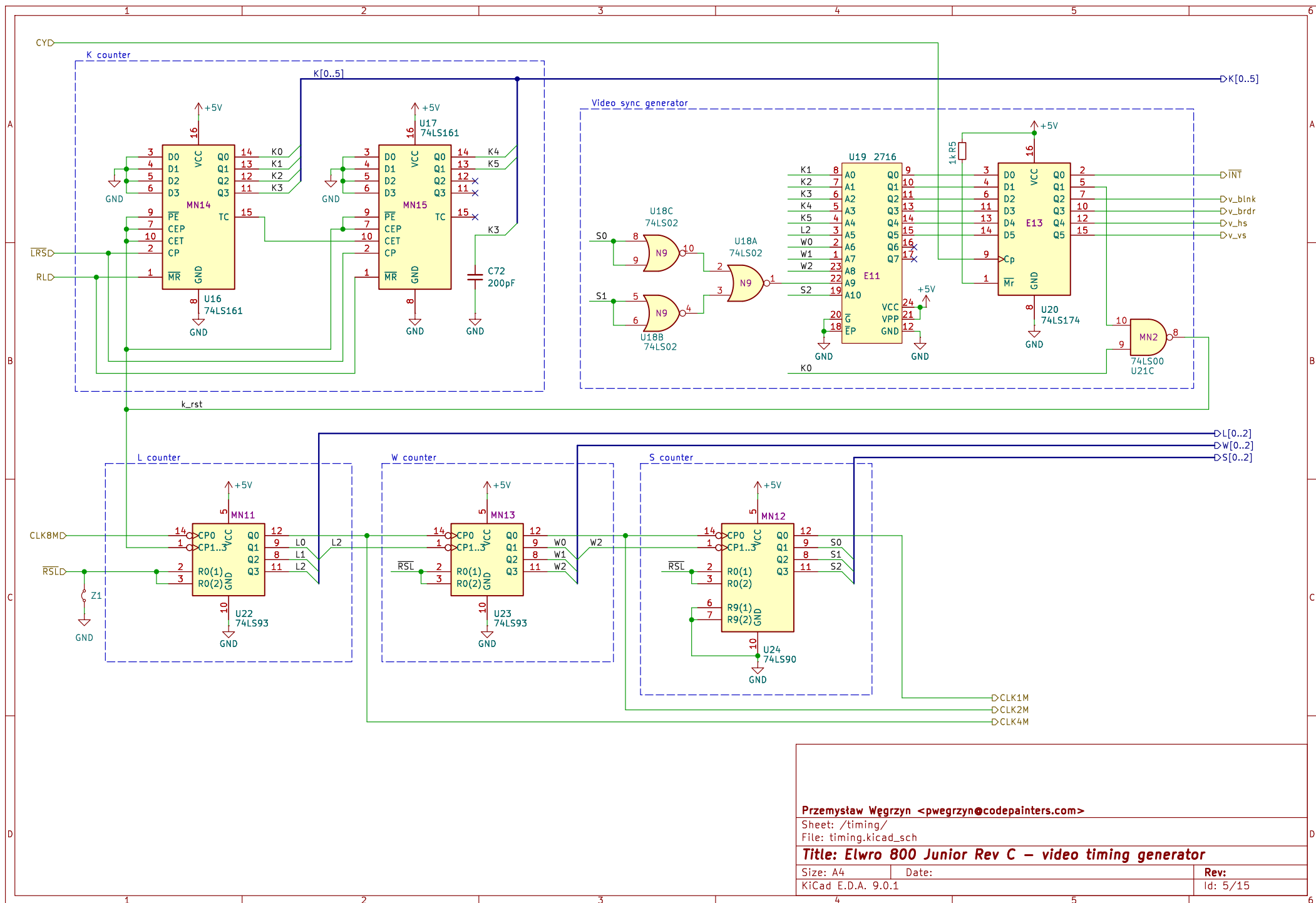


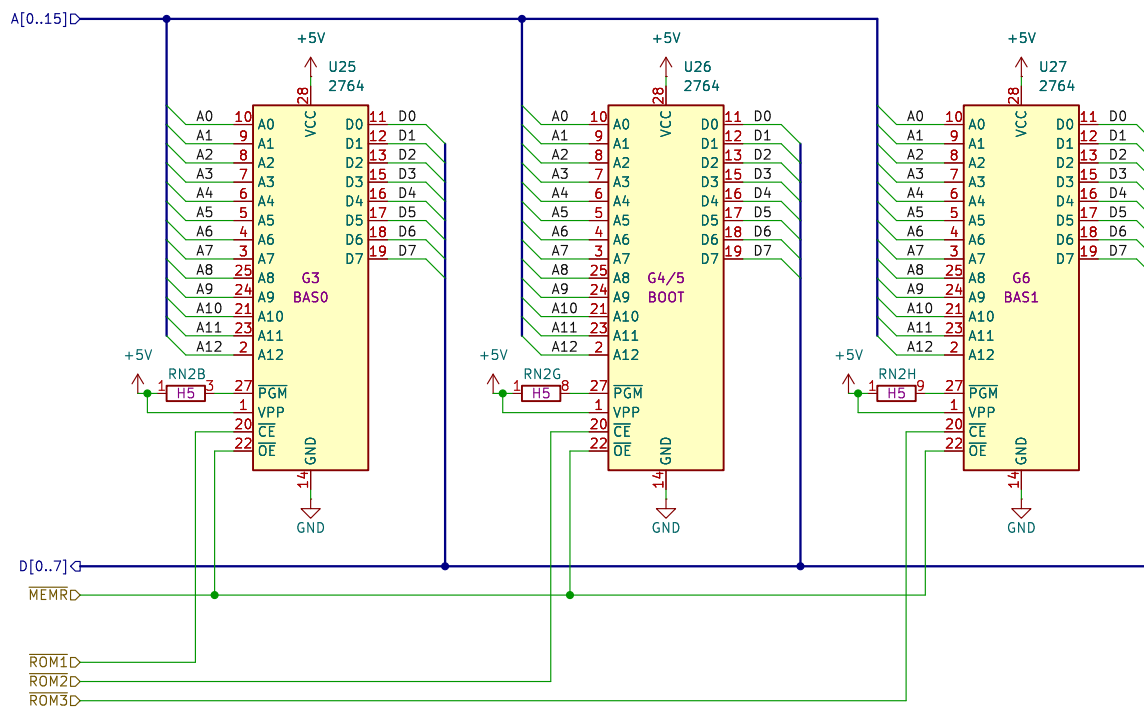
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Sheet: /addr_dec/
File: addr_dec.kicad_sch

Title: Elwro 800 Junior Rev C – address decoders

Size: A3	Date:	Rev:
KiCad E.D.A. 9.0.1		Id: 4/15





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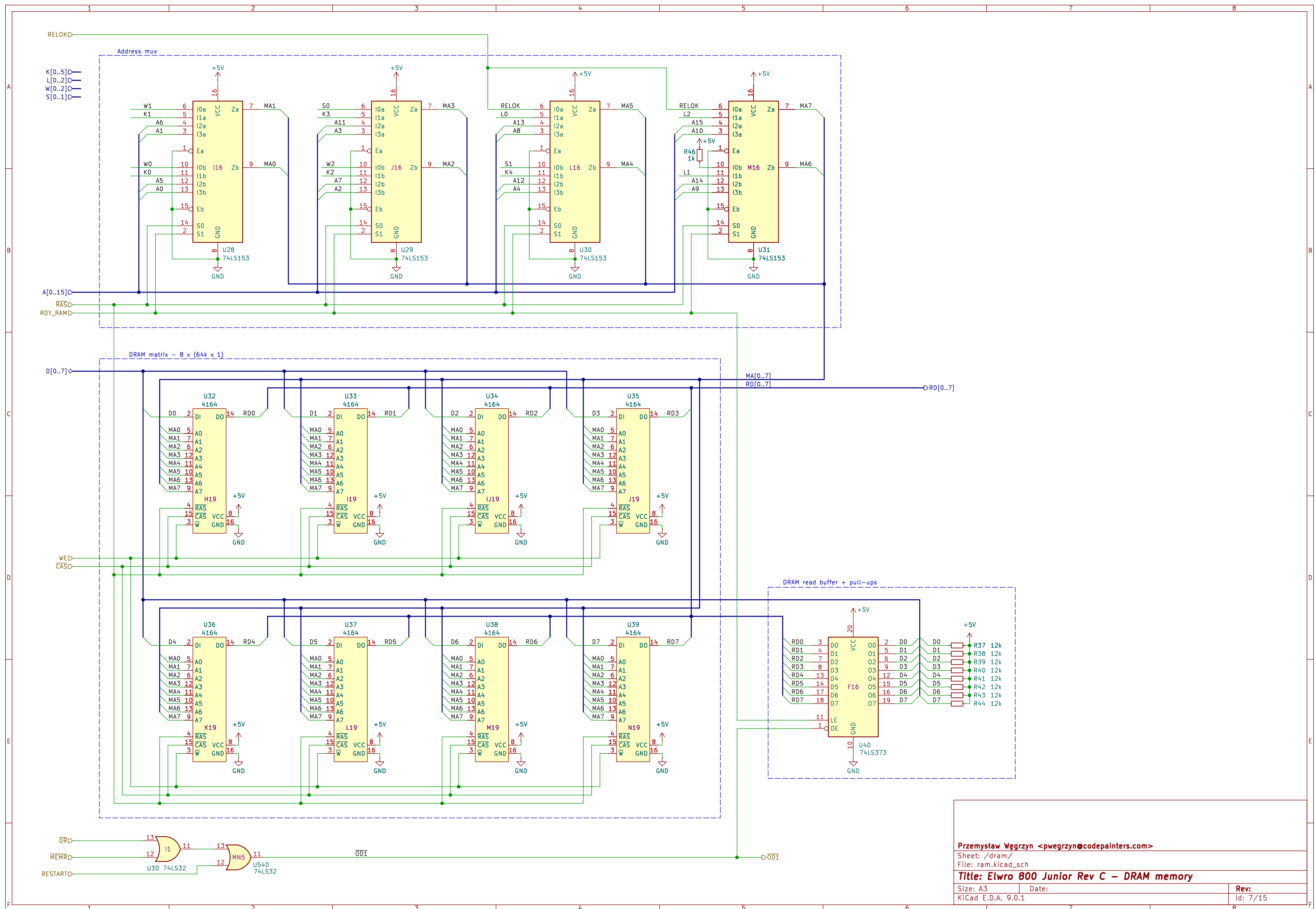
Sheet: /rom/
File: rom.kicad_sch

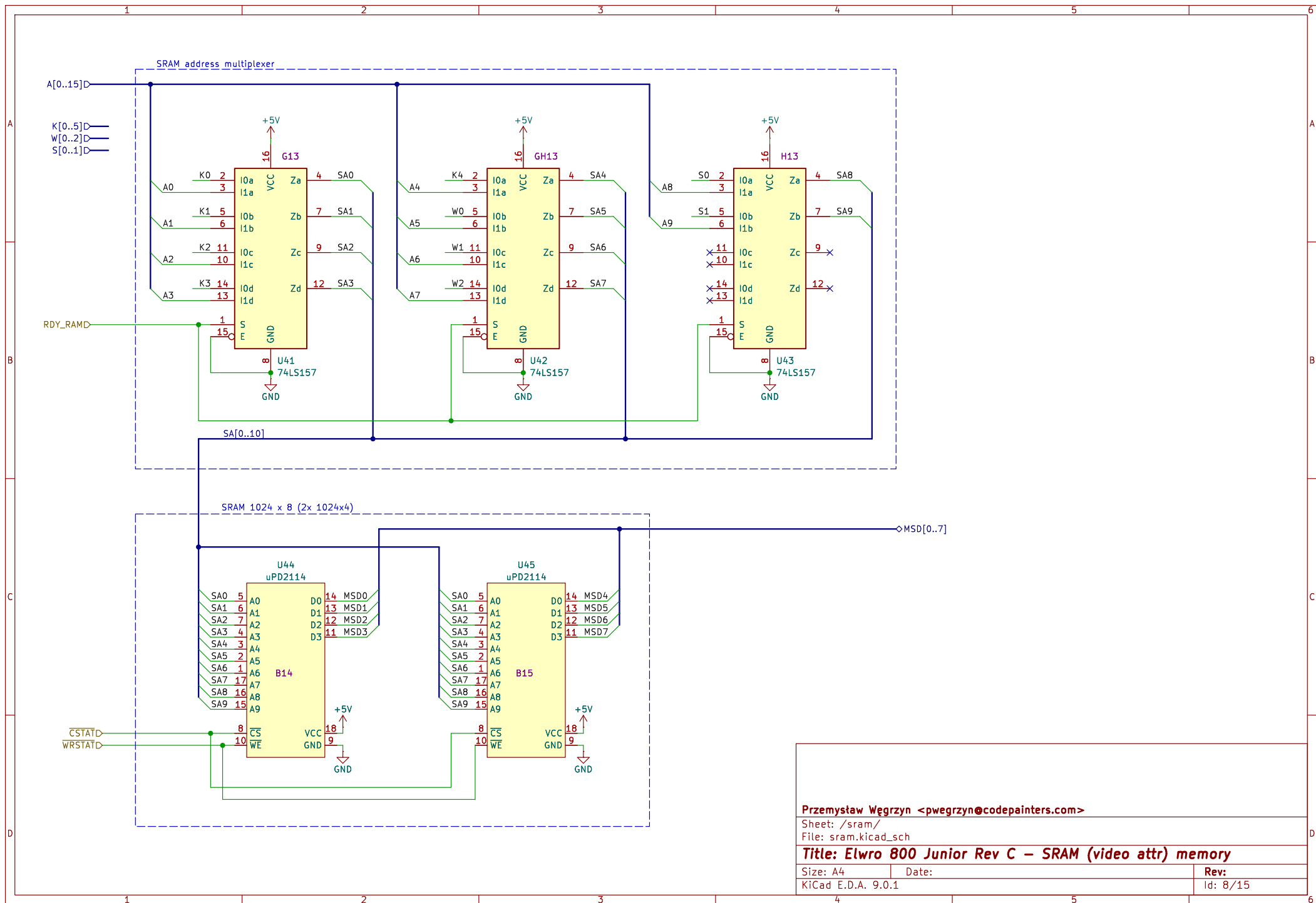
Title: Elwro 800 Junior Rev C – ROM memory

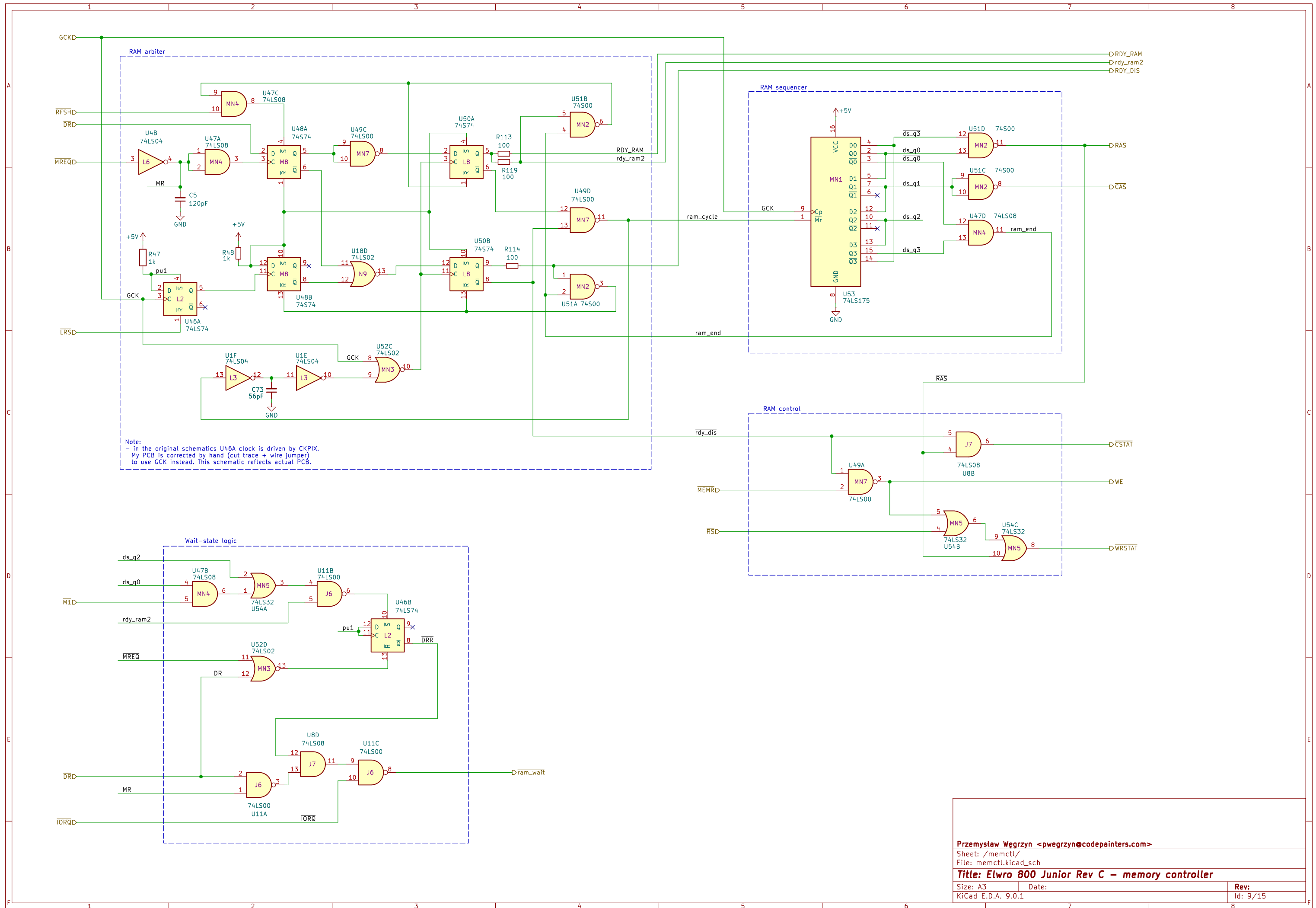
Size: A4
KiCad E.D.A. 9.0.1

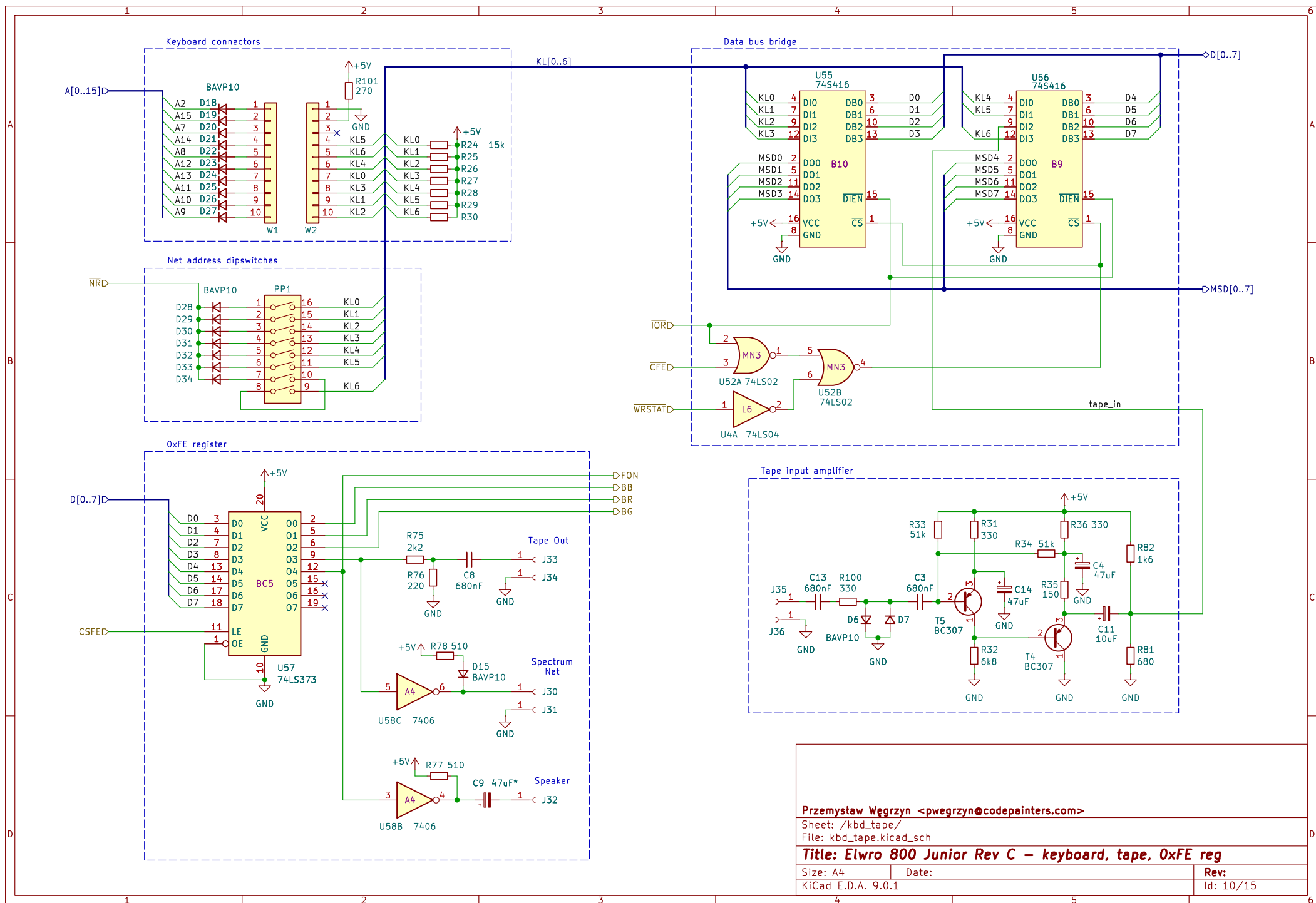
Date:

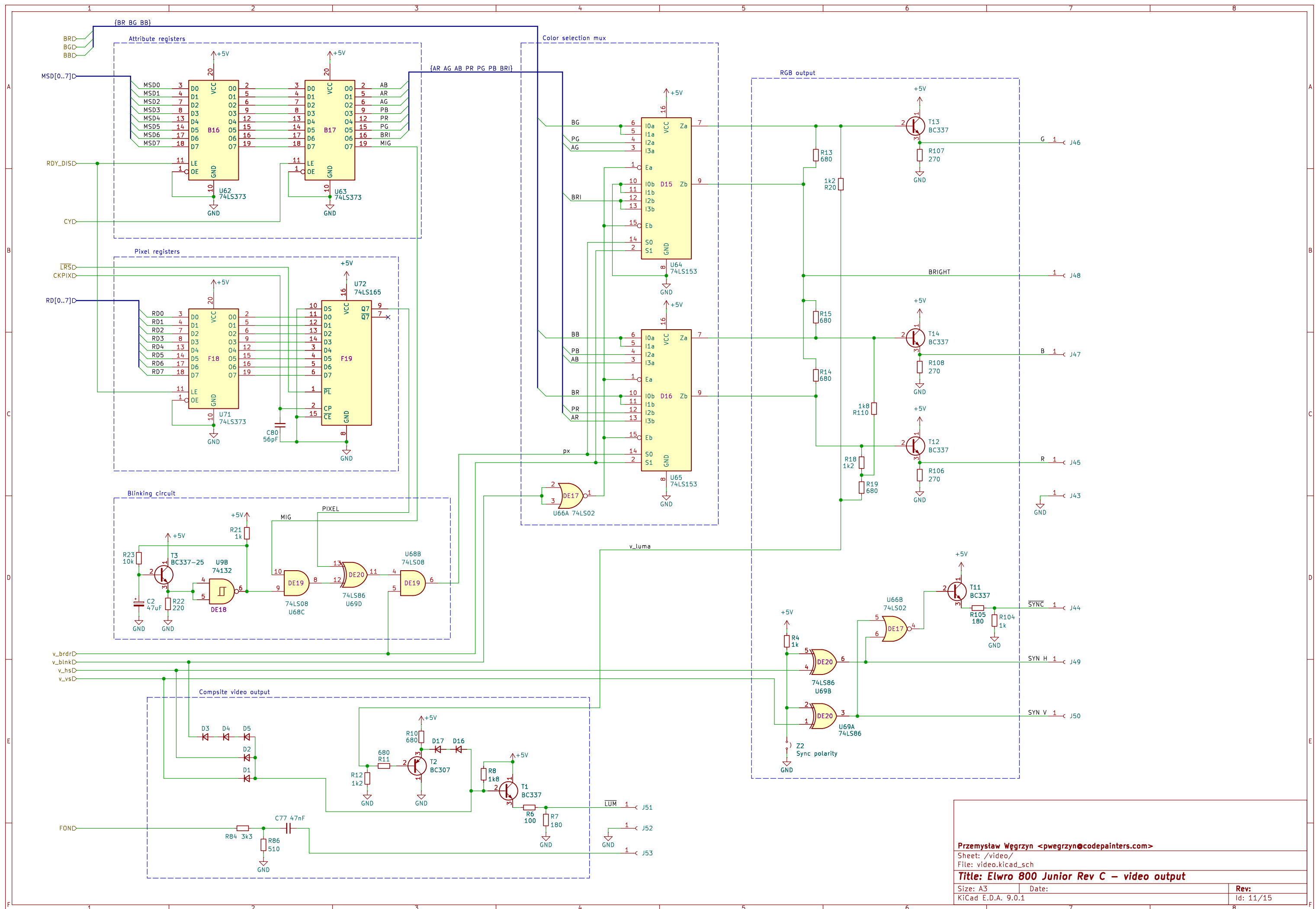
Rev:
Id: 6/15

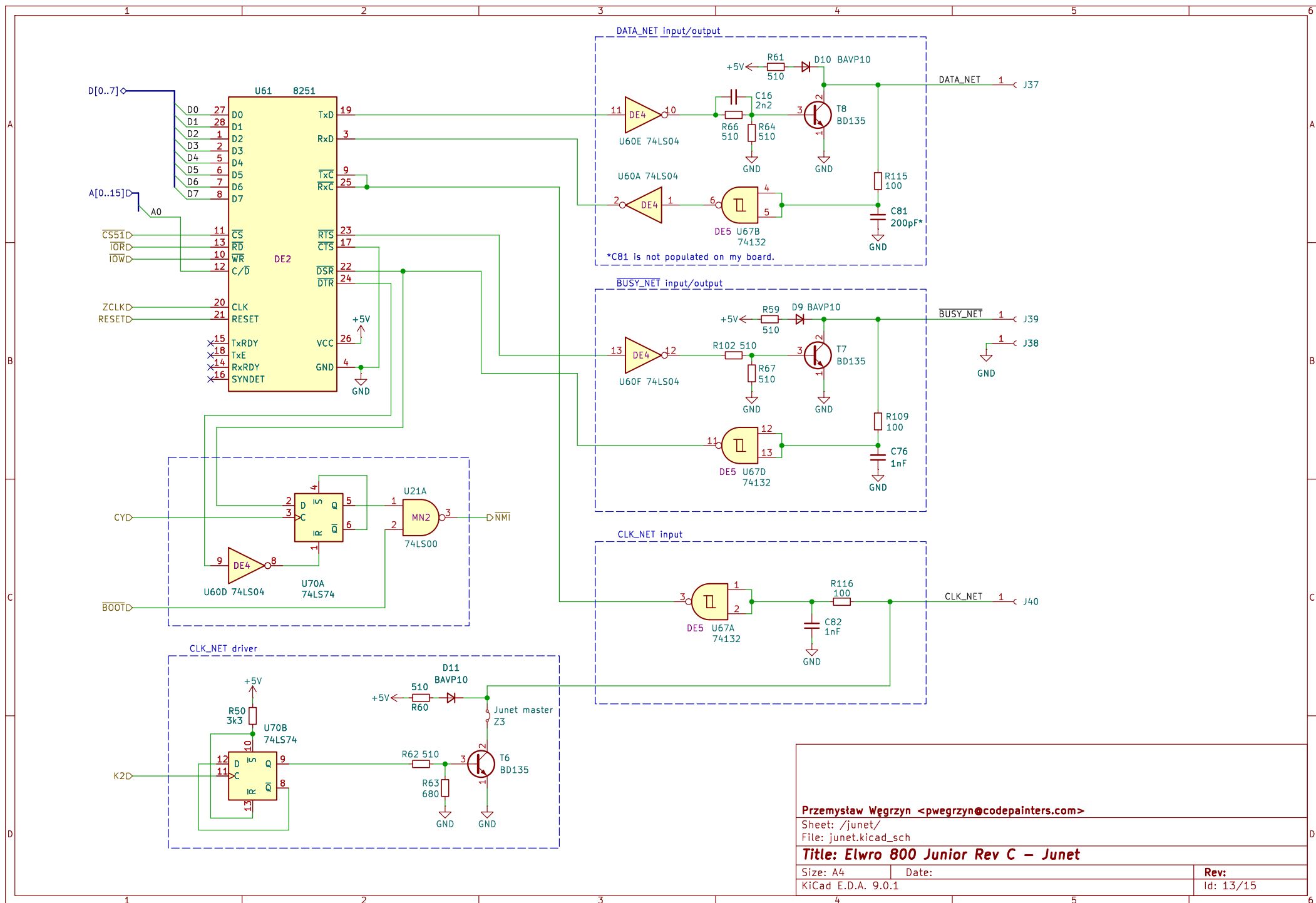












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Sheet: /junet/

File: junet.kicad_sch

Title: Elwro 800 Junior Rev C – Junet

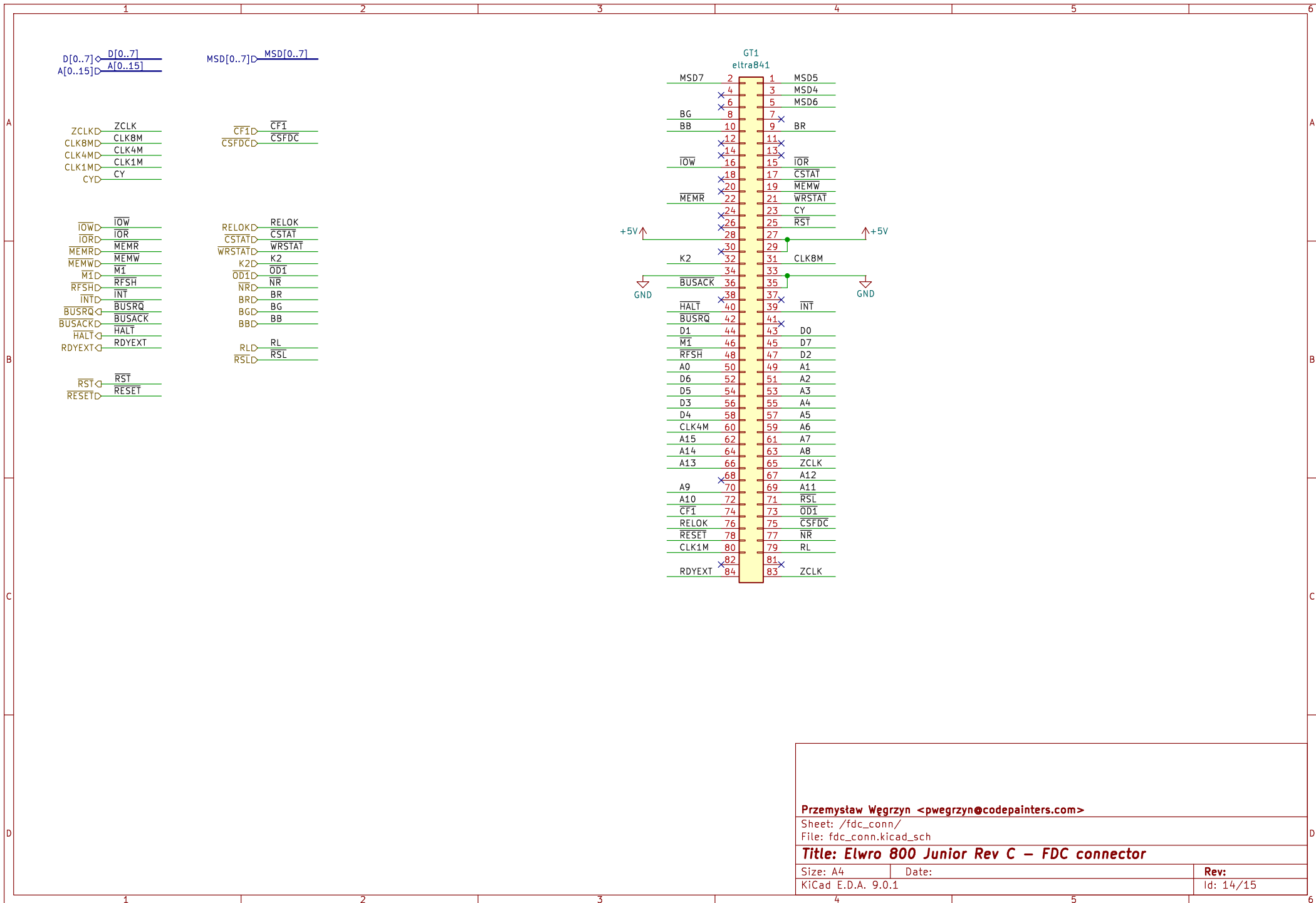
Size: A4

Date:

KiCad E.D.A. 9.0.1

Rev:

Id: 13/15



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Sheet: /fdc_conn/
File: fdc_conn.kicad_sch

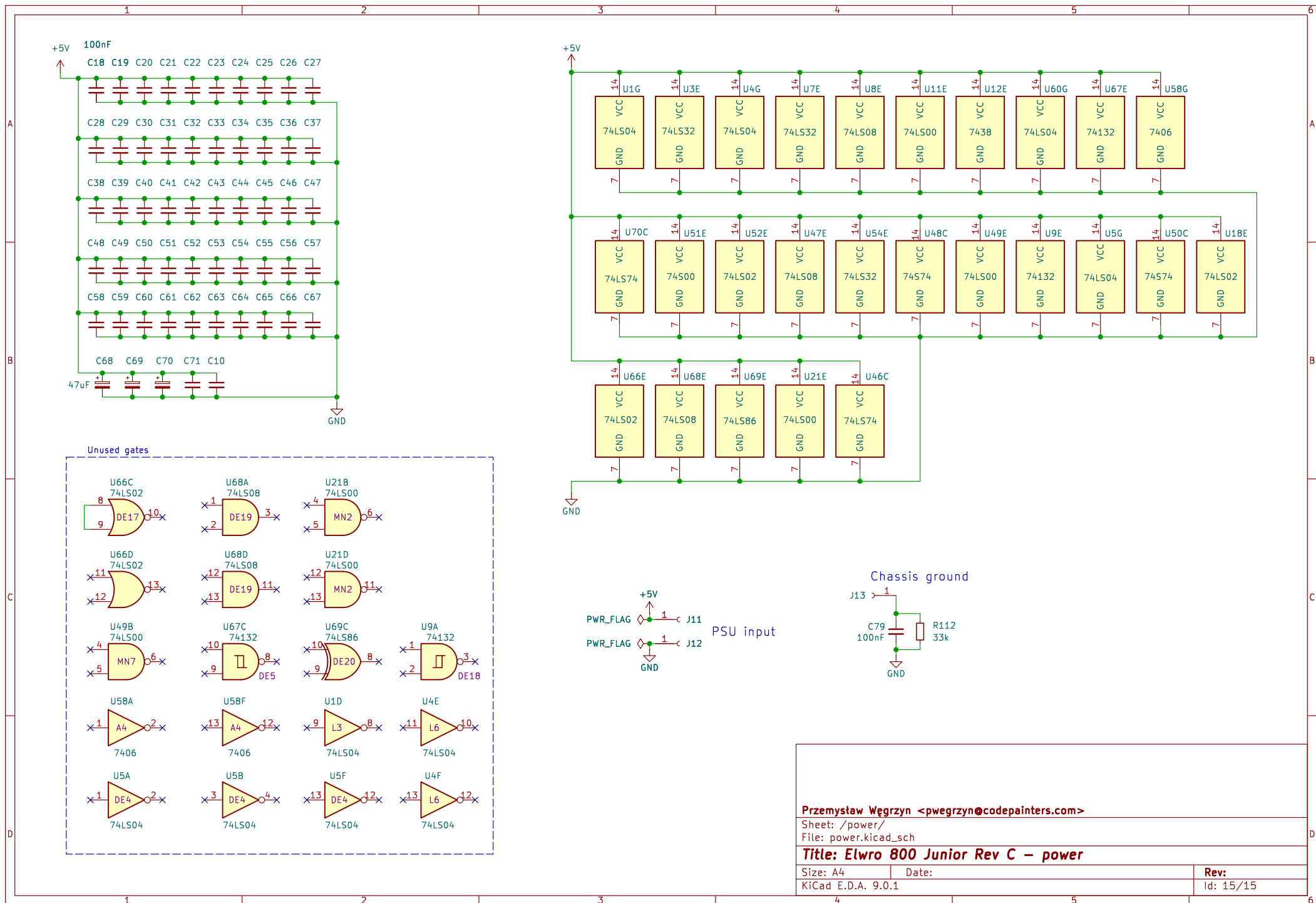
Title: Elwro 800 Junior Rev C – FDC connector

Size: A4
KiCad E.D.A. 9.0.1

Date:

Rev:

Id: 14/15



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Sheet: /power/
File: power.kicad_sch

Title: Elwro 800 Junior Rev C – power

Size: A4

Date:

KiCad E.D.A. 9.0.1

Rev:

Id: 15/15