MOS Data Area (Dated: 18.08.2019)

Address	Offset	Size	Description	
0x8000	0	1	Number of OS modules loaded.	
	1	2	1st Module Segment	Space for 10 Modules
	3	2	2nd Module Segment	
	...			
	19	2	10th Module Segment	
	21	2	Offset 1st Despatcher Module	Space for 50 Despatcher routines Used by INT 0x41
	23	2	Segment 1st Despatch Module	
	...			
	217	2	Offset 50th Despatcher Module	
	219	2	Segment 50th Despatch Module	
	221	36	Free	
	256	768	Loader	

Memory Map MOS

Size	Size	Function	Address
-		VGA Memory	A0000
1 KiB		EBDA	9FFFF 9FC00
606 KiB	127 KiB	EBDA/Free	9FBFF 80C00
	482 KiB	Free	80BFF 8400
	1 KiB 1024	MOS data area	83FF 8000
	512	Bootloader I/O Area	7FFF 7E00
	512	Bootloader	7DFF 7C00
	4 KiB 4096	Stack	7BFF 6C00
		Not used by MOS	CBFF