SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING: MYSORE - 570 006

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2016-17

M.Tech Industrial Electronics

AB 102

Day	07.30 - 08.30	08.30 - 09.30	09.30 - 10.30		11.00 - 11.50	11.50 – 12.40	12.40 - 01.30		02.30 - 03.30	03.30 - 04.30	04.30 - 05.30
Monday			LIE 130 BASK	$_{\rm BASK}$ $ 0 $		LIE 141 TS	LIE 110 NS	L	LIE 130L BASK		
Tuesday		LIE 141 TS	LIE 110 NS	F F	LIE 153 SG	LIE 120 NMR	LIE 130 BASK	$\begin{bmatrix} U \\ N \end{bmatrix}$	LIE 153L SG		
Wednesday			LIE 120 NMR	E E	LIE 130 BASK	LIE 153 SG	LIE 110 NS	H			
Thursday			LIE 153 SG	B	LIE 110 NS	LIE 141 TS	LIE 120 NMR	B	LIE 110T NS		
Friday			LIE 141 TS	RE	LIE 130 BASK	LIE 120 NMR	LIE 153 SG	R E	LIE 141L TS		
Saturday				A K		IE LAB1 VN		K			

LIE 110: Linear Algebra

LIE 120: Power Electronics

LIE 130: Embedded Systems

LIE 141: Advanced DSP

LIE 153: Advances in VLSI

LIE 16L: IE LAB1

(Head of the Department)

(Principal)

SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING: MYSORE – 570 006

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2016-17

M.Tech in Networking And Internet Engg. AB 203

Day	07.30 – 08.30	08.30 - 09.30	09.30 - 10.30		11.00 - 11.50	11.50 – 12.40	12.40 - 01.30		02.30 - 03.30	03.30 - 04.30	04.30 - 05.30
Monday			LNI 130 NS	$\begin{bmatrix} C \\ O \end{bmatrix}$	LNI 151 X10	LNI 141 NSP	LNI 110 CRN	L	LNI 130L X7		
Tuesday			LNI 120 TRM	F F	LNI 110 CRN	LNI 130 NS	LNI 151 X10	$\begin{bmatrix} U \\ N \end{bmatrix}$	LNI 141L NSP		
Wednesday			LNI 141 X10	E E	LNI 120 TRM	LNI 110 CRN	LNI 141 NSP	H			
Thursday			LNI 141 NSP	B R	LNI 151 X10	LNI 120 TRM	LNI 130 NS	B	LNI 110L CRN		
Friday			LNI 120 TRM	E	LNI 130 NS	LNI 151 NSP	LNI 110 CRN	$\begin{bmatrix} R \\ E \end{bmatrix}$	LNI 151L X10		
Saturday		LNI 1 TR		A K	LNI LAB1 K K						

LNI 110: Adv. Comm Networks LNI 120: Cryptography LNI 130: Network Programming

LNI 141: Storage Area Networks LNI 151: OOAD and JAVA LNI 16L: LNI LAB1

(Head of the Department) (Principal)

SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING: MYSORE – 570 006

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2016-17

M.Tech in Automotive Electronics

Day	07.30 – 08.30	08.30 - 09.30	09.30 - 10.30		11.00 - 11.50	11.50 – 12.40	12.40 – 01.30		02.30 - 03.30	03.30 - 04.30	04.30 - 05.30
Monday		MAL110 RS	MAL 120 X	$\begin{bmatrix} C \\ O \end{bmatrix}$	MAL 140 SPK	MAL 151 MNSS		L	MAL 151 (L) MNSS		
Tuesday		MAL110 RS	MAL110 RS	F	MAL 120 X	MAL 140 SPK	MAL 151 MNSS	$\begin{bmatrix} U \\ N \end{bmatrix}$	MAL 1 M	` '	
Wednesday		MAL110 RS		E E	MAL 130 KTV	MAL 130 KTV		H			
Thursday			MAL 140 SPK	B	MAL 151 MNSS	MAL 120 X	MAL 130 KTV	B	MAL 130 KTV		
Friday				R E	MAL 120 X	MAL 151 MNSS	MAL 140 SPK	R E		MAL 16L YSD	
Saturday		MAL110(T) RS	MAL110(T) RS	K	MAL 130(T) KTV	MAL 130 (T) KTV		K			

MAL 110: Mechatronics

MAL 120: Automotive Materials

MAL 130: Design of Automotive Dynamic System

MAL 140: Digital Control Systems

MAL 151: Embedded Systems

MAL 16L: Automotive Control System Lab

(Head of the Department)

(Principal)

Room No: AB 106